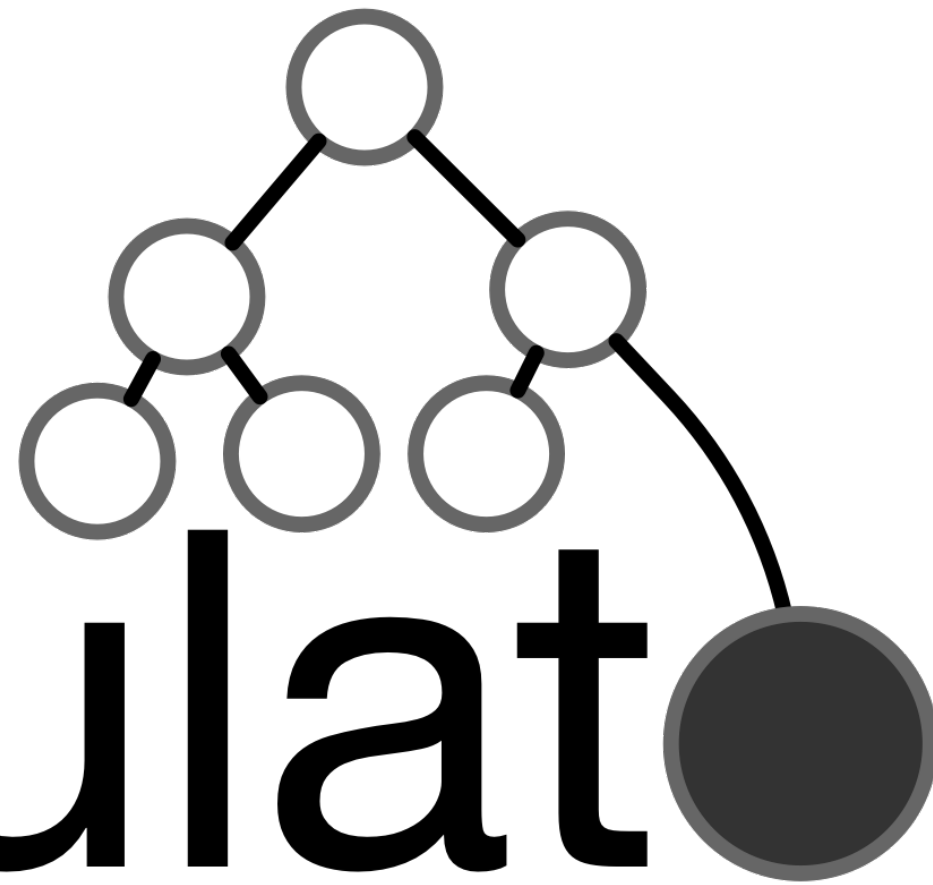


# Data Calculator

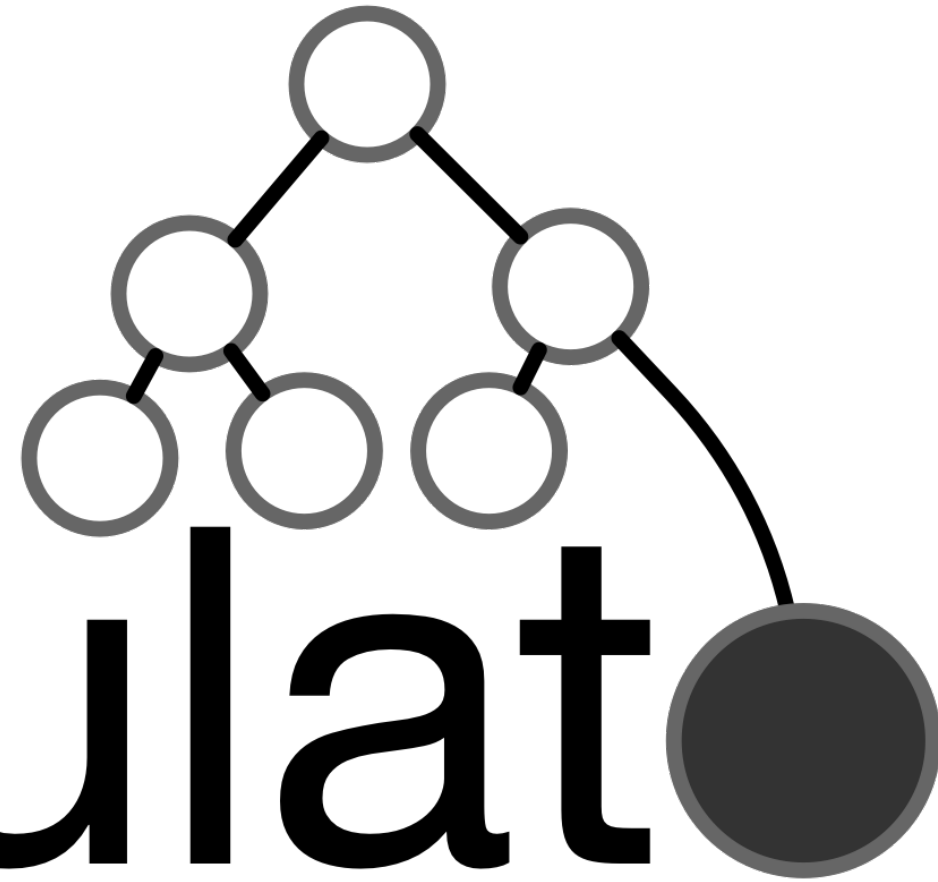


*Stratos Idreos & Data Systems Lab*

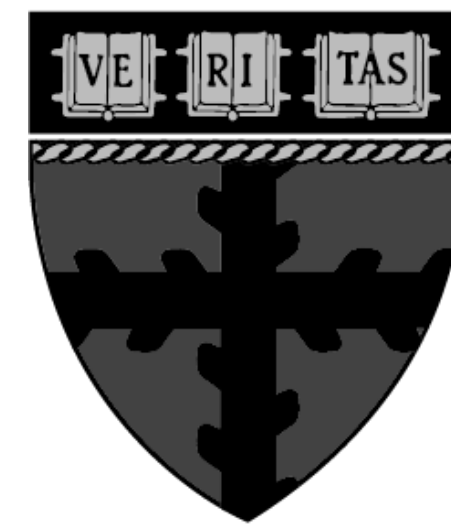


**DASlab**  
@ Harvard SEAS

# Data Calculator



*Stratos Idreos & Data Systems Lab*

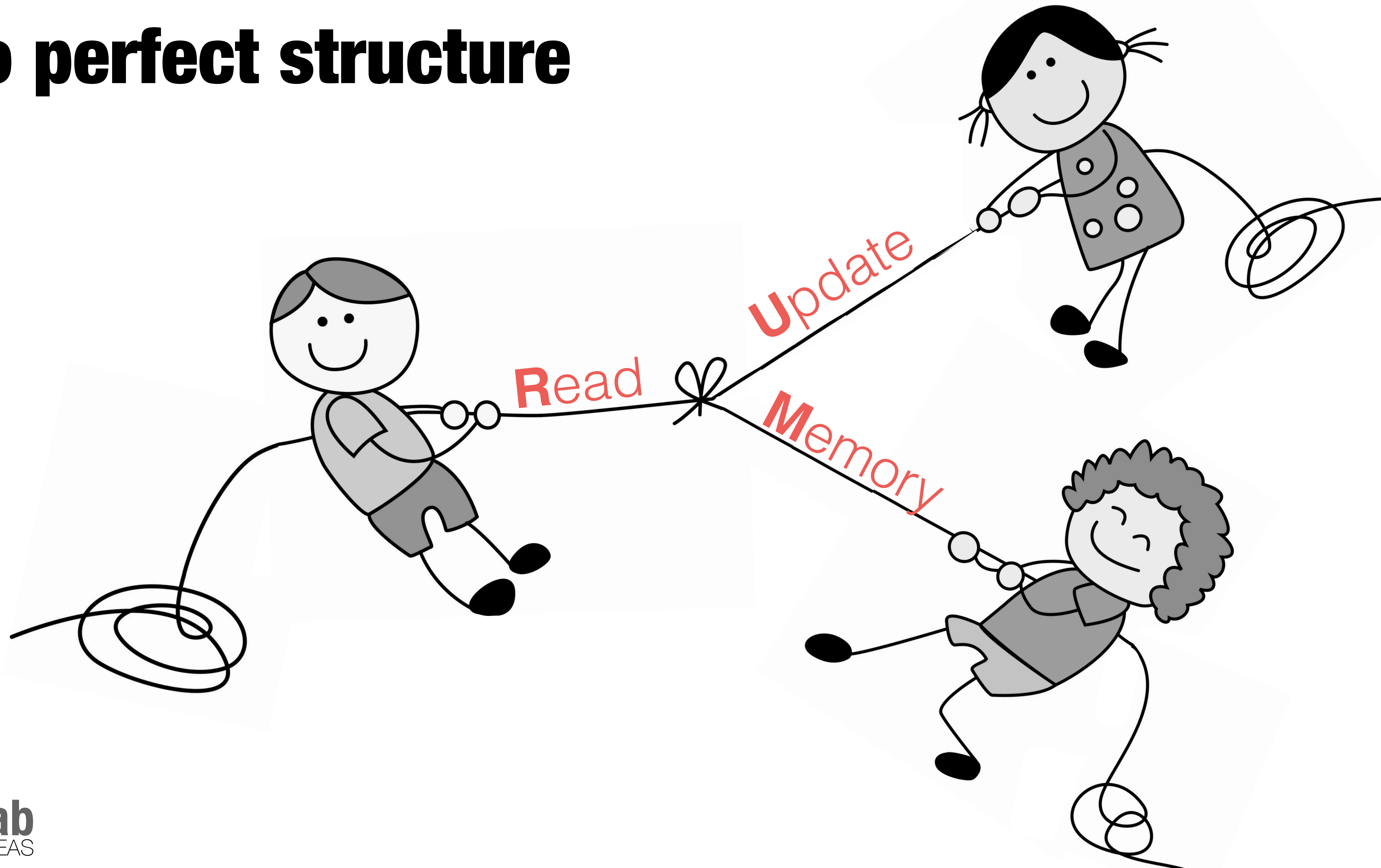


**DASlab**  
@ Harvard SEAS

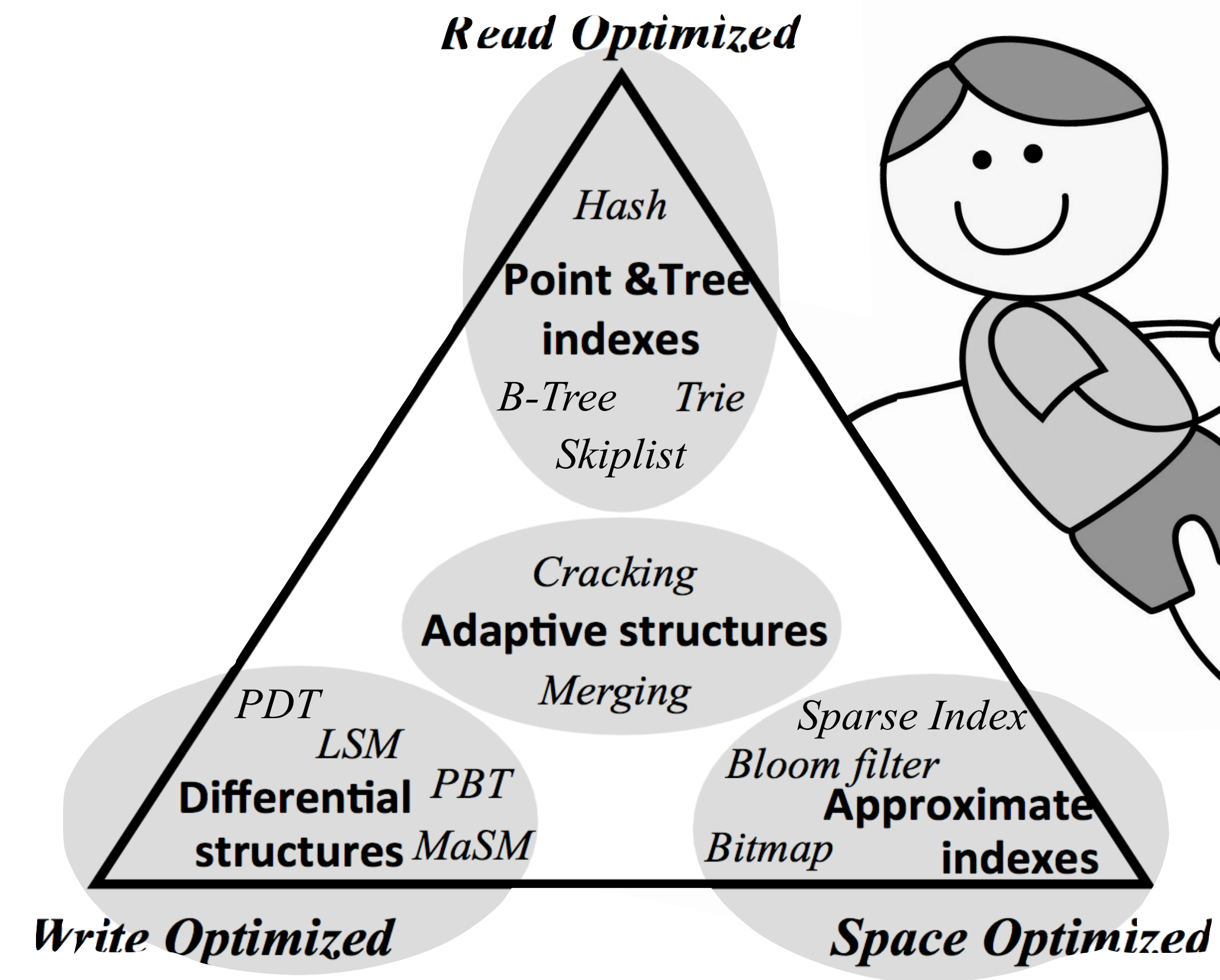
WHAT IF WE COULD **REASON** ABOUT THE  
**DESIGN SPACE** OF DATA STRUCTURES?



# 1. no perfect structure



# 1. no perfect structure



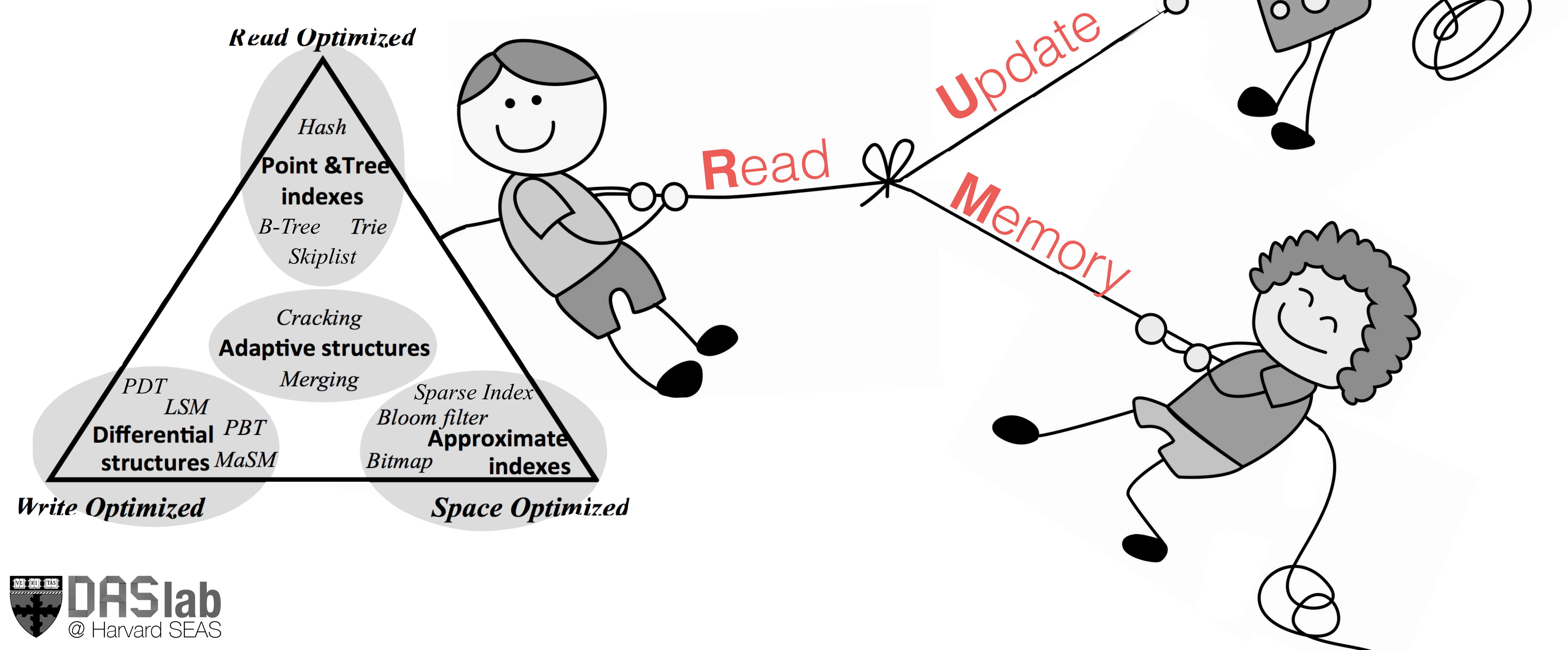
Read

Update

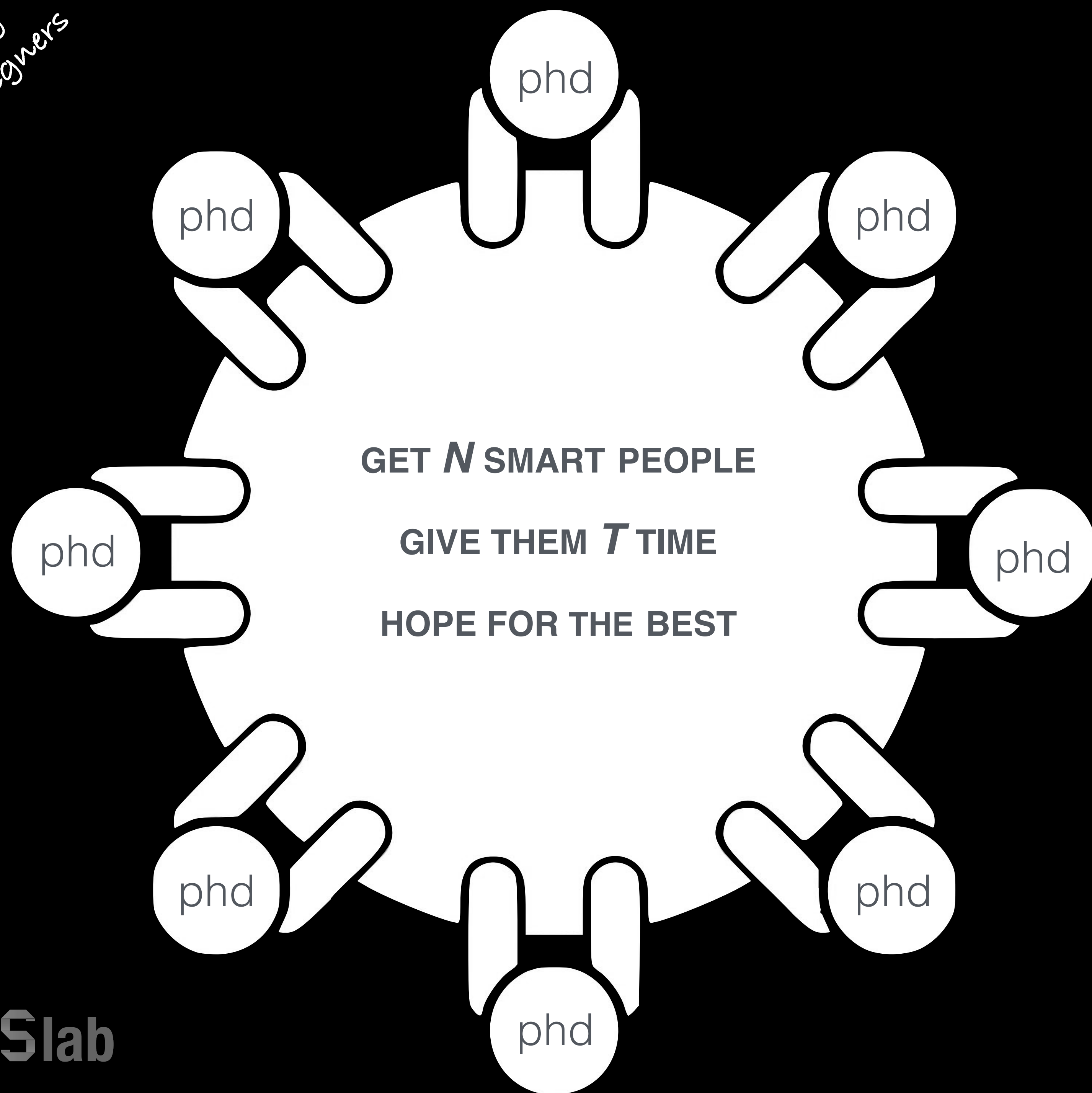
Memory



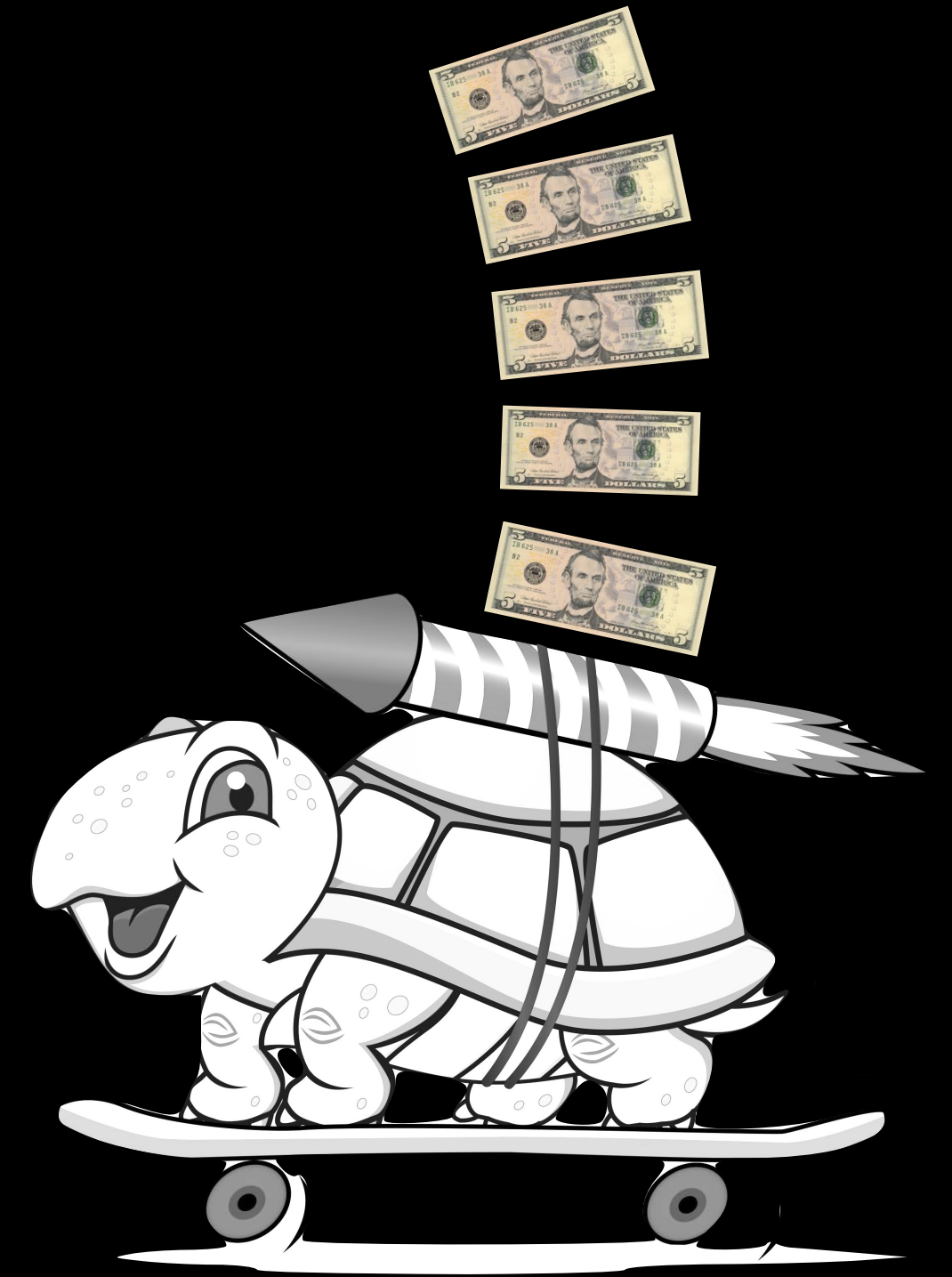
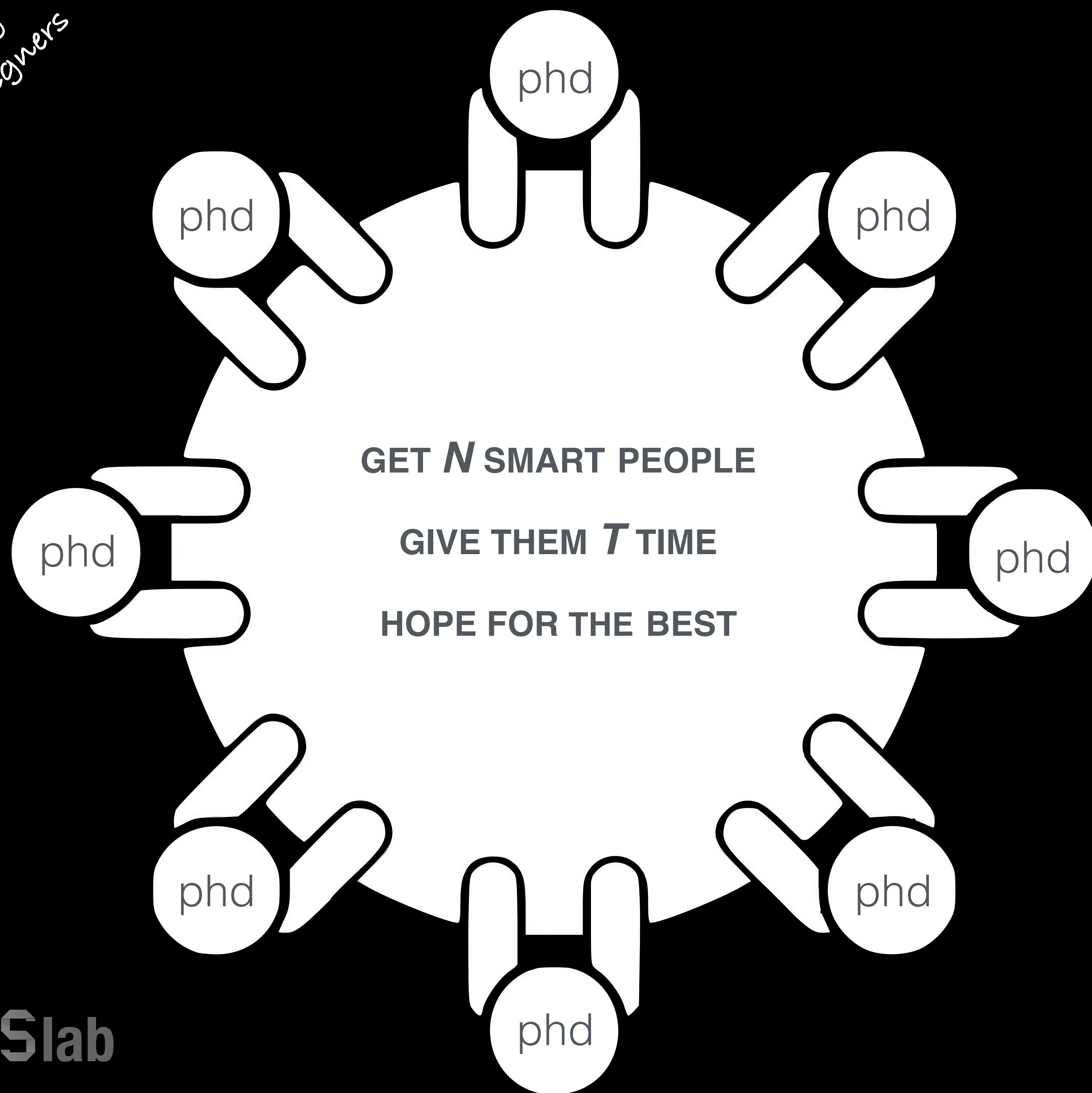
1. no perfect structure
2. apps & h/w evolve



the dining  
systems designers



the dinning  
systems designers



# AUTO DESIGN





ROBERT TARJAN

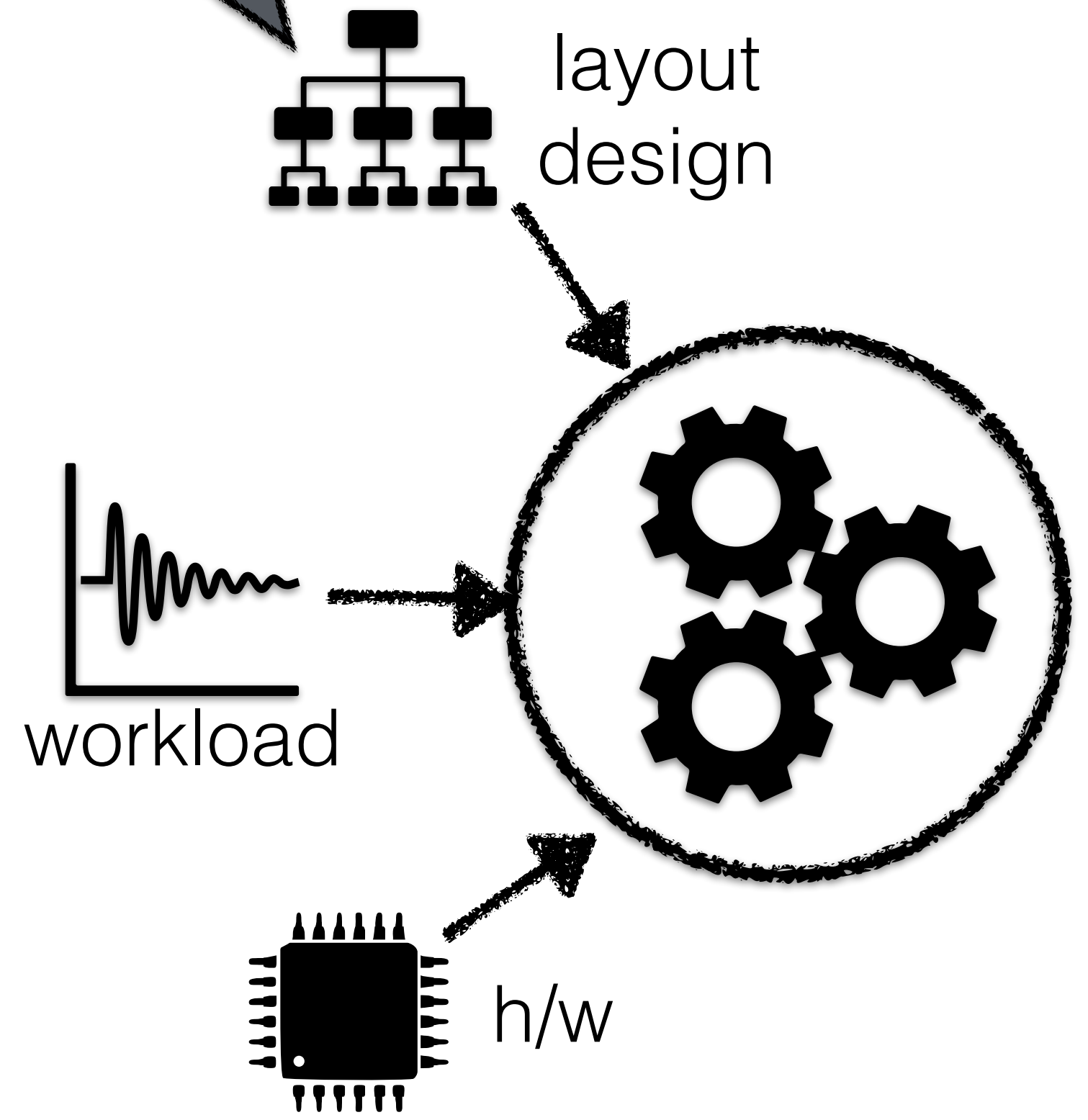
**“IS THERE A CALCULUS OF DATA STRUCTURES**  
by which one can choose the appropriate representation  
and techniques for a given problem?” (SIAM, 1978)

**HOW MANY AND WHICH?**

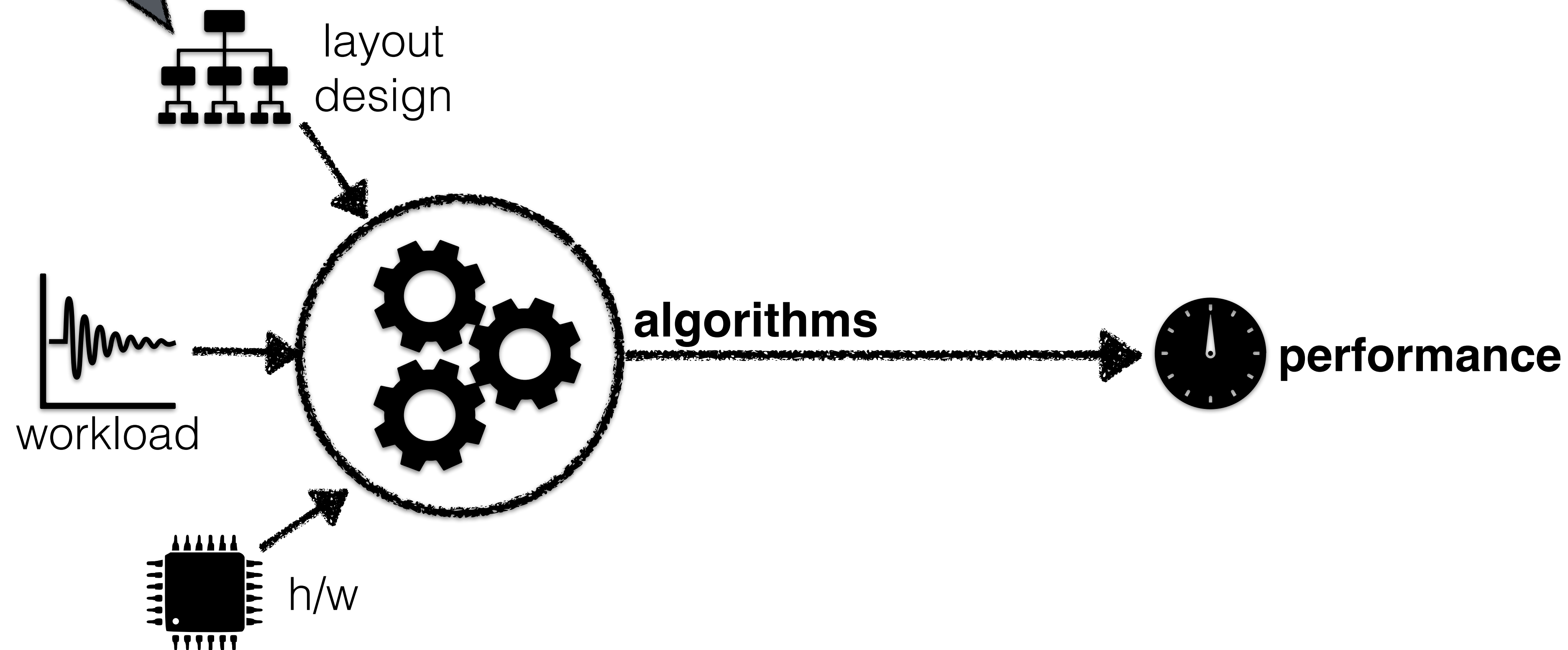
**COMPUTE PERFORMANCE?**



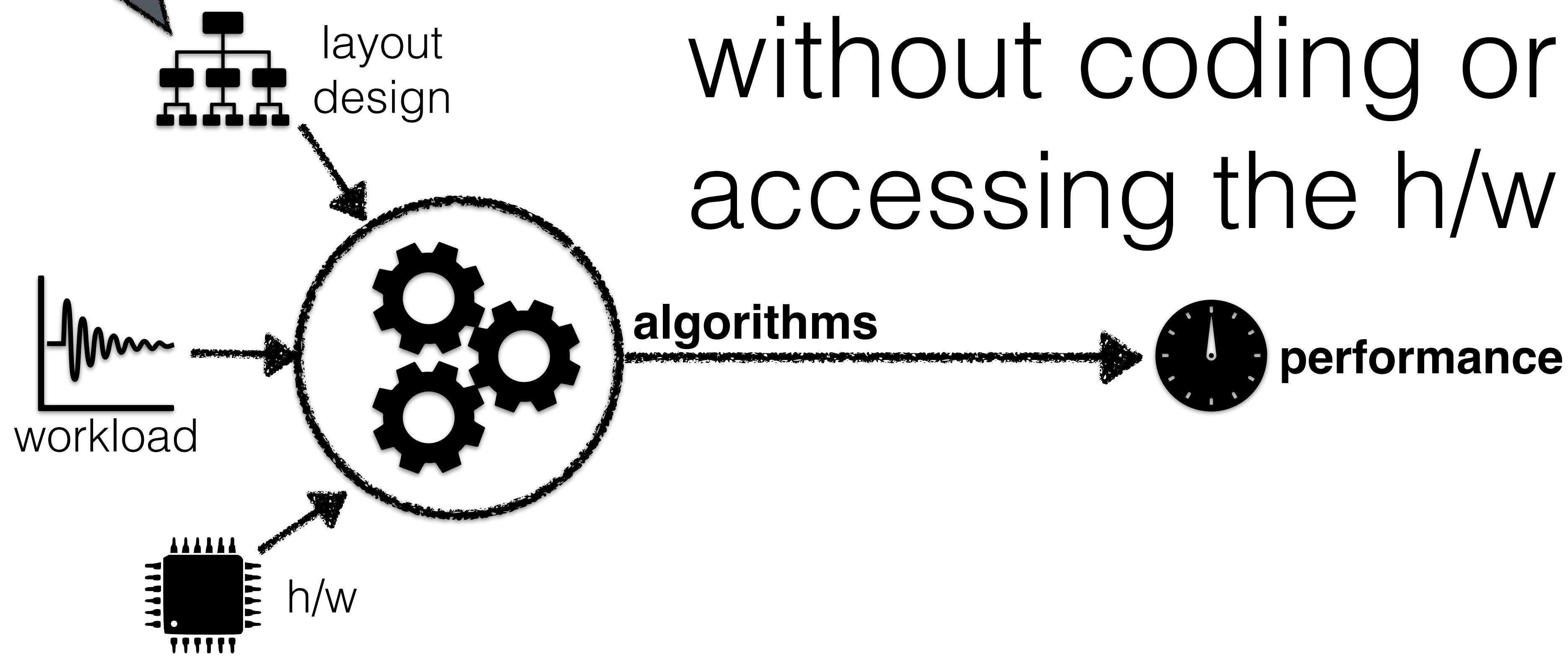
DESIGN SPACE  
OF POSSIBLE  
STORAGE LAYOUTS



DESIGN SPACE  
OF POSSIBLE  
STORAGE LAYOUTS



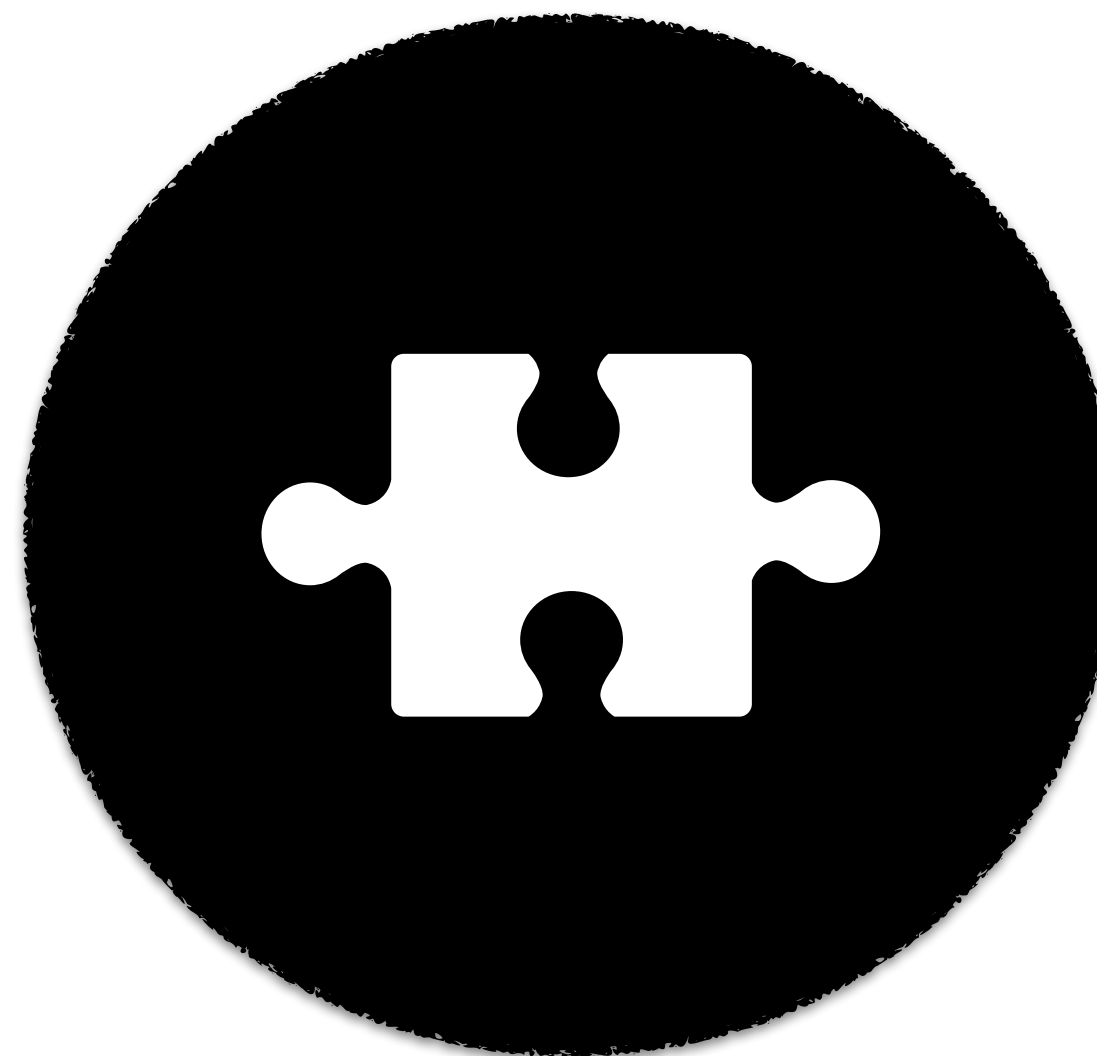
DESIGN SPACE  
OF POSSIBLE  
STORAGE LAYOUTS



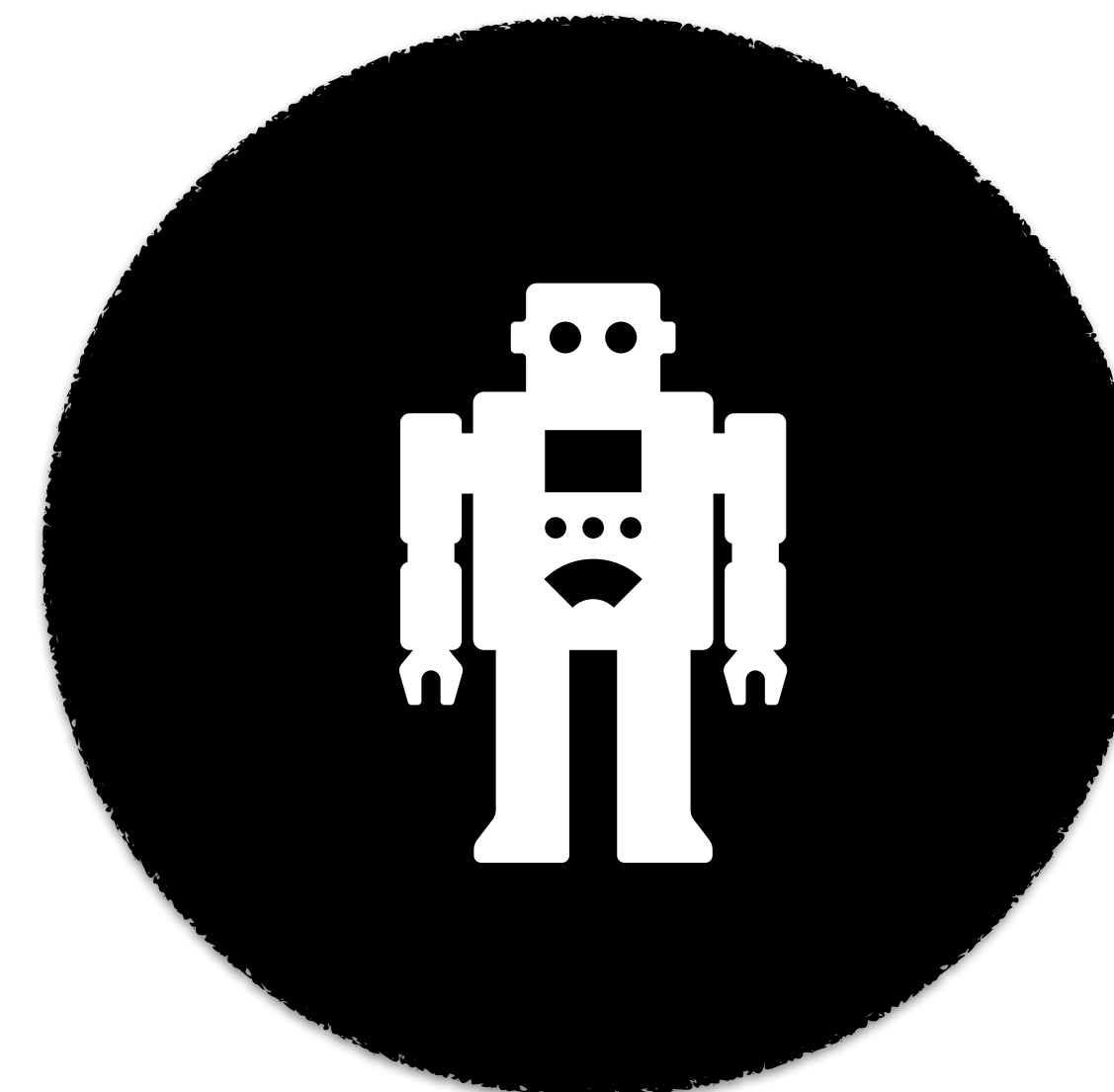
without coding or  
accessing the h/w



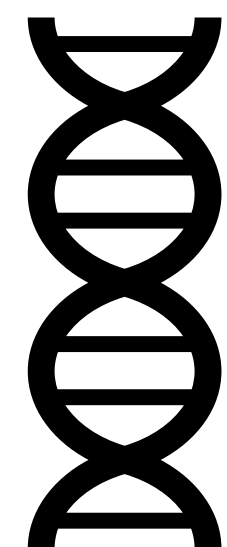
WHAT-IF DESIGN



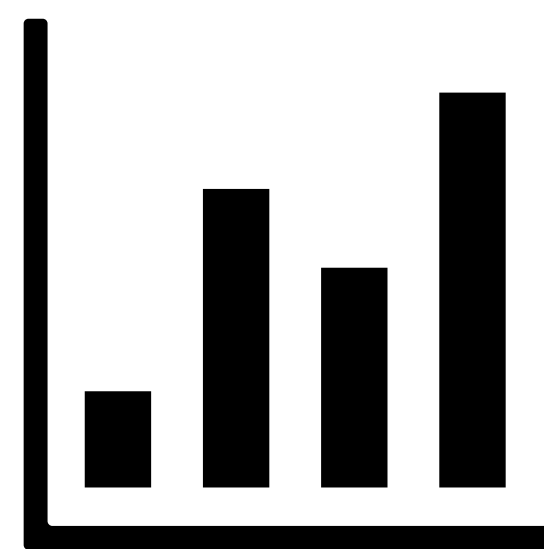
AUTO-DESIGN



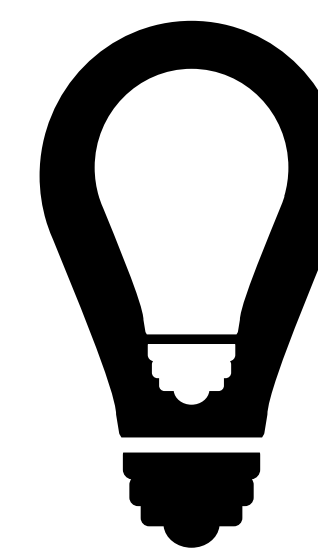
SELF-DESIGNING SYSTEMS



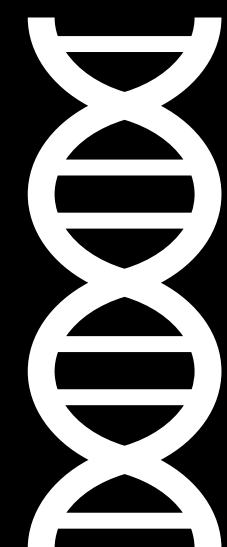
DESIGN SPACE



COST SYNTHESIS



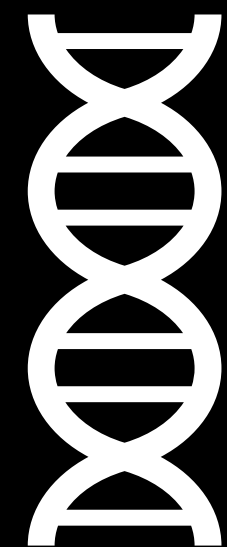
HOW TO USE



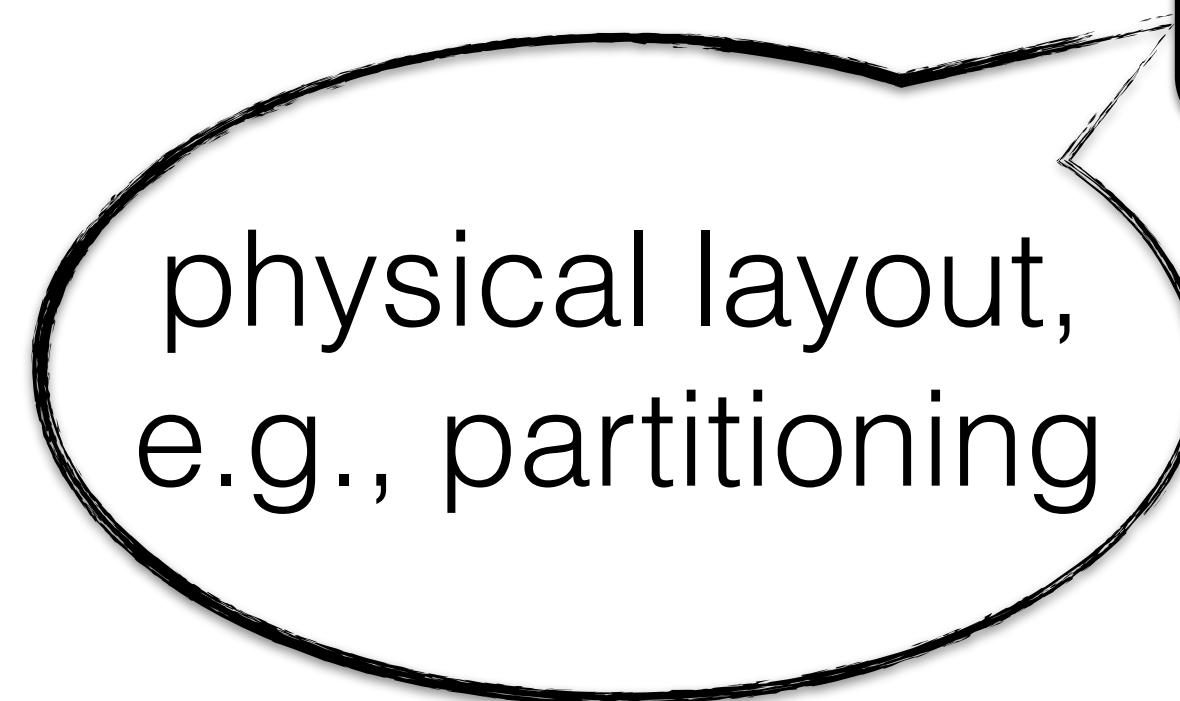
DESIGN SPACE

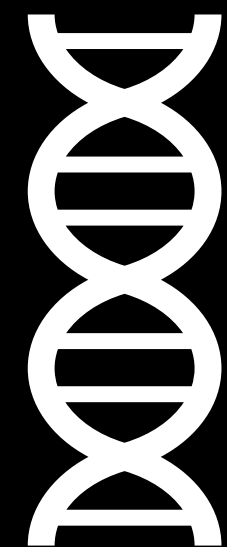
INDEX

DATA

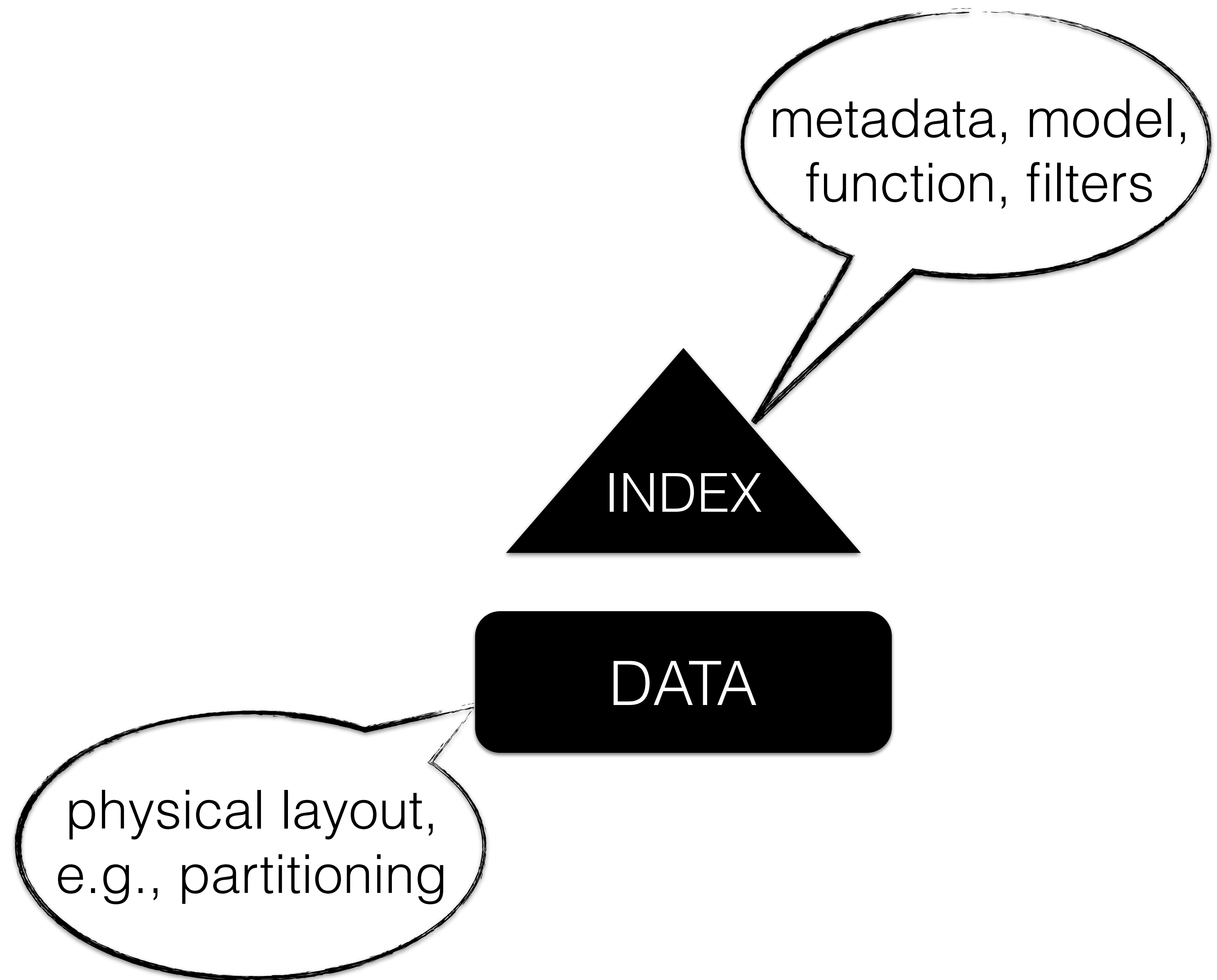


DESIGN SPACE

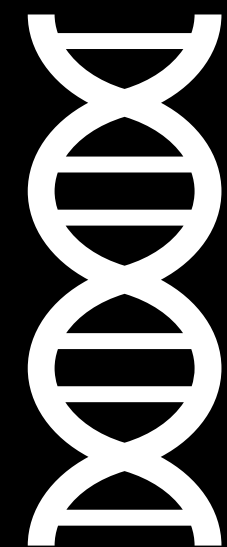




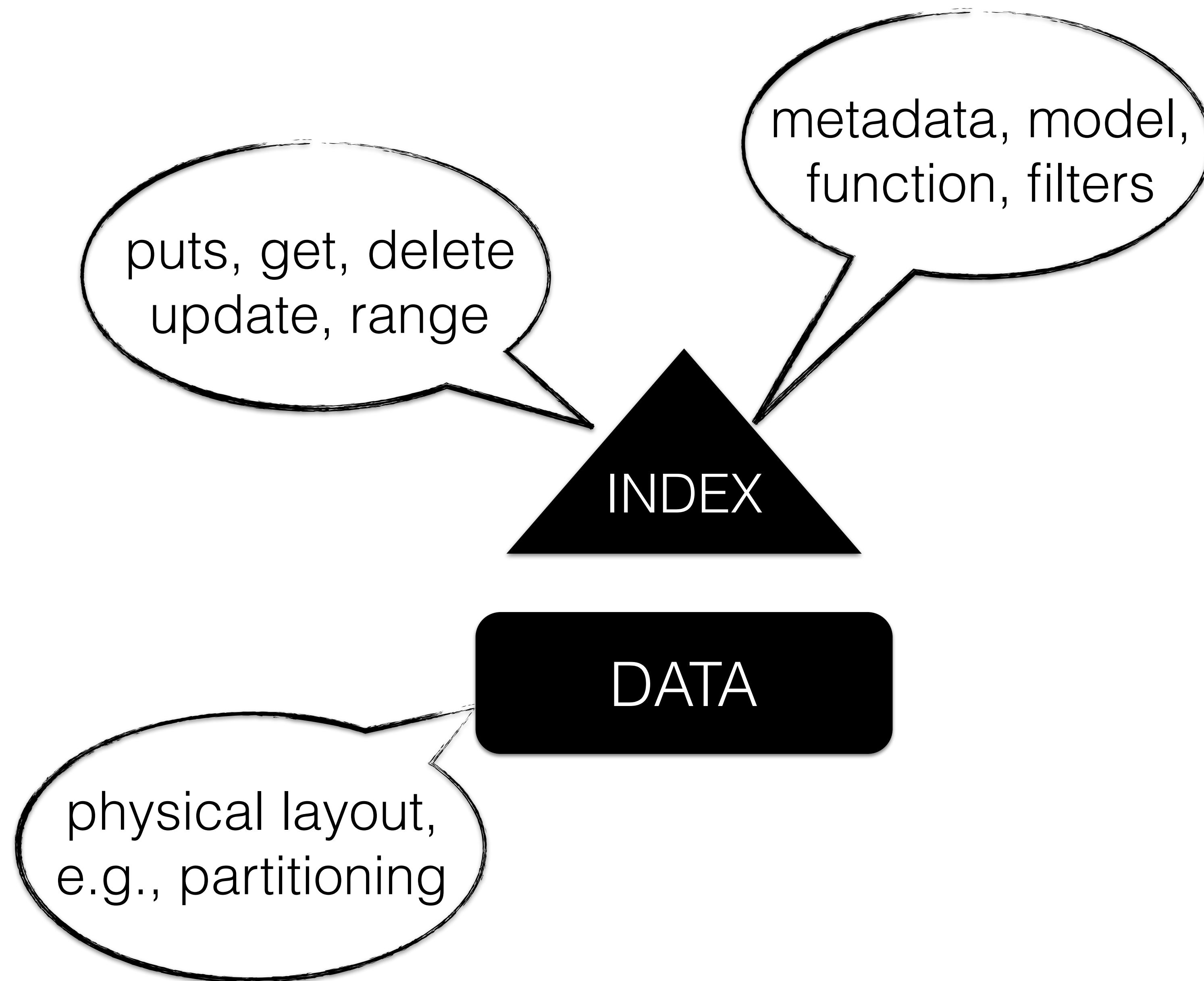
DESIGN SPACE

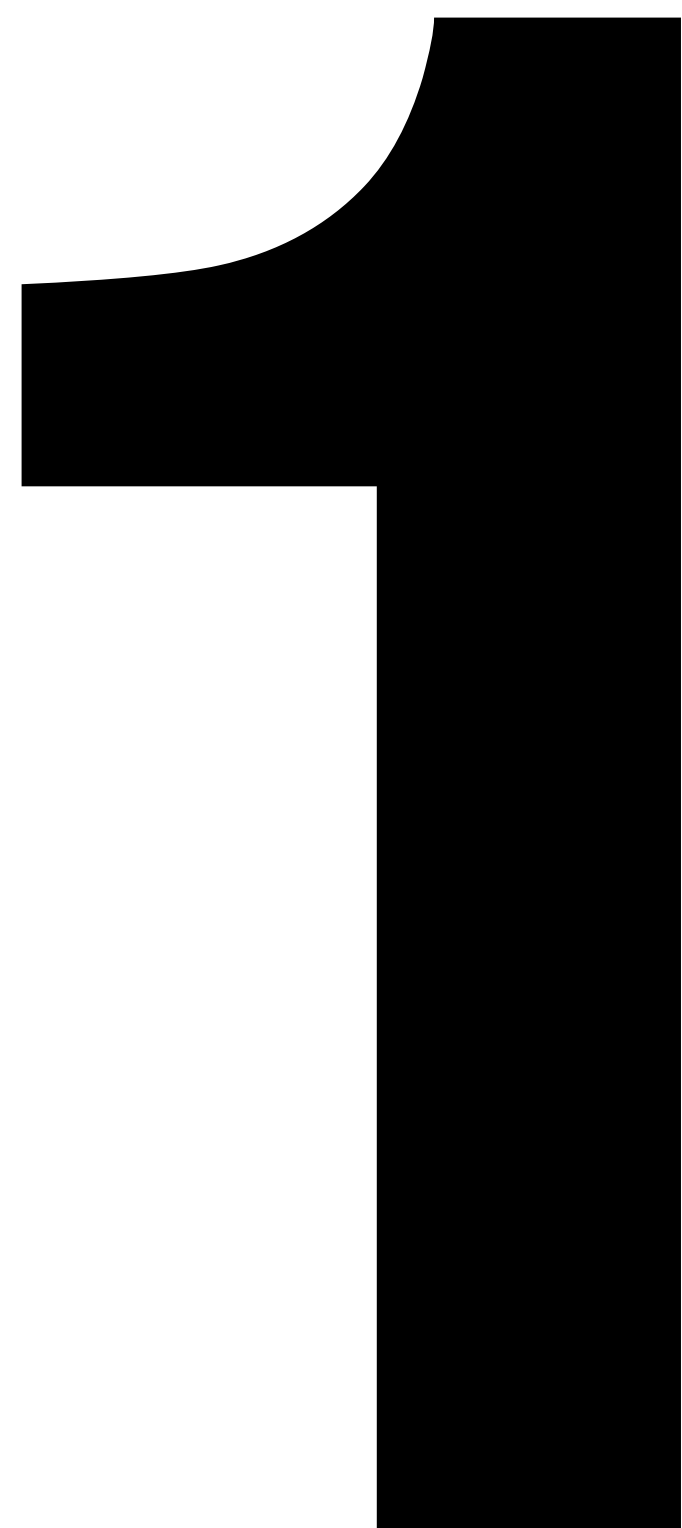






DESIGN SPACE



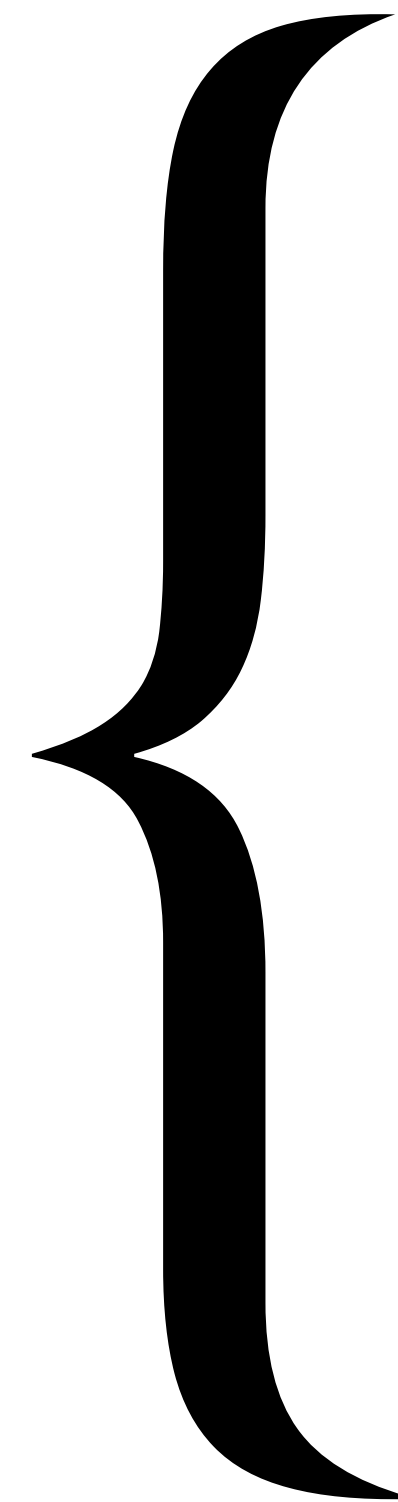


EACH  
DESIGN:  
A **SET** OF  
CONCEPTS

{partitioning, links, fence pointers,...}

2

DESIGN



**COMBINATION**

of existing concepts

---

**TUNING**

of existing concepts

---

**NEW**

concept

# 3

(ALMOST) ALL  
DESIGNS ARE A  
COMBINATION/TUNING  
OF **EXISTING CONCEPTS**



*I hope for nothing.  
I fear nothing.  
I am free.*

Nikos Kazantzakis

if we know the **fundamental** building blocks,

if we know the **fundamental** building blocks,  
how they combine and their properties,

if we know the **fundamental** building blocks,  
how they combine and their properties,

then we can **automate** the discovery of  
novel combinations and tunings

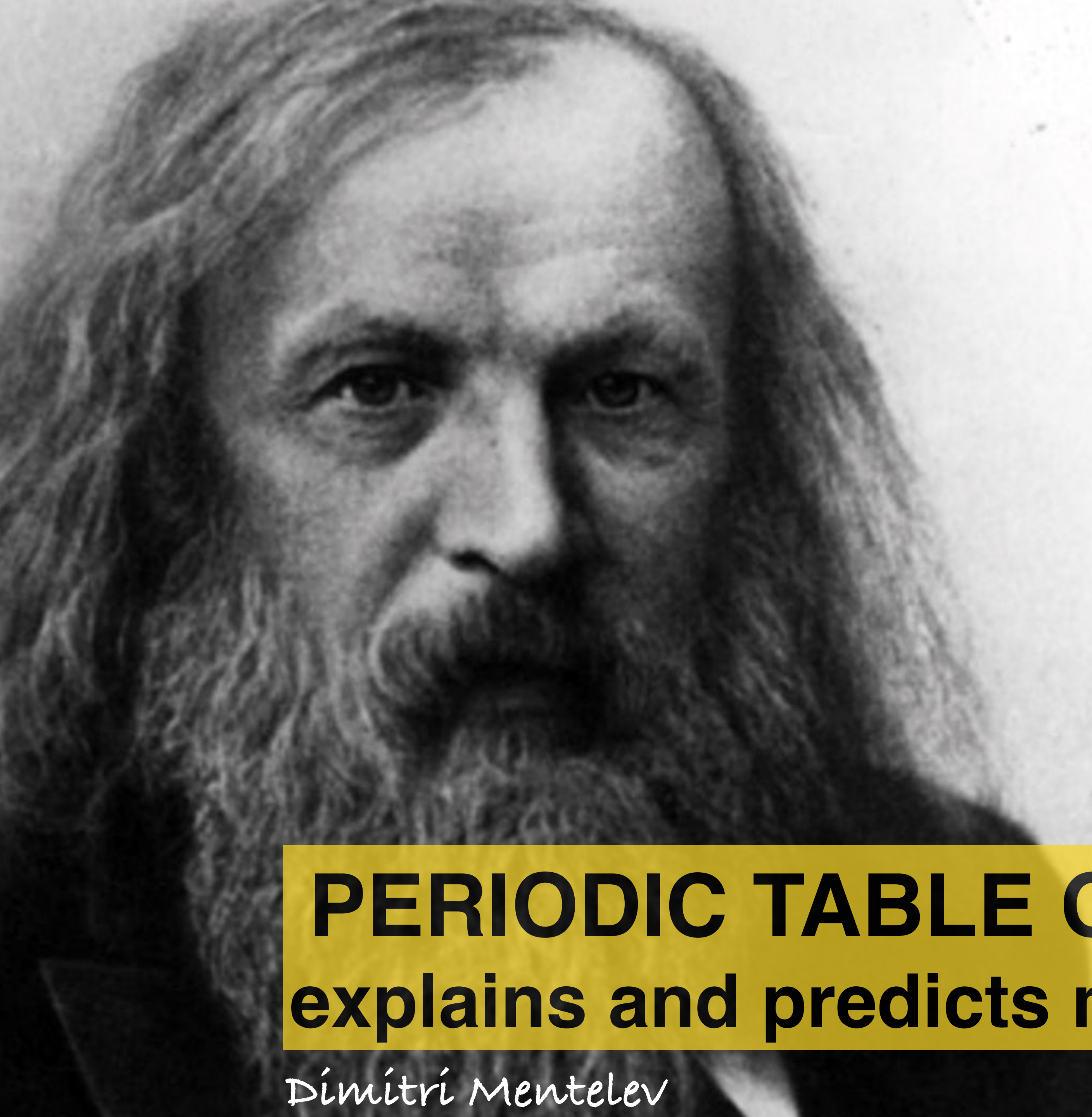


## DESIGN SPACE

if we know the **fundamental** building blocks,  
how they combine and their properties,

then we can **automate** the discovery of  
novel combinations and tunings

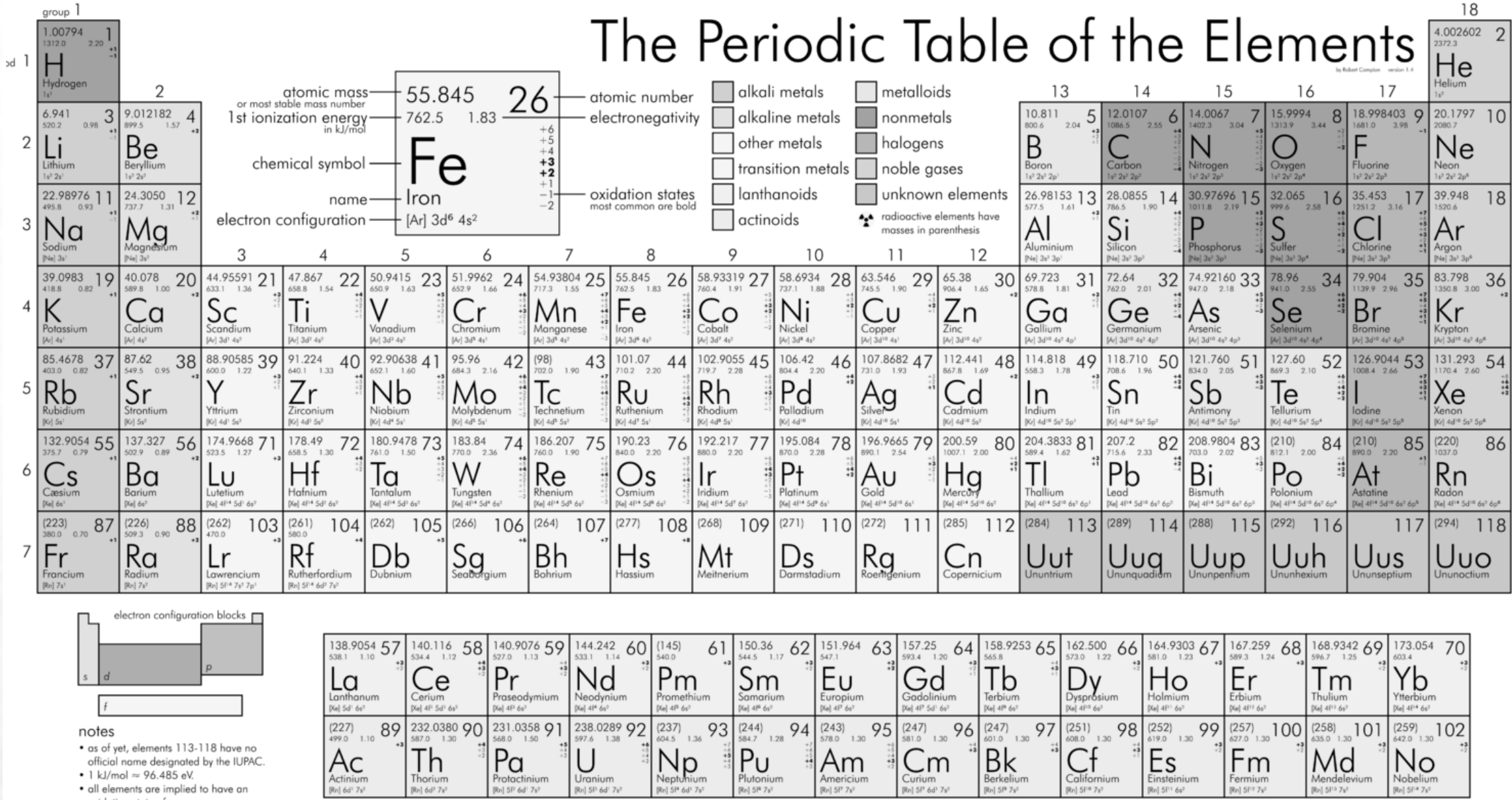
NAVIGATION



# PERIODIC TABLE OF ELEMENTS

## explains and predicts missing elements

Dimitrí Mendeleev



**structures elements** based on atomic number, electron configuration, and recurring chemical properties





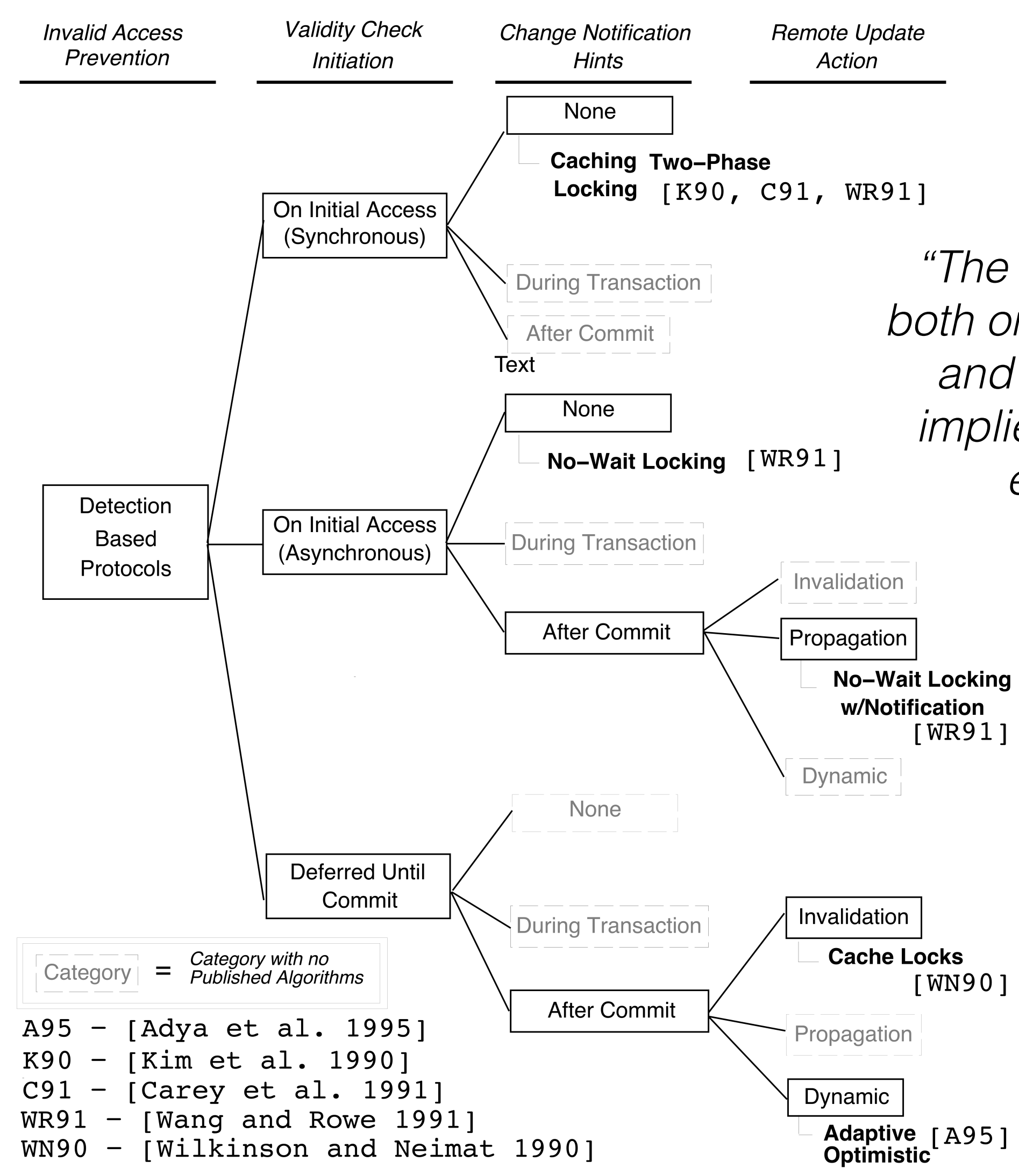




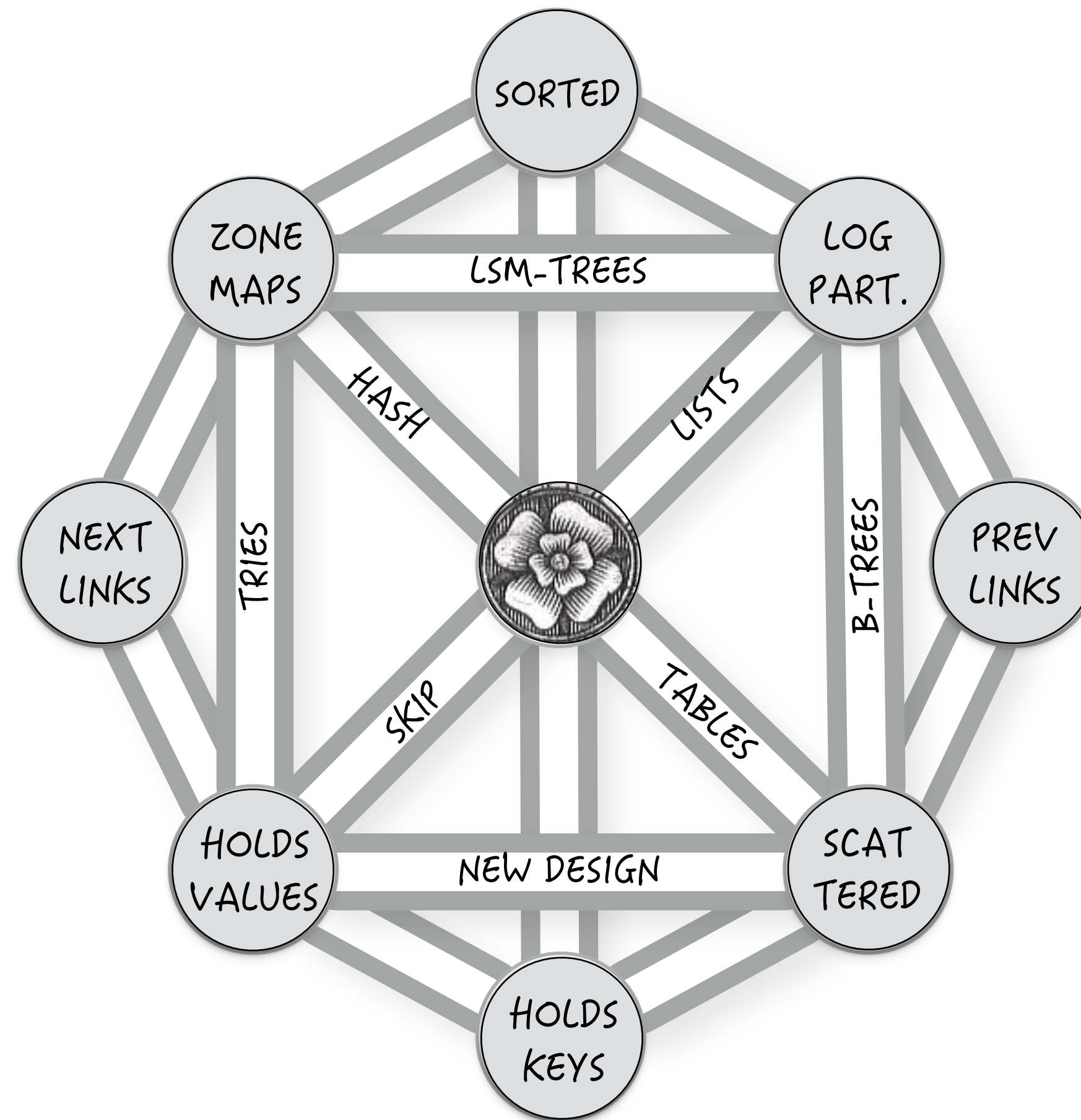
# TAXONOMY OF COMPLEX ALGORITHMS

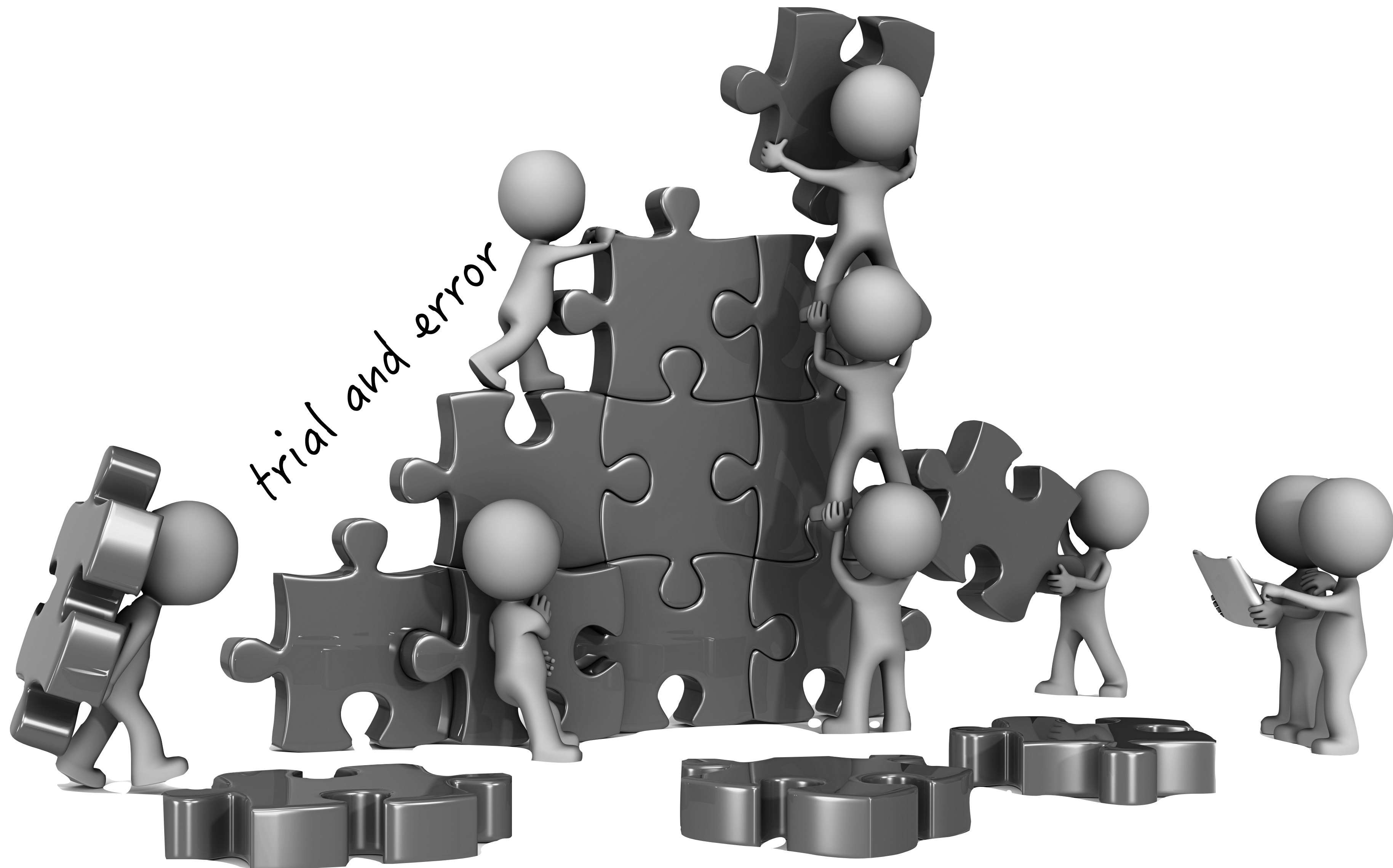
## transactional cache consistency maintenance

Mike Franklin



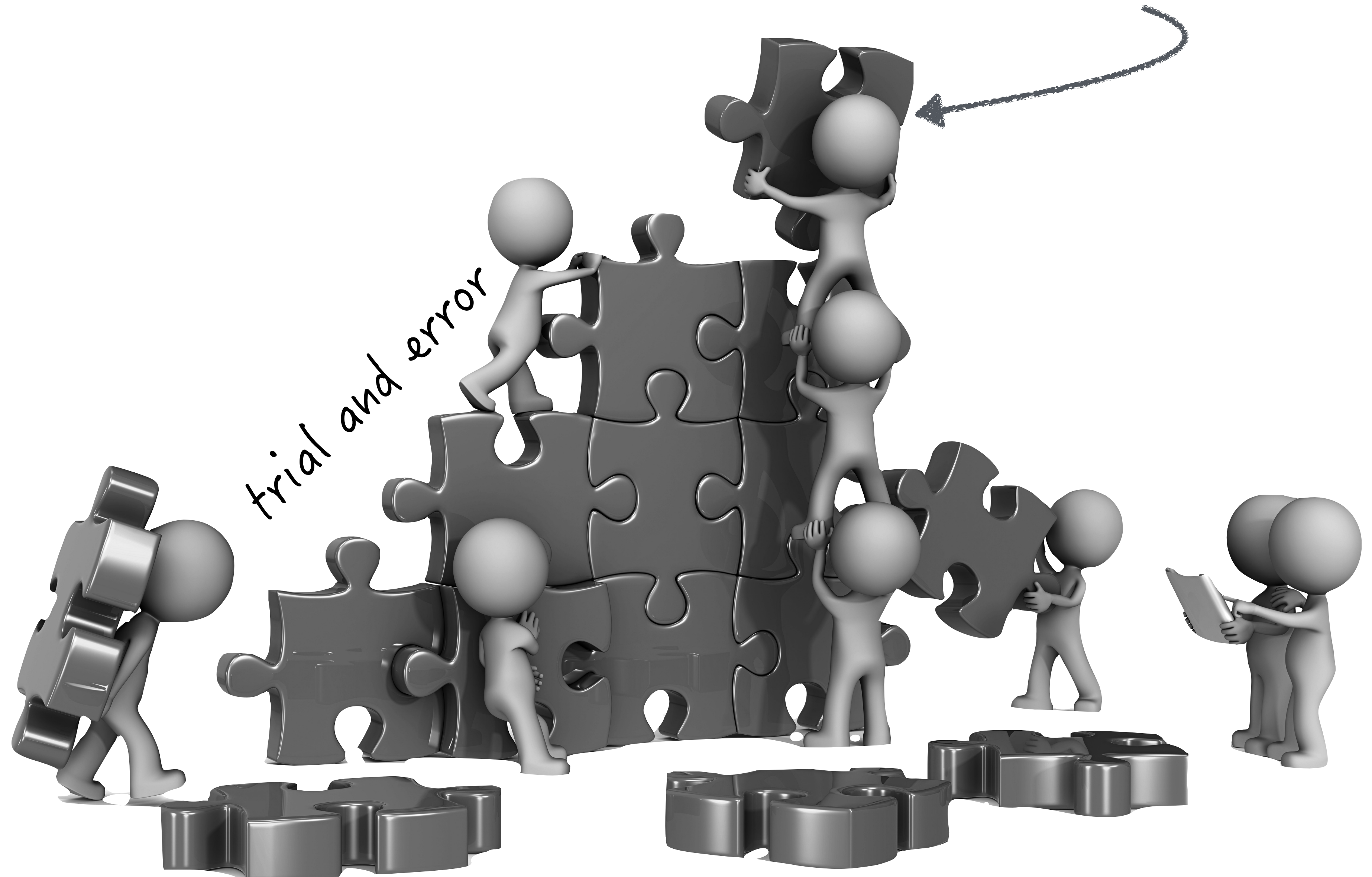
*“The taxonomy is used to shed light both on the nature of the design space and on the performance tradeoffs implied by many of the choices that exist in the design space.”*







**FIRST PRINCIPLE:** DESIGN CONCEPT THAT IS NOT POSSIBLE OR MEANINGFUL TO BREAK FURTHER



# MAP LAYOUT FIRST

- 1 KNOWN DESIGNS
- 2 OPEN QUESTIONS





**{arrays, logs, lsm-trees, b-trees}, filters, bitmaps, compression, stats**

e.g., 1000x NoSQL k-v: bloom filter bits, merging policy

e.g., access path selection: scans vs b-tree depends on concurrency

e.g., robust scans with value by value lossy compression

e.g., updatable bitmap indexes

e.g., fast statistics/ML

...

## EXAMPLE: The design space of **NoSQL Key-value Stores**



MEMORY  
DISK

**insert (key-value)**

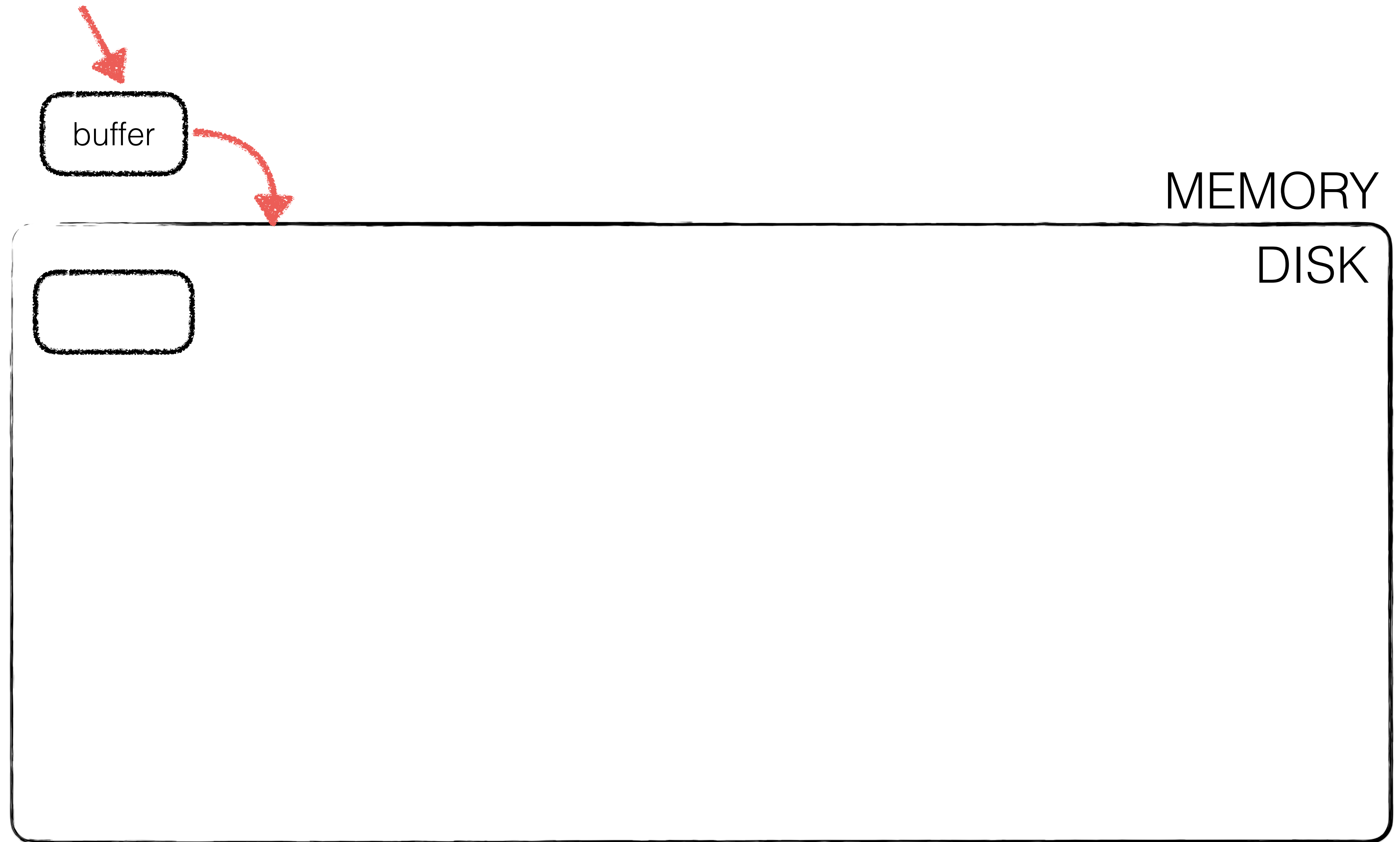


buffer

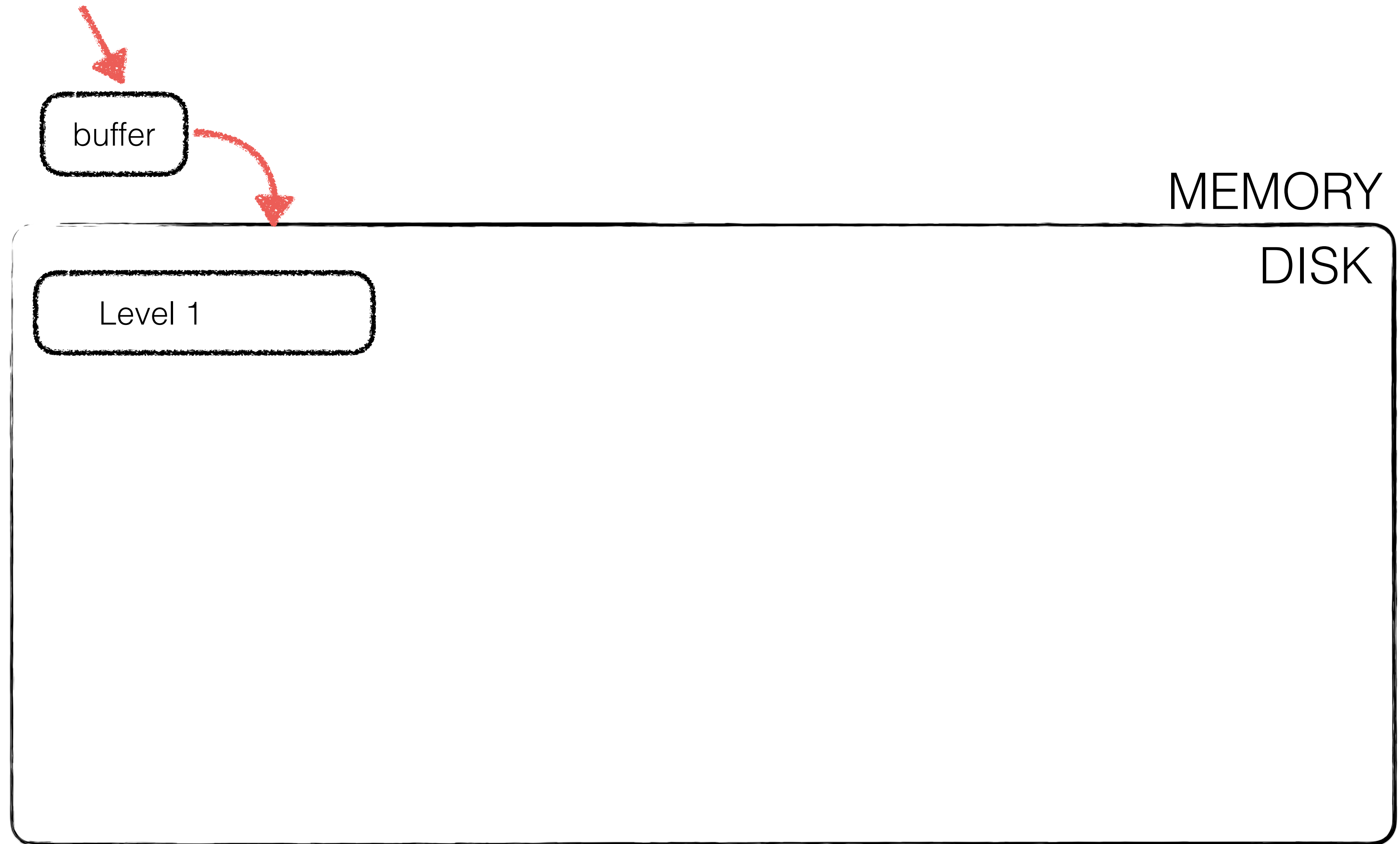
MEMORY

DISK

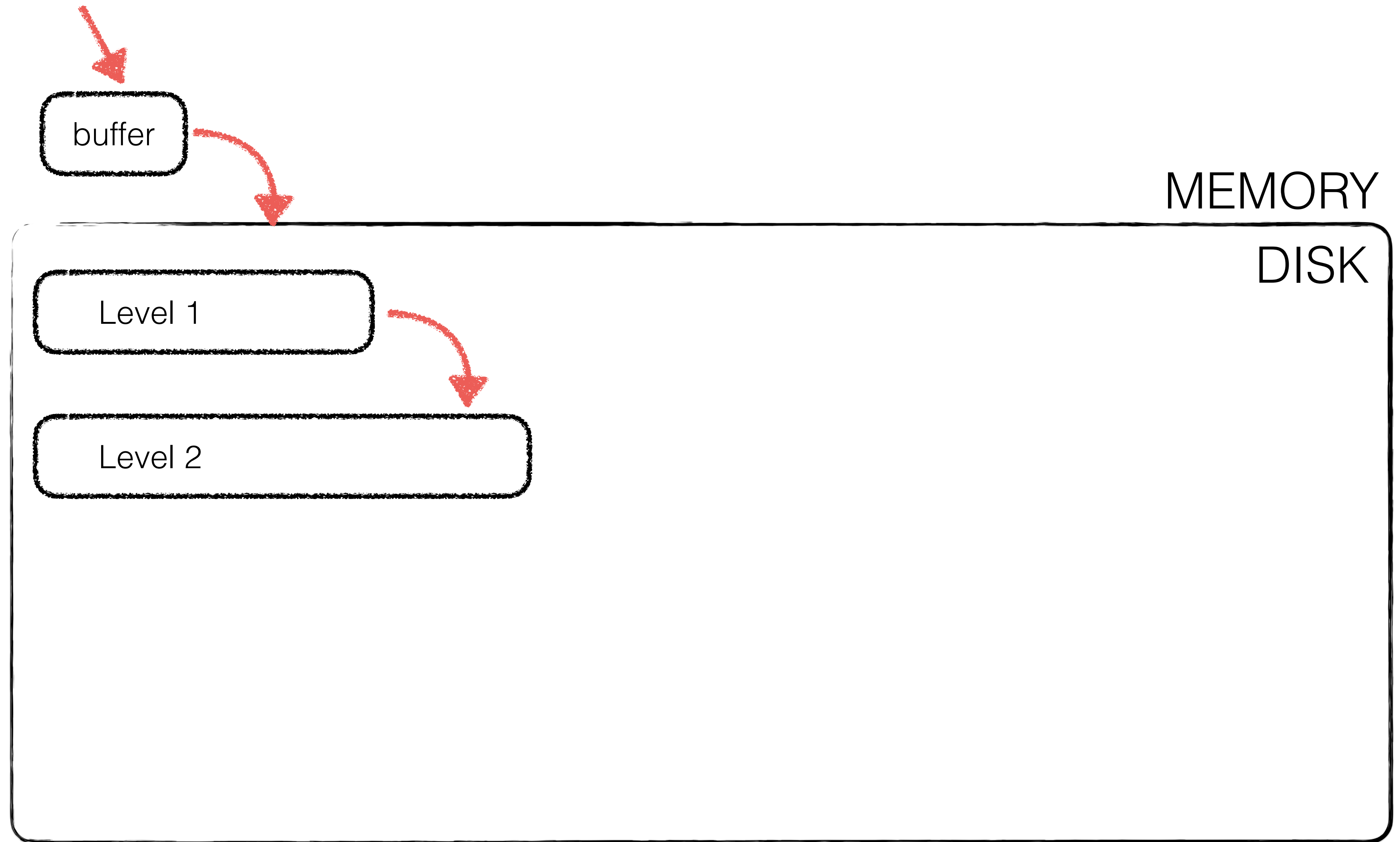
**insert (key-value)**



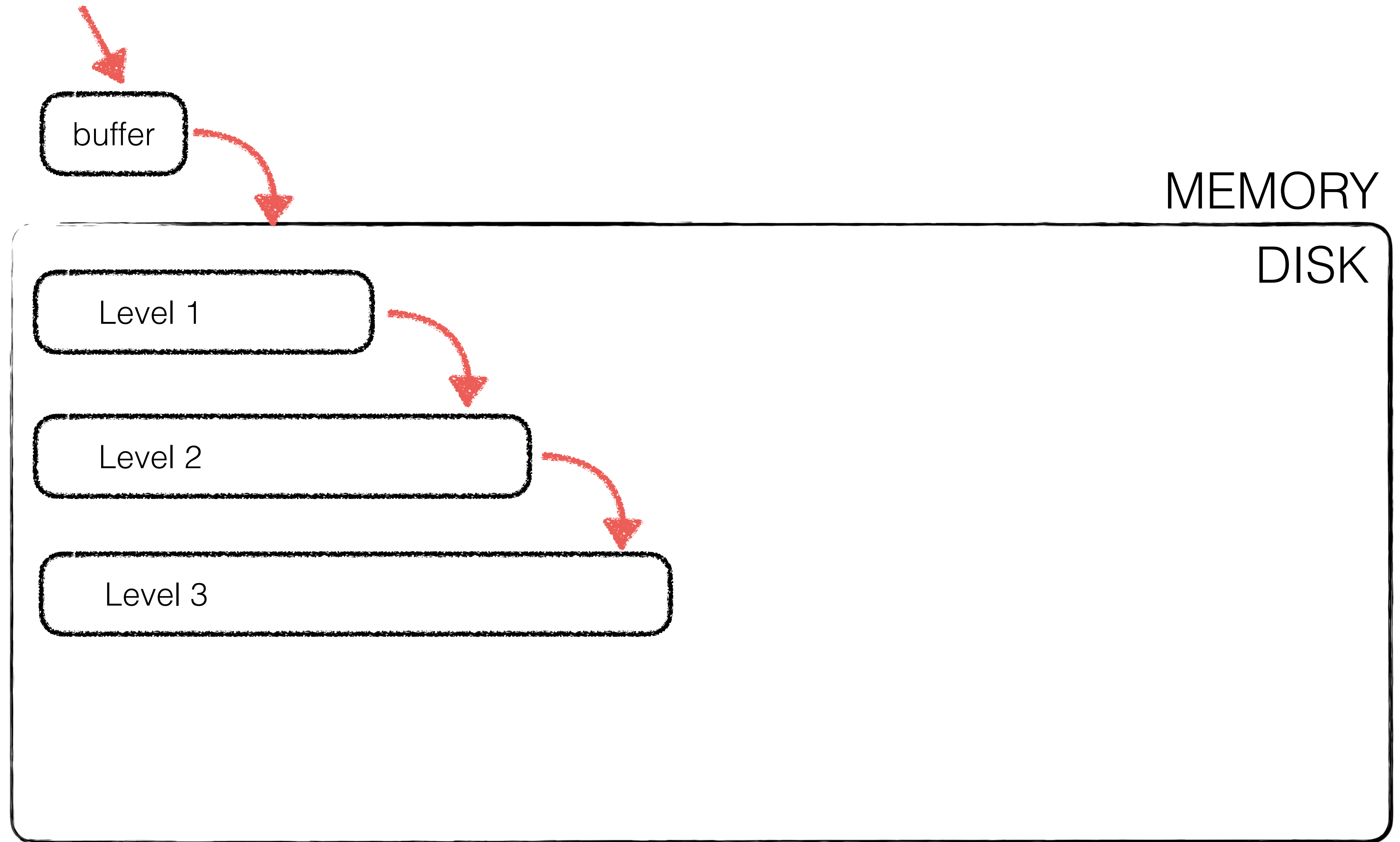
**insert (key-value)**



**insert (key-value)**

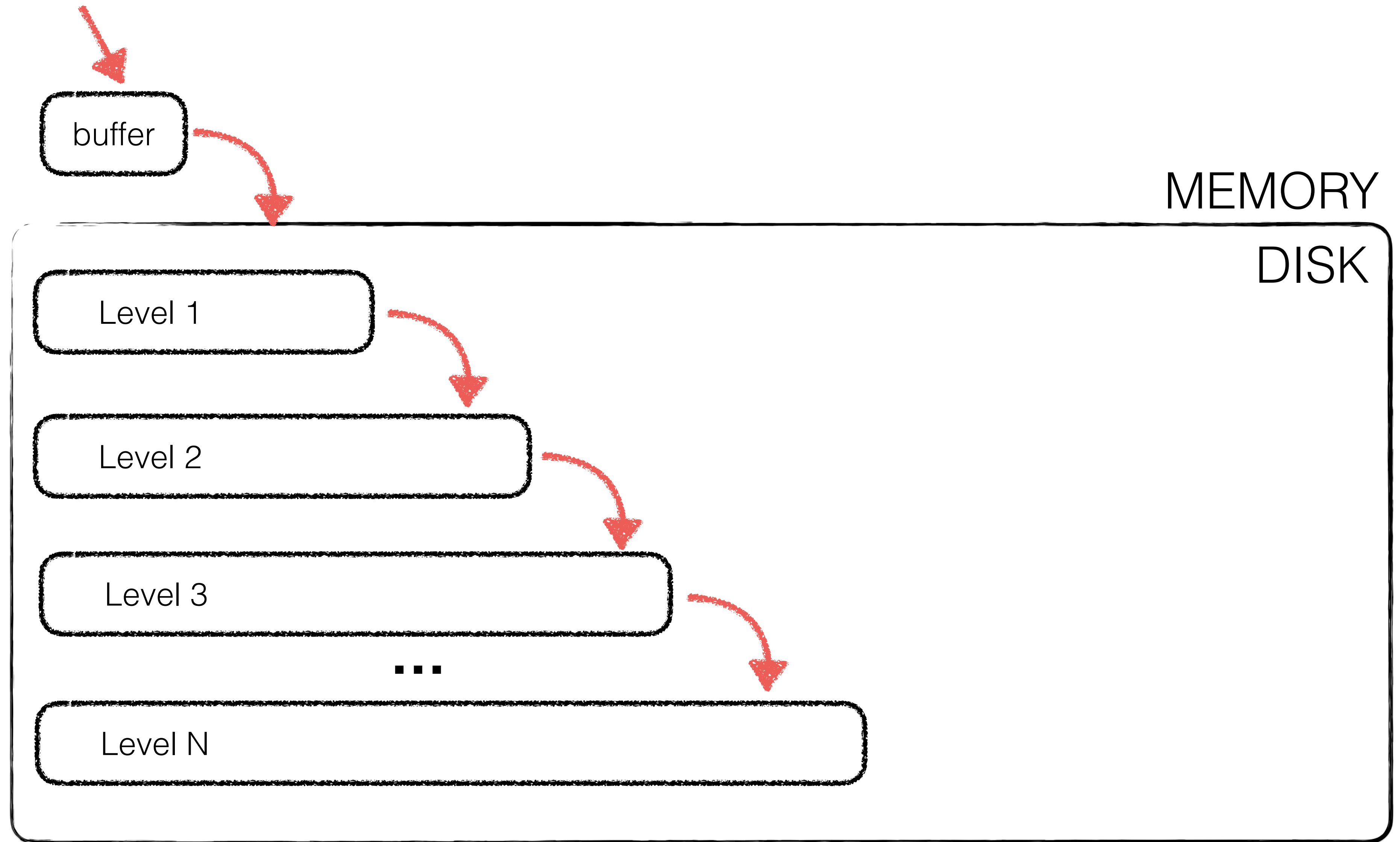


**insert (key-value)**

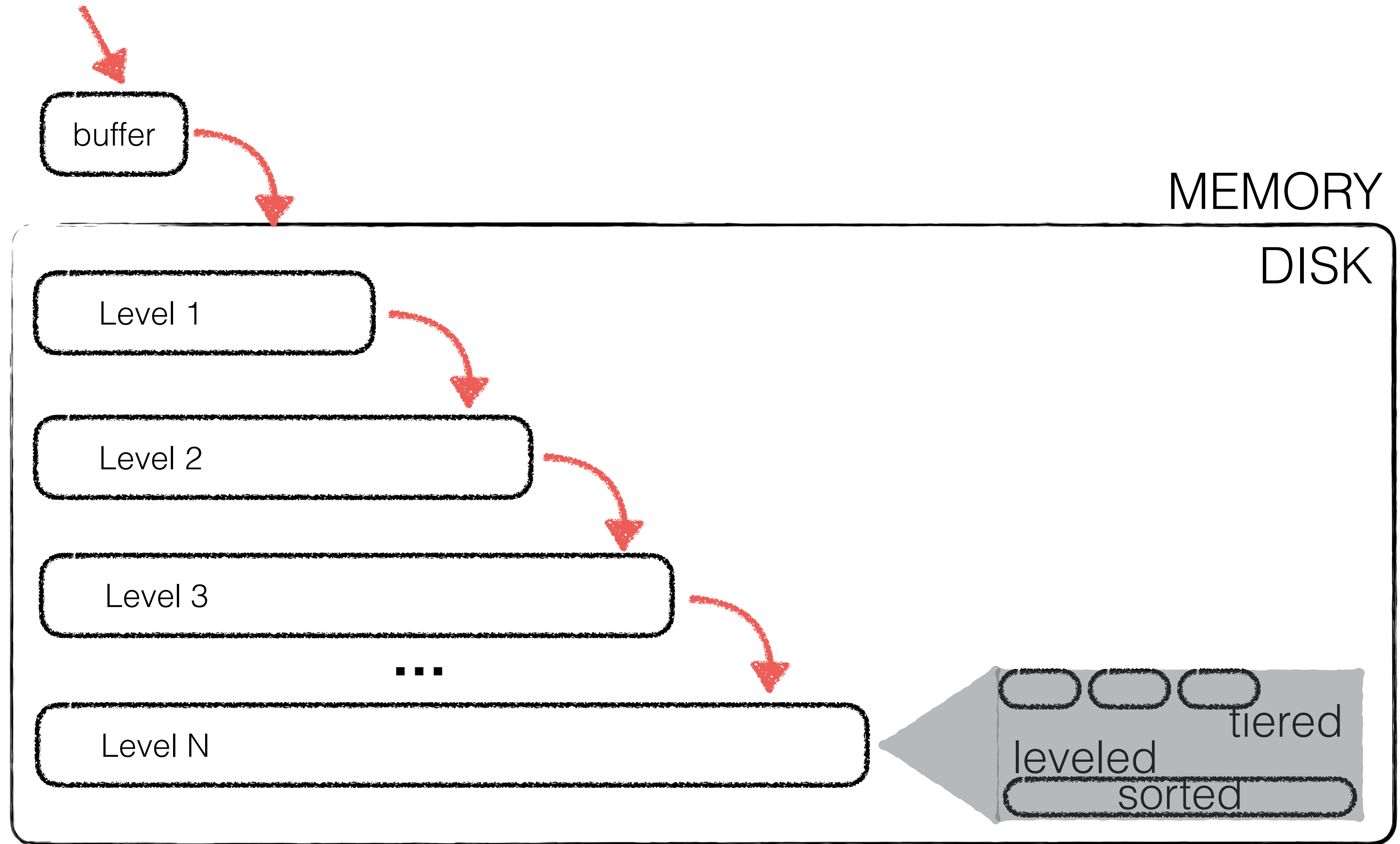




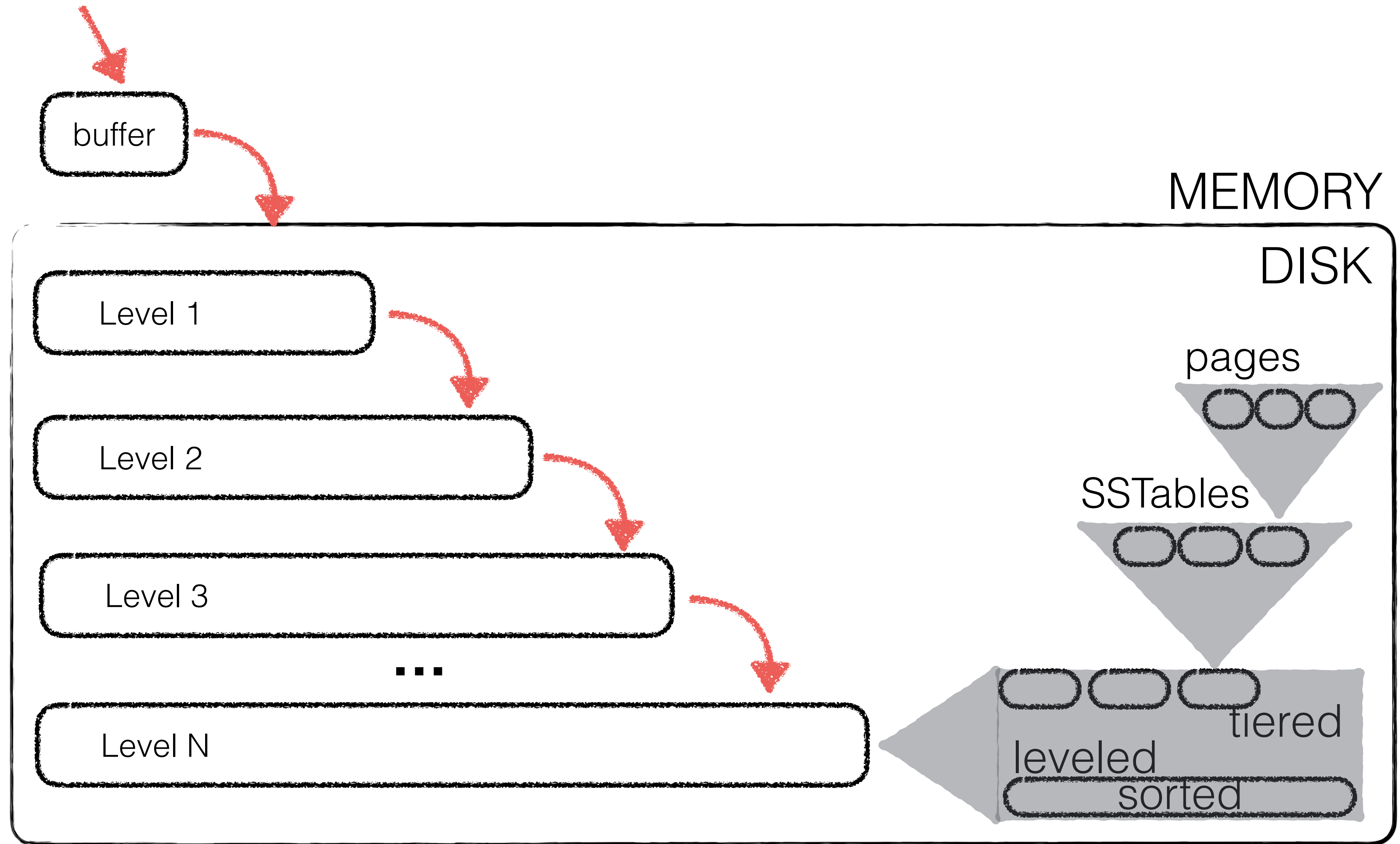
**insert (key-value)**



insert (key-value)



insert (key-value)



buffer

Level 1

Level 2

Level 3

...

Level N

MEMORY  
DISK

pages

SSTables

tiered  
leveled  
sorted

[1,0,0,1,1,1]  
hash fun.

bloom  
filters



...



buffer

Level 1

Level 2

Level 3

...

Level N

MEMORY  
DISK

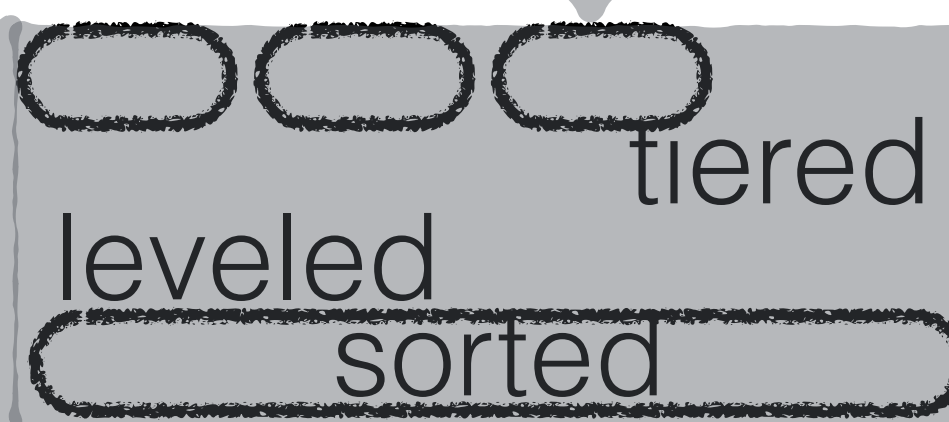
pages



SSTables



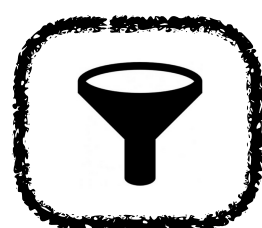
tiered





[1,0,0,1,1,1]  
hash fun.

bloom  
filters

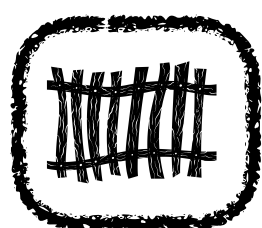
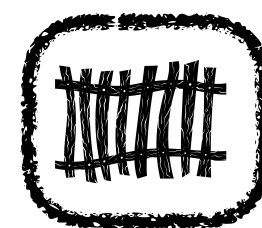
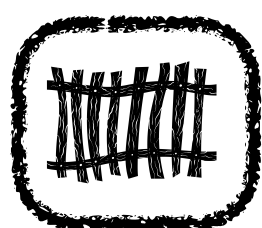


...

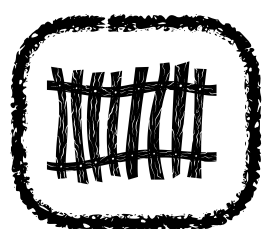


[min-max]

fence  
pointers



...



buffer

Level 1

Level 2

Level 3

...

Level N

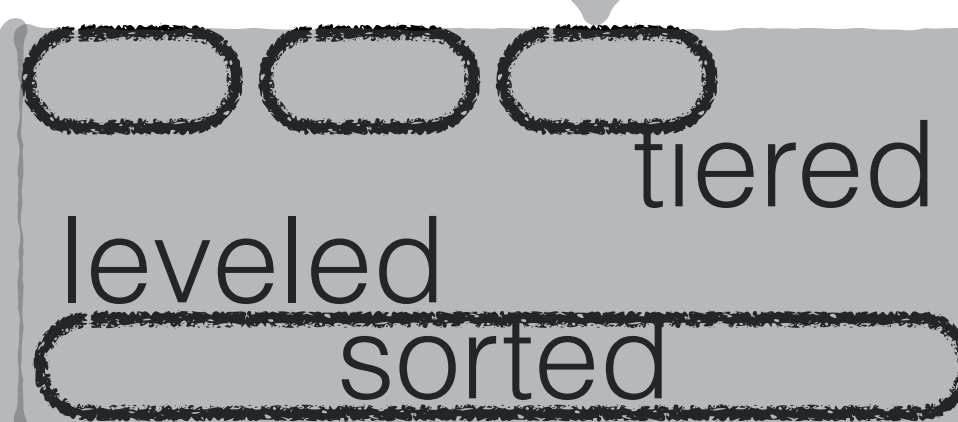
MEMORY

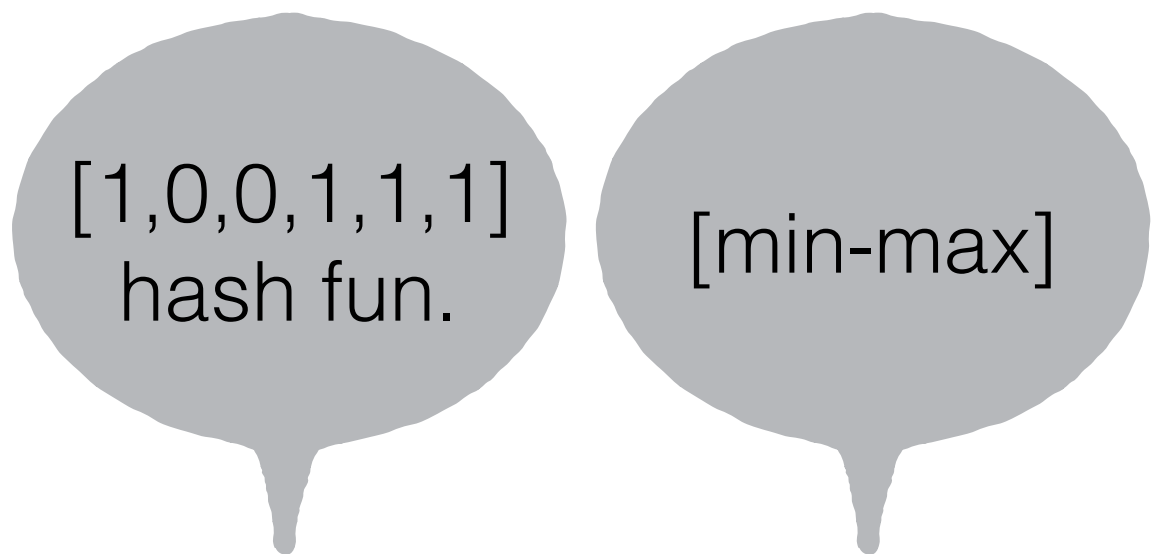
DISK

pages



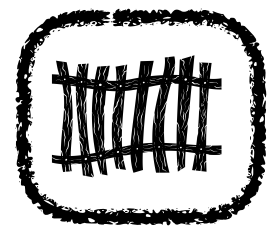
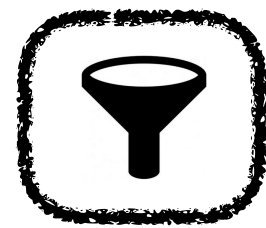
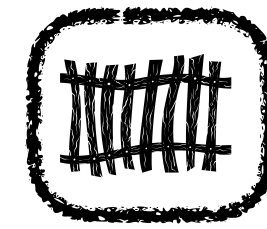
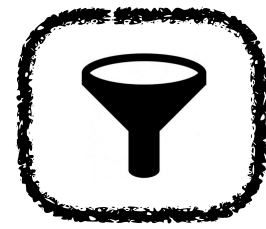
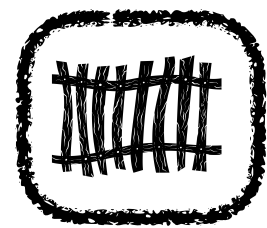
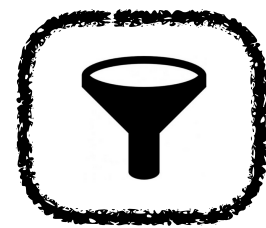
SSTables





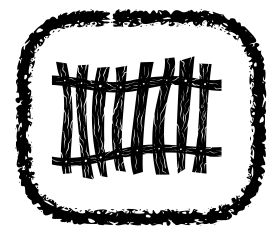
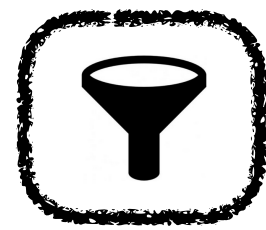
bloom  
filters

fence  
pointers



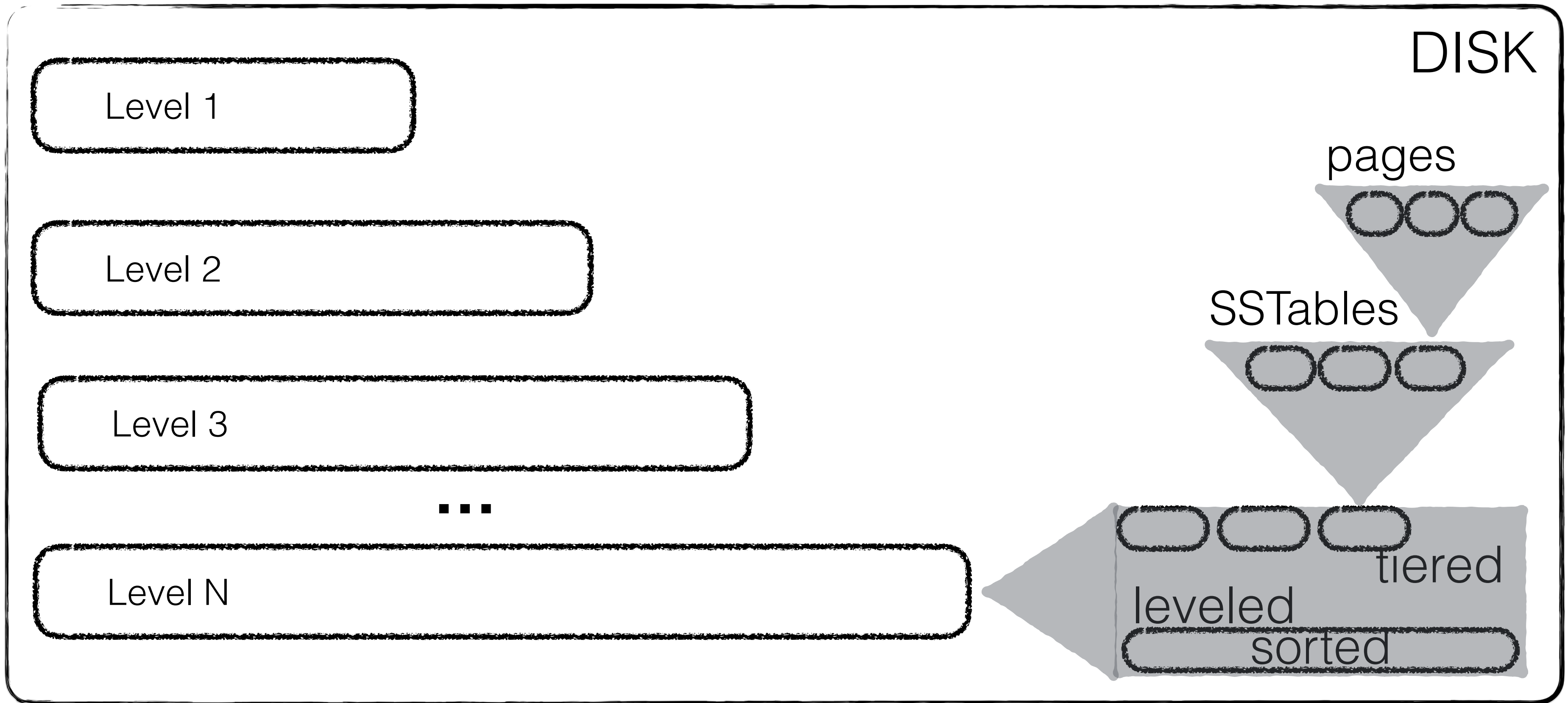
...

...



get (key)

buffer



[1,0,0,1,1,1]  
hash fun.

[min-max]

bloom  
filters

fence  
pointers

get (key)

buffer

MEMORY

DISK

pages

SSTables

tiered

leveled

sorted

Level 1

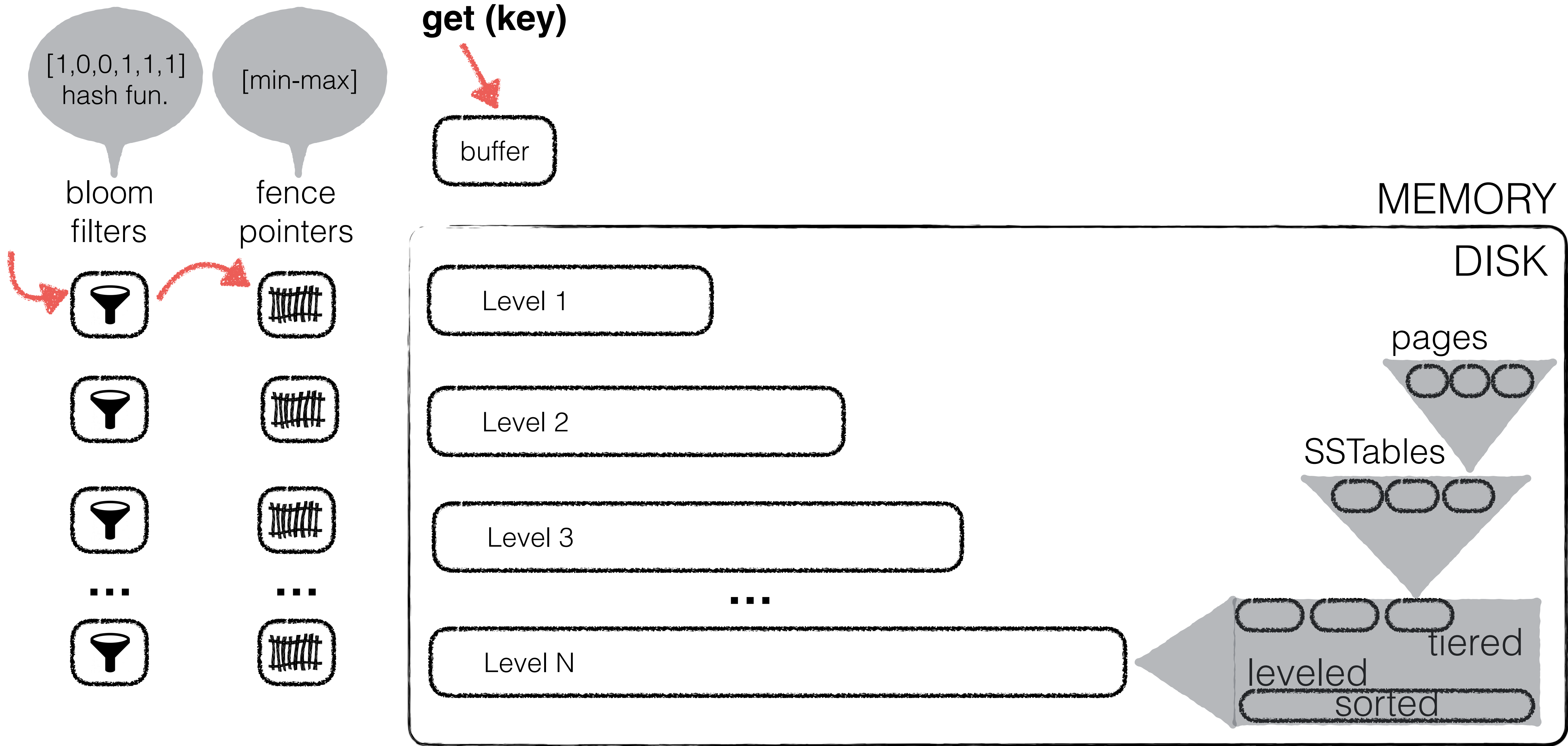
Level 2

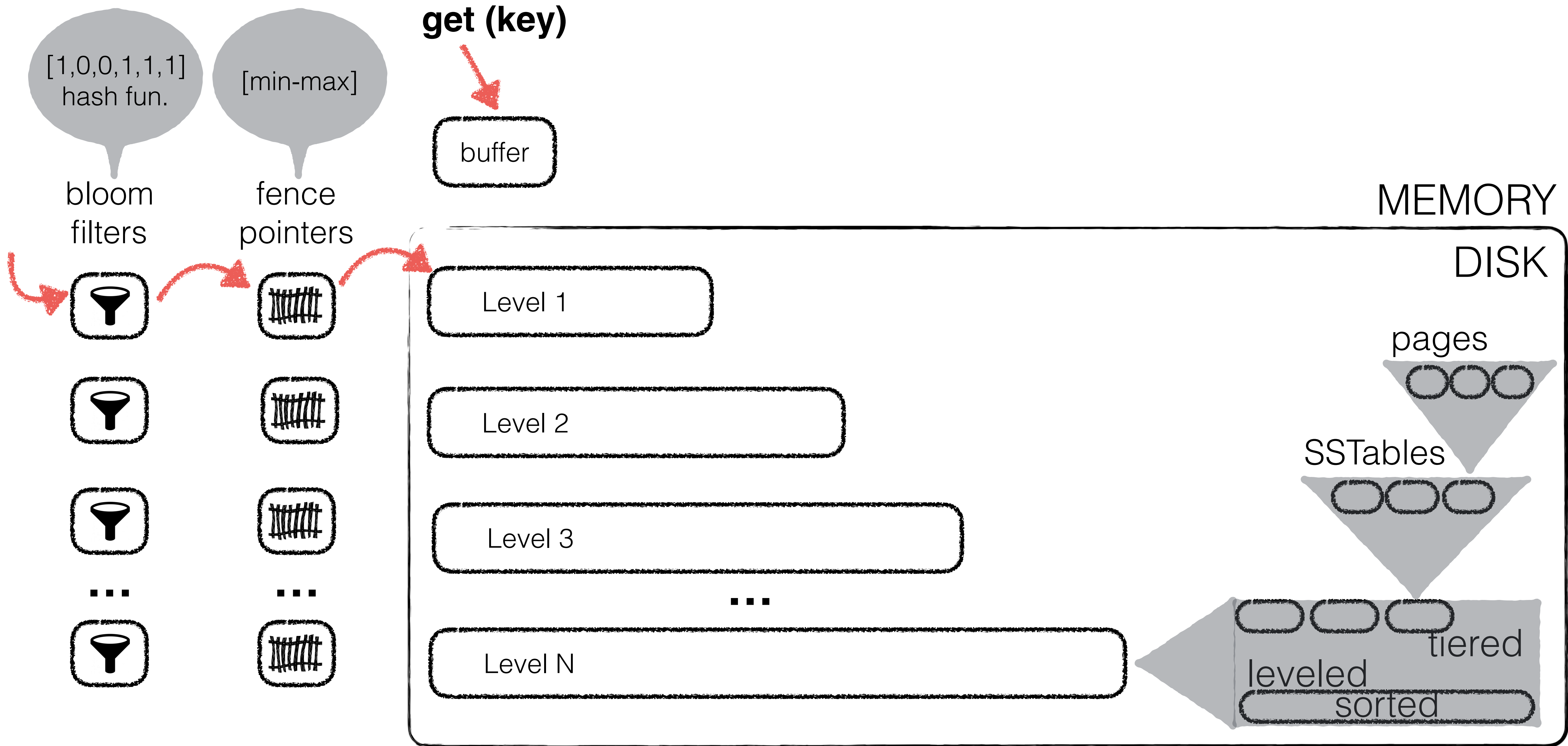
Level 3

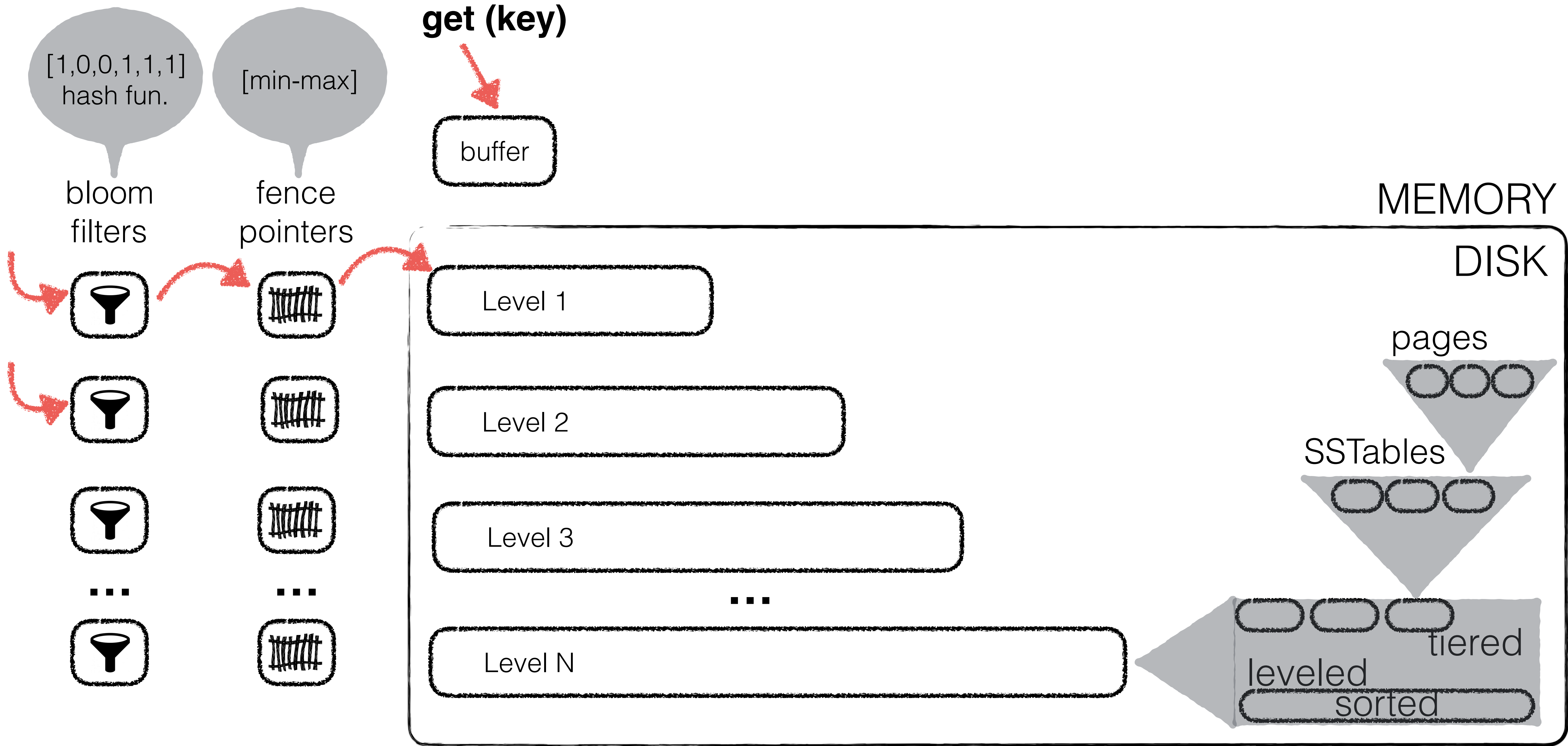
Level N

...

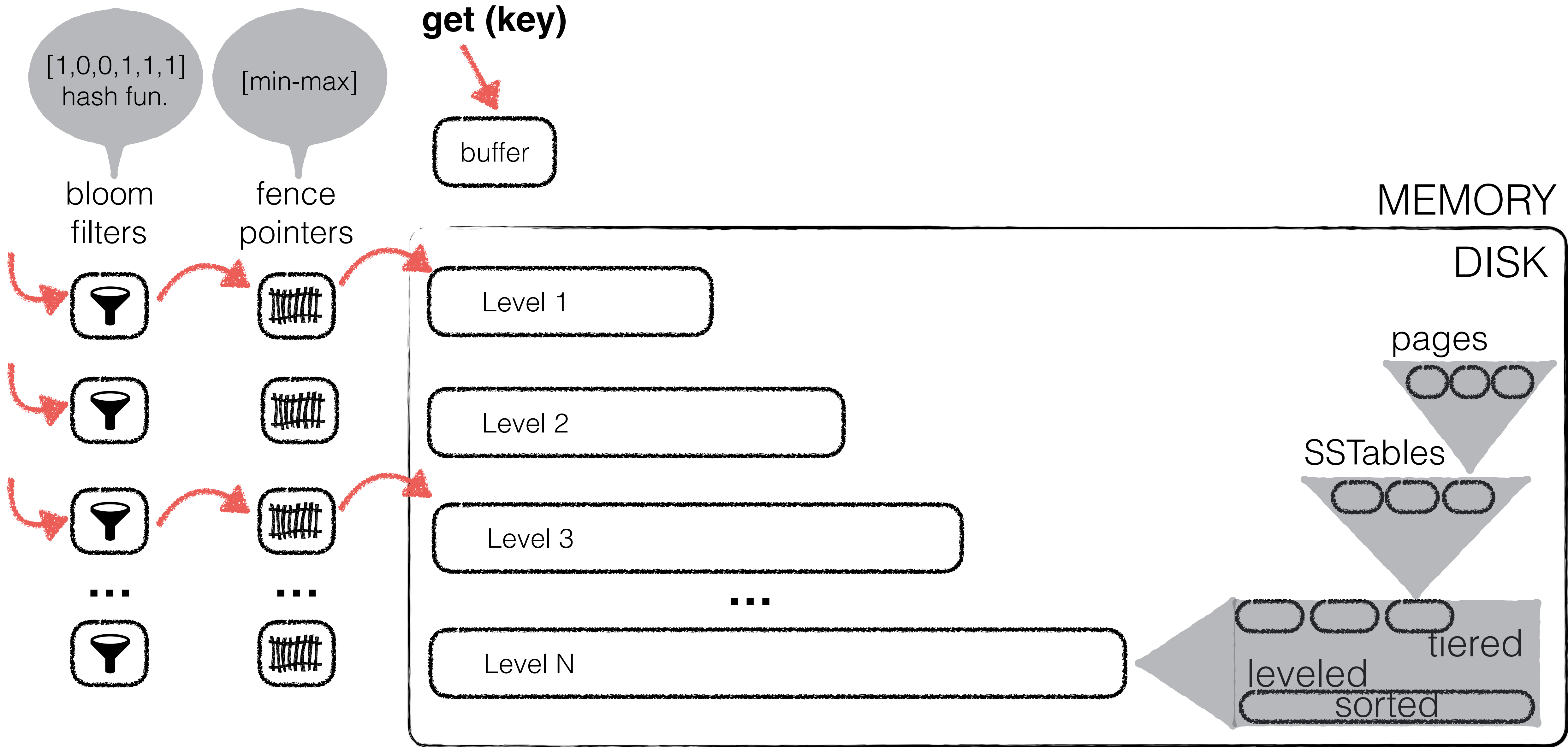


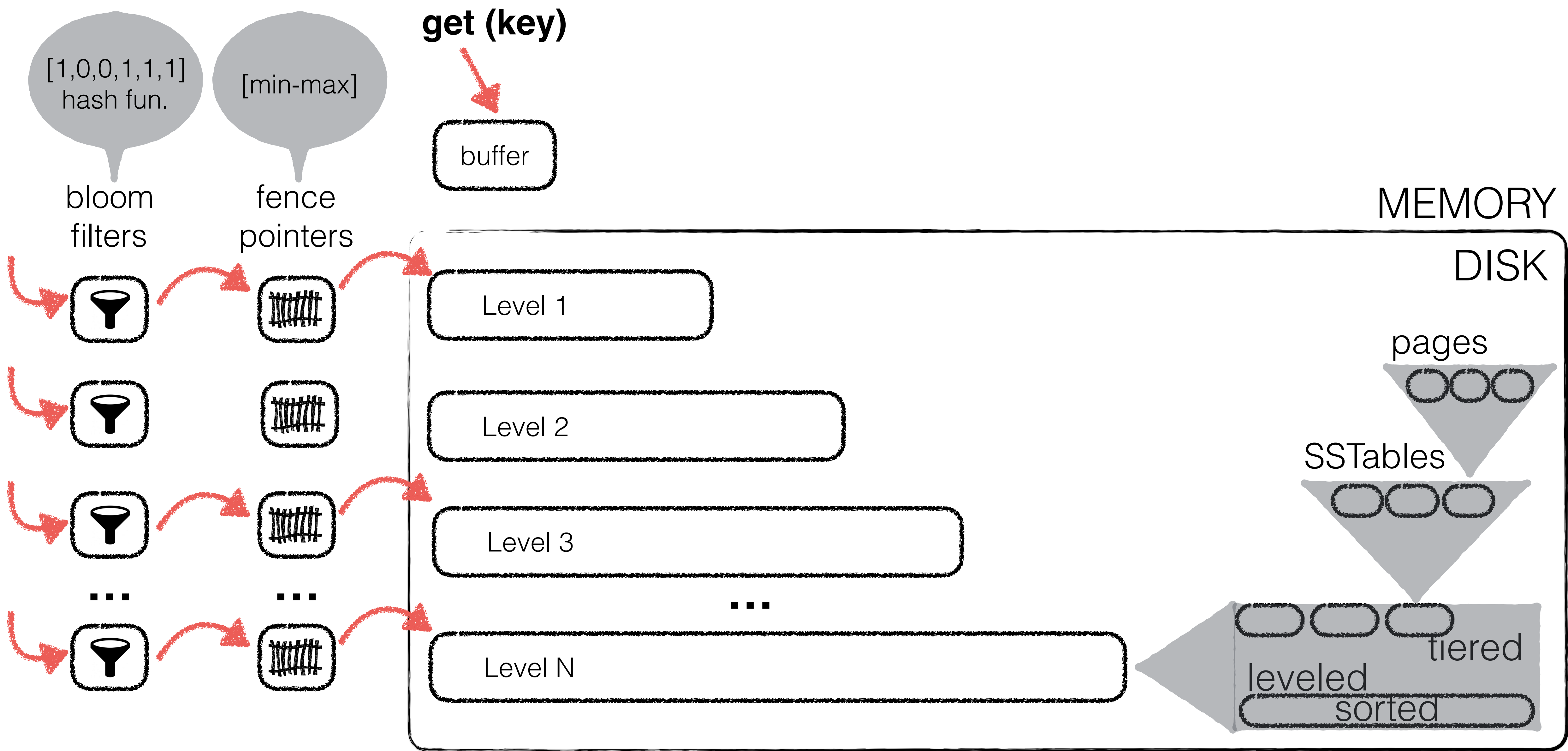




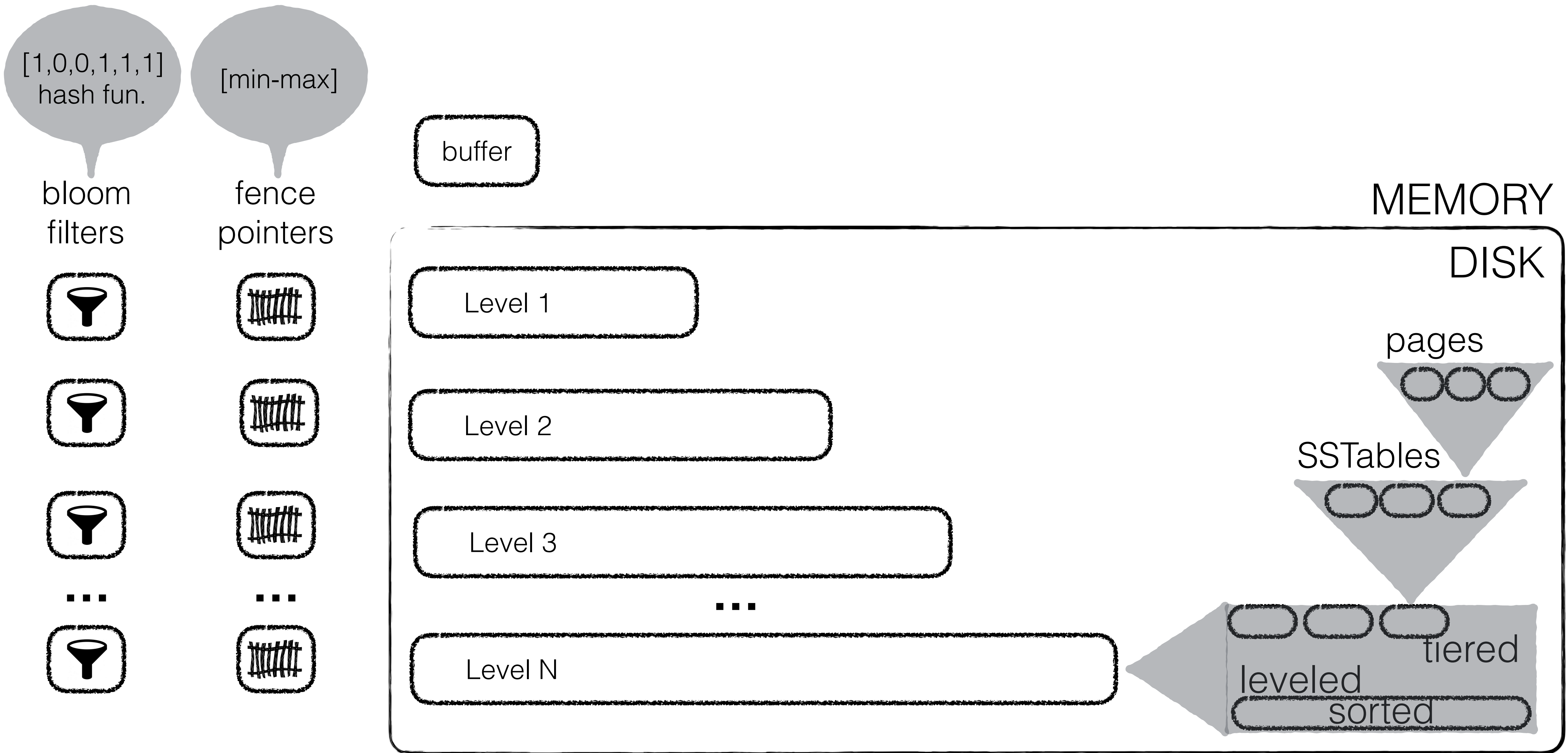






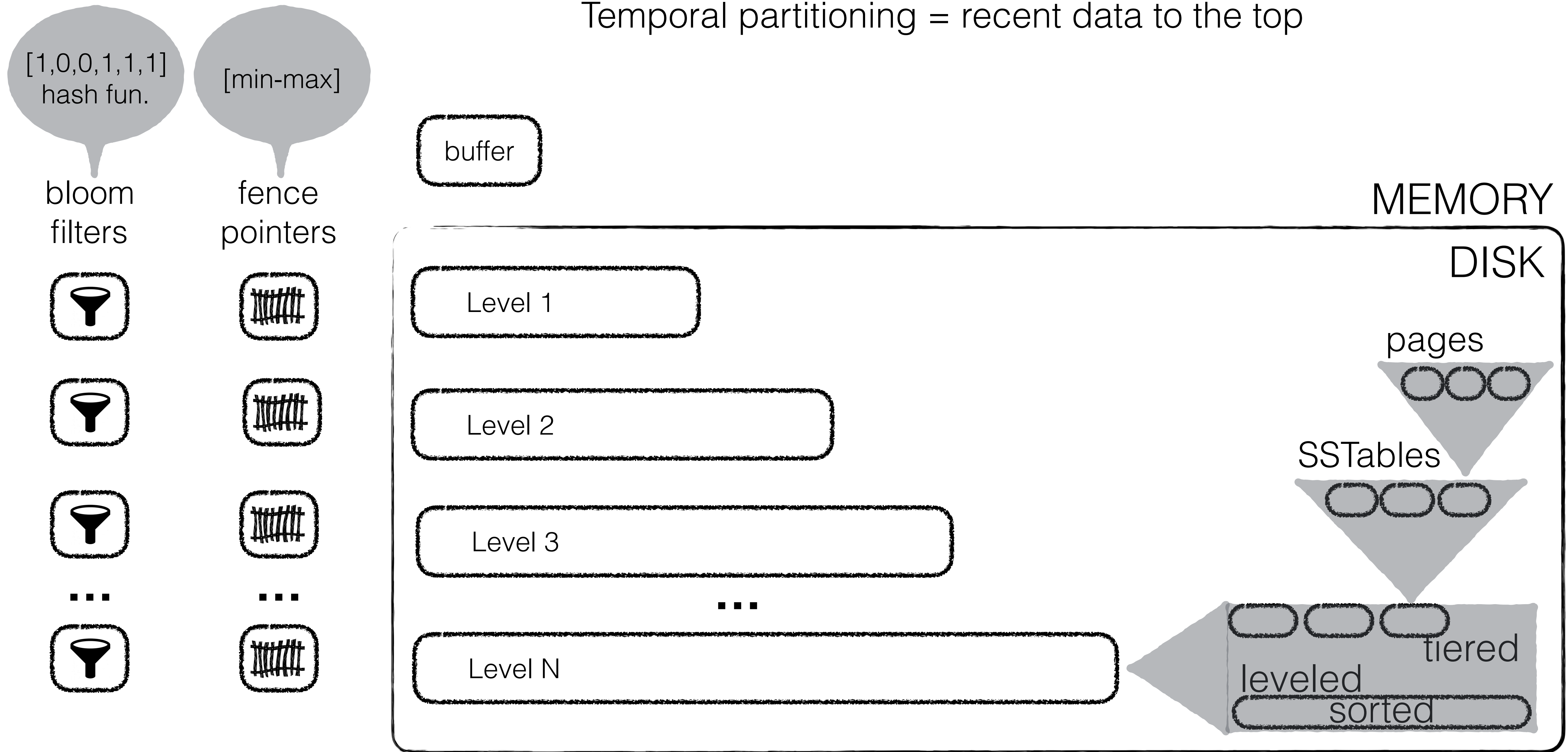


Instant writes, but overtime have to be merged > 1



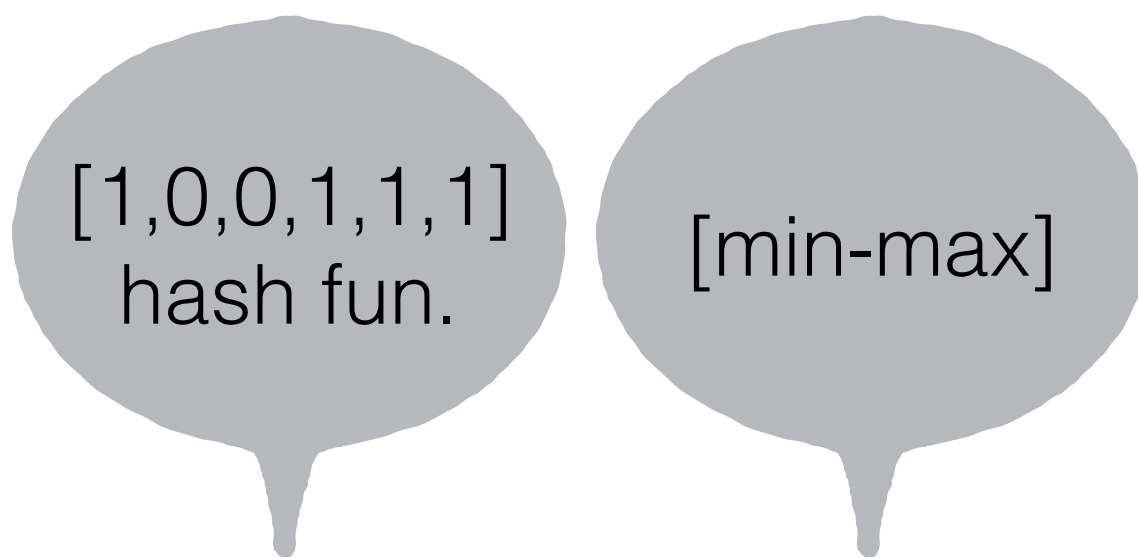


Instant writes, but overtime have to be merged > 1  
Temporal partitioning = recent data to the top

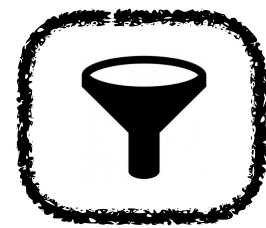
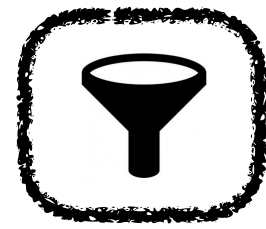




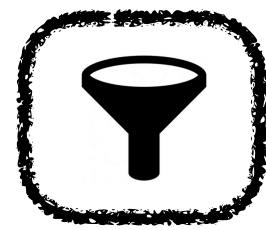
Instant writes, but overtime have to be merged > 1  
Temporal partitioning = recent data to the top  
But duplicates across levels (memory amplification)



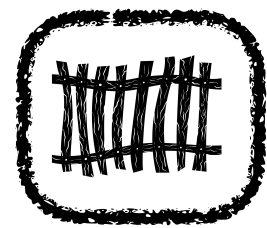
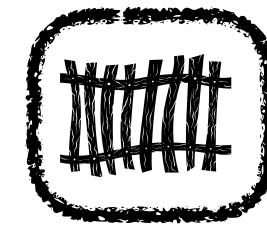
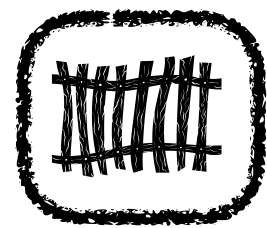
bloom  
filters



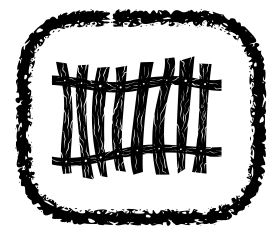
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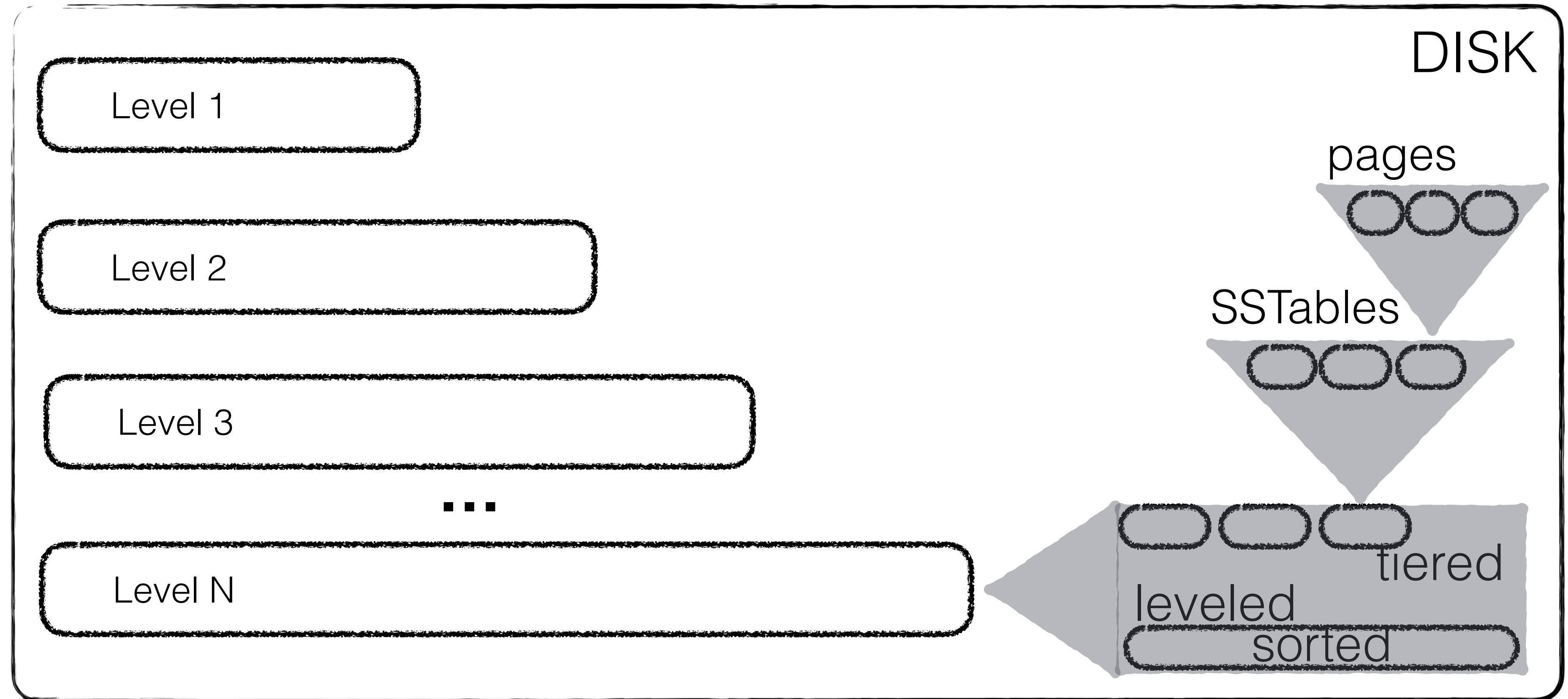
fence  
pointers



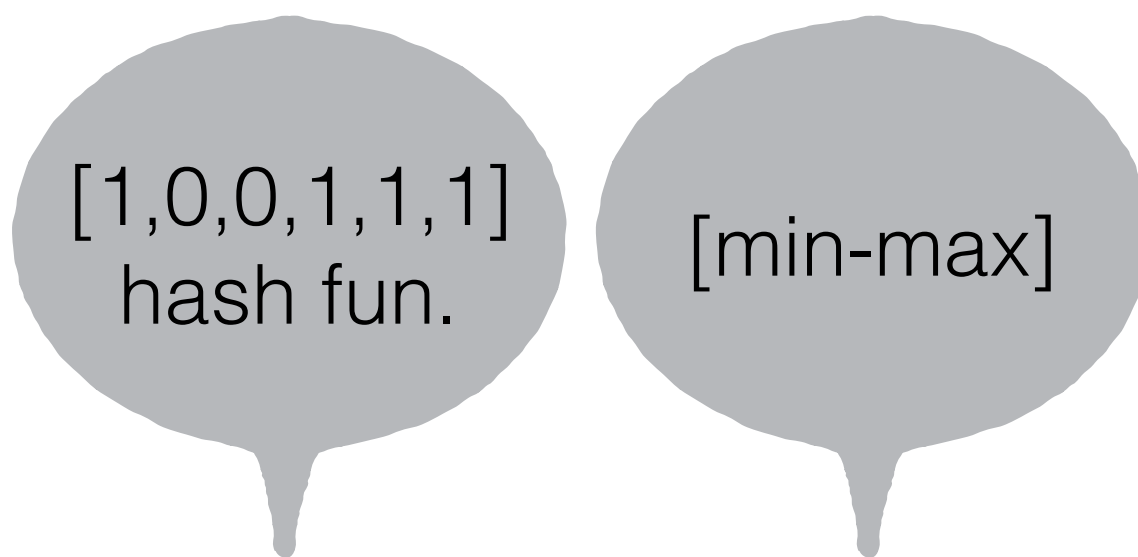
...



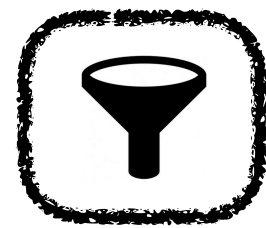
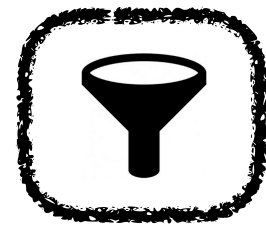
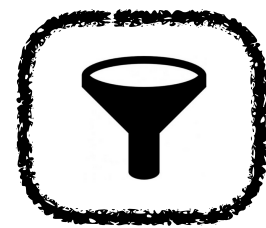
buffer



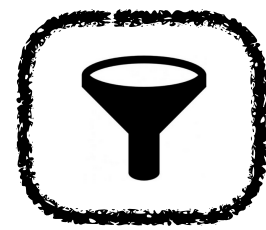
Instant writes, but overtime have to be merged > 1  
Temporal partitioning = recent data to the top  
But duplicates across levels (memory amplification)  
Reads can get away with 0-1 disk page read per level



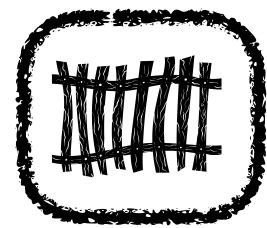
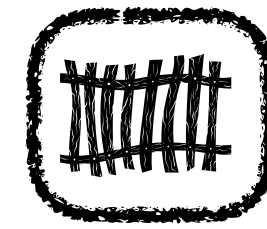
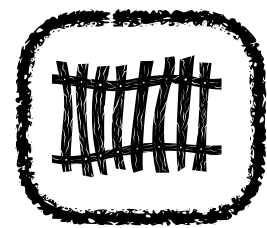
bloom  
filters



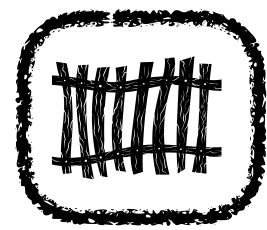
...



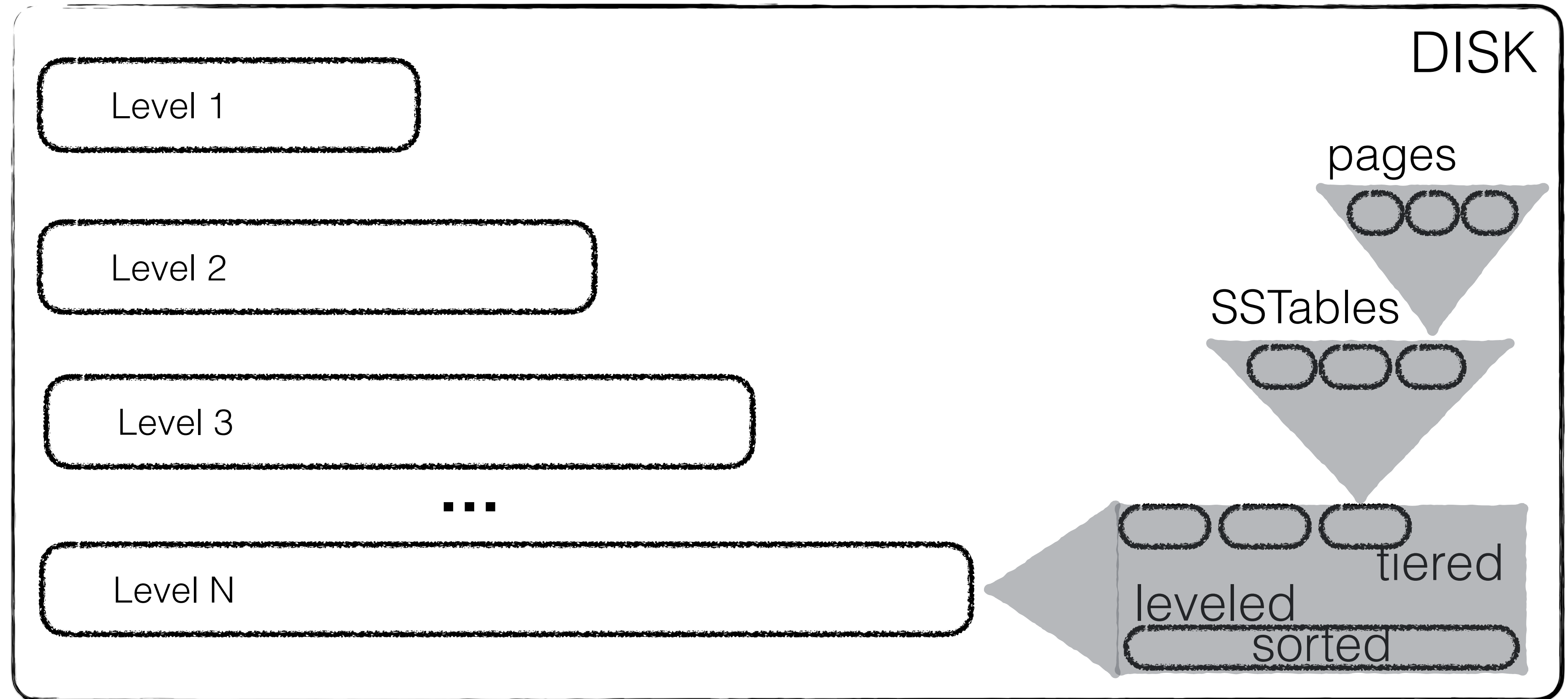
fence  
pointers



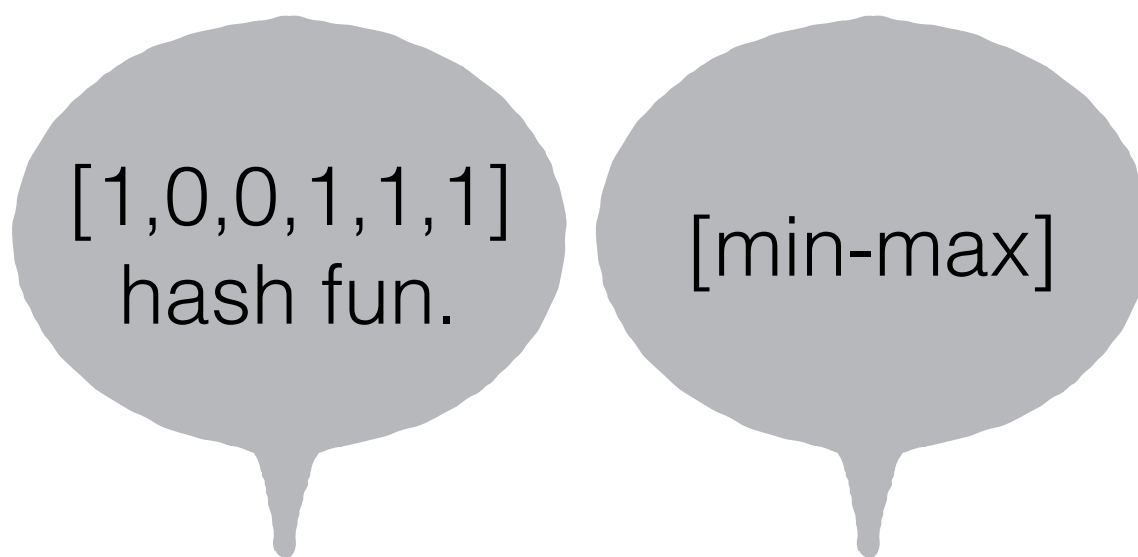
...



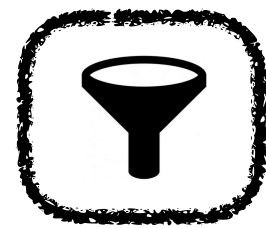
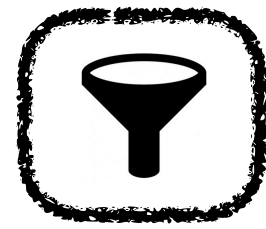
buffer



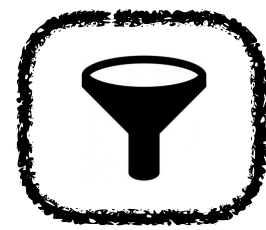
Instant writes, but overtime have to be merged > 1  
Temporal partitioning = recent data to the top  
But duplicates across levels (memory amplification)  
Reads can get away with 0-1 disk page read per level  
Run layout affect read/write costs



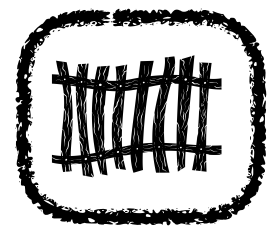
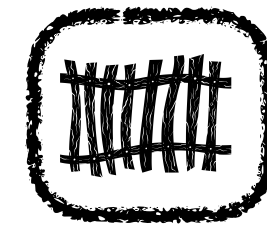
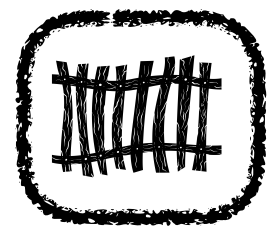
bloom  
filters



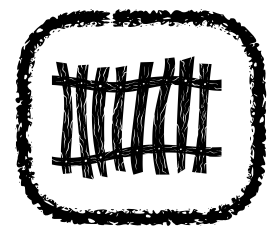
...



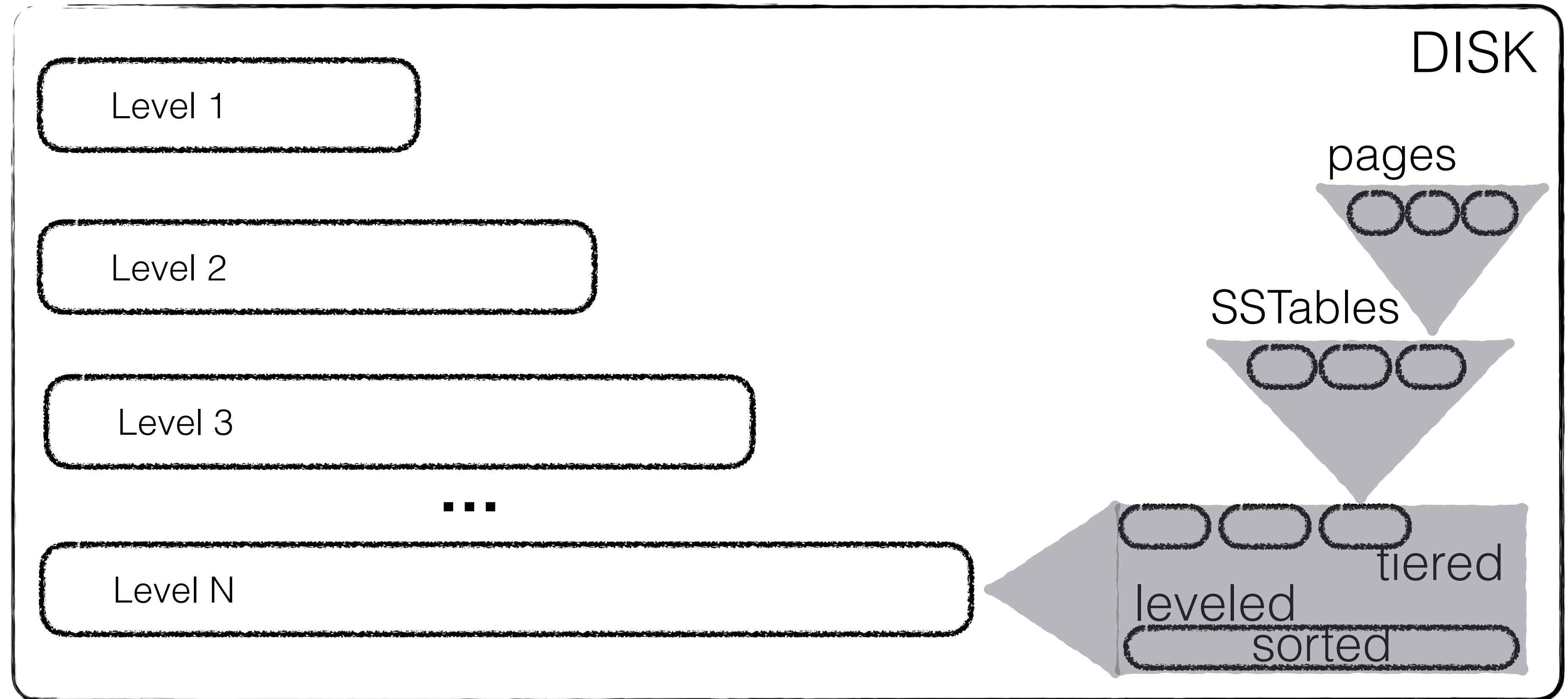
fence  
pointers



...

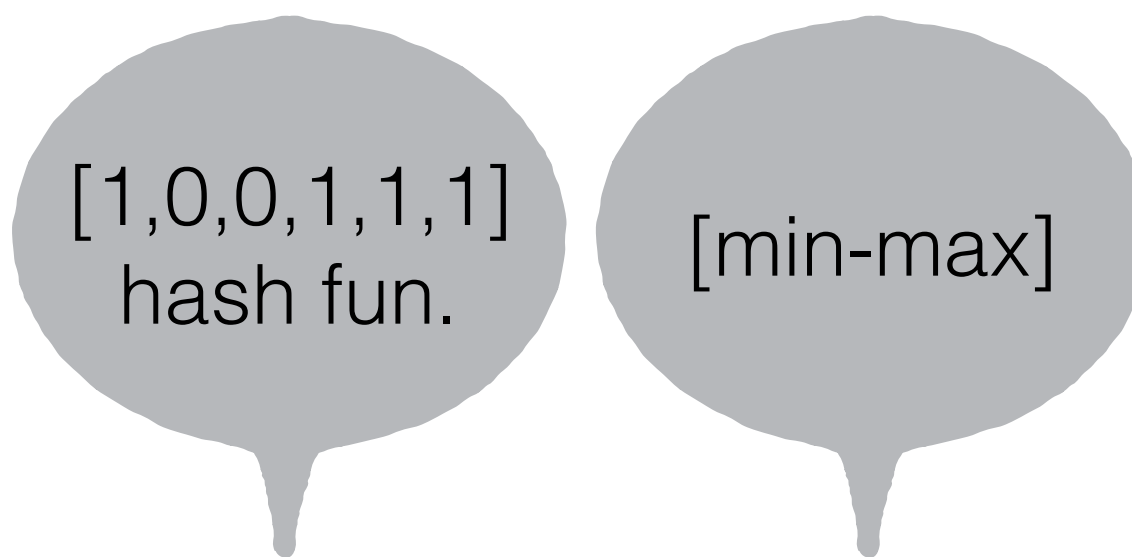


buffer



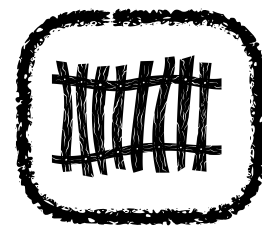
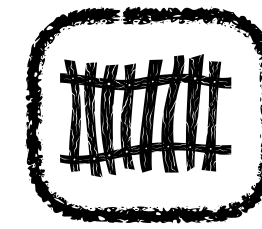
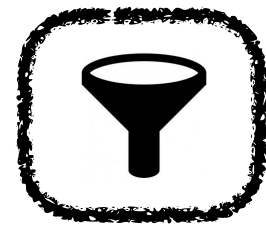
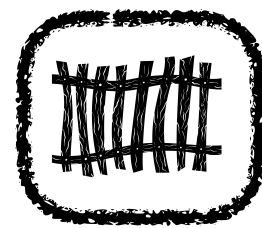


Instant writes, but overtime have to be merged > 1  
Temporal partitioning = recent data to the top  
But duplicates across levels (memory amplification)  
Reads can get away with 0-1 disk page read per level  
Run layout affect read/write costs  
Size ratio affects everything



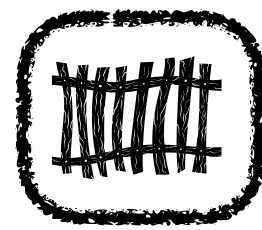
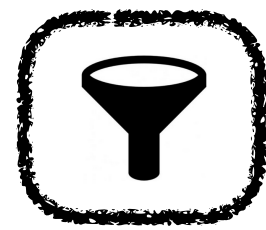
bloom  
filters

fence  
pointers

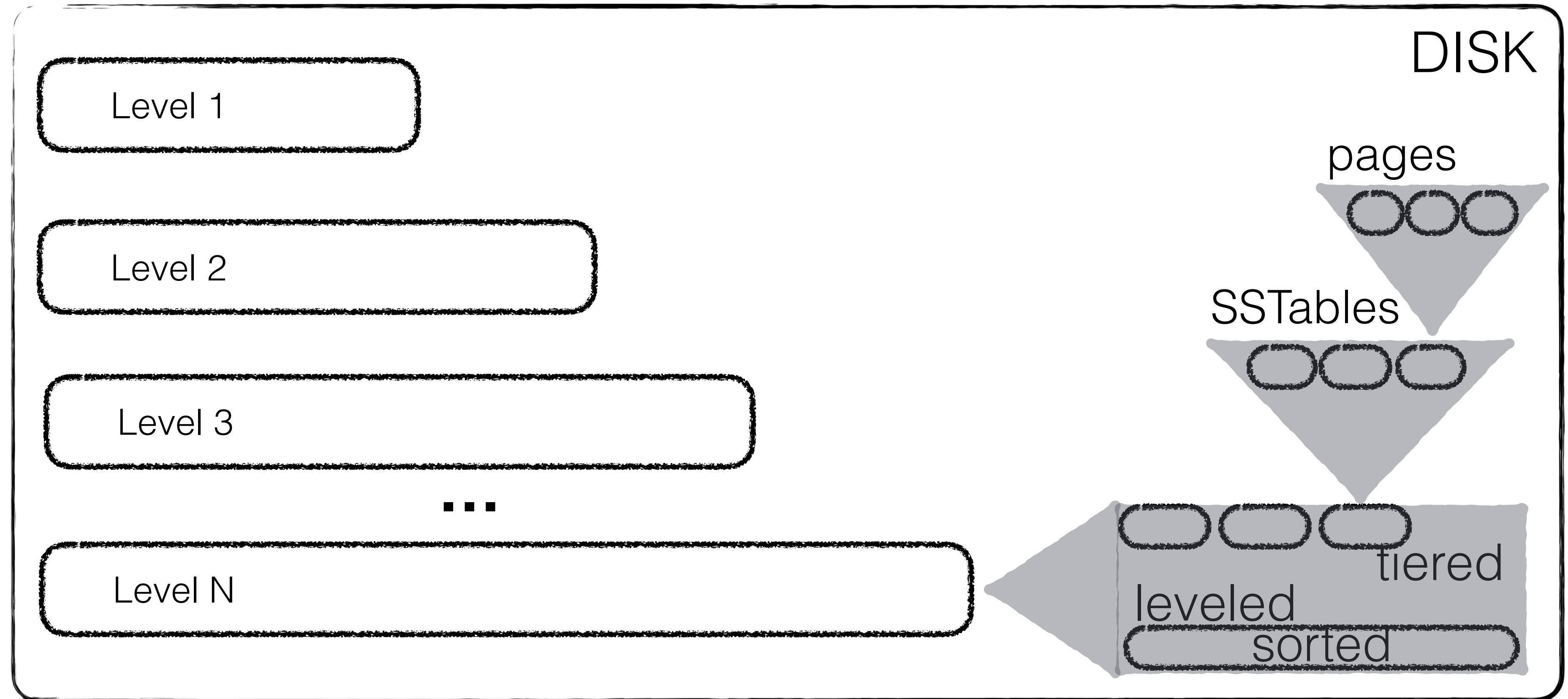


...

...



buffer





[1,0,0,1,1,1]  
hash fun.

bloom  
filters

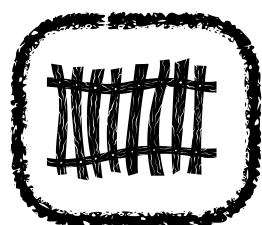
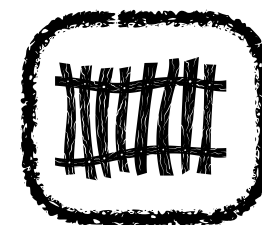
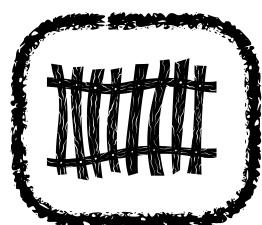


...

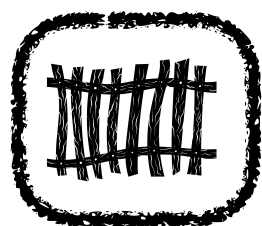


[min-max]

fence  
pointers



...



buffer

Level 1

Level 2

Level 3

...

Level N

size ratio

merge policy

filters bits per entry

size of (page, buffer, ..)

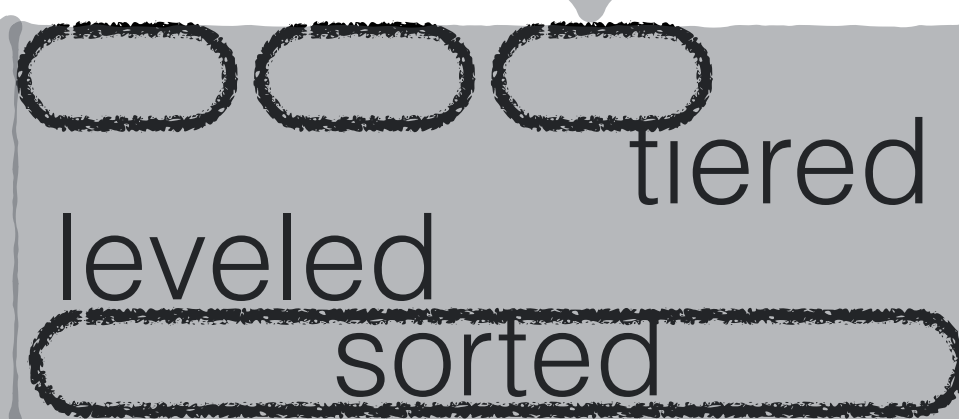
internal k-v layout

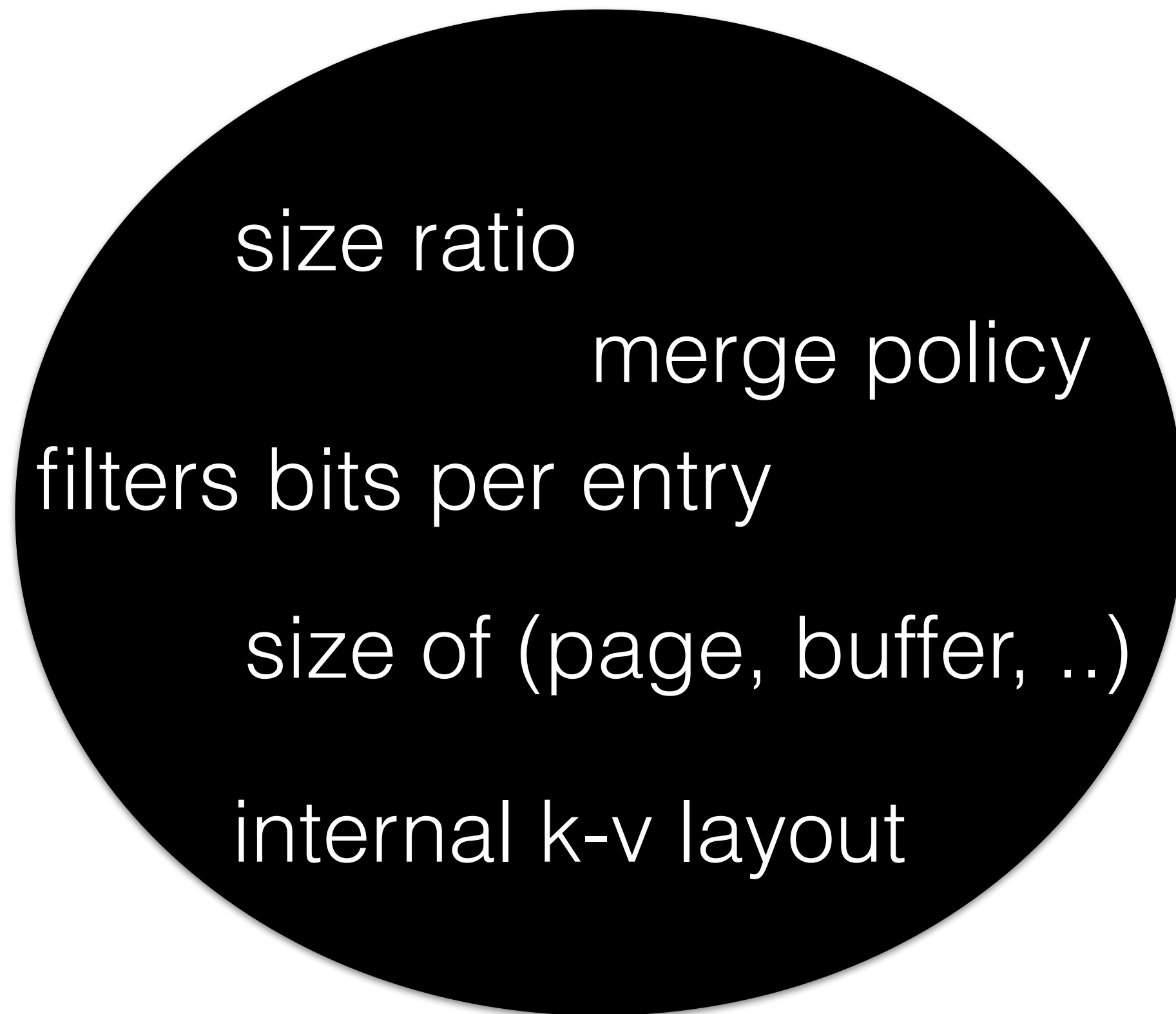
MEMORY  
DISK

pages



SSTables





## **For every design principle X:**

1. Which are X's meaningful values?
2. How does X affect read, update and memory amplification?
3. Should X be a design principle or can it be optimized out?



**bits per entry in filters**

**monkey**@SIGMOD2017

**M**onkey: **O**ptimal **N**avigable

**K**ey-Value Store



**bits per entry in filters**

**monkey**@SIGMOD2017

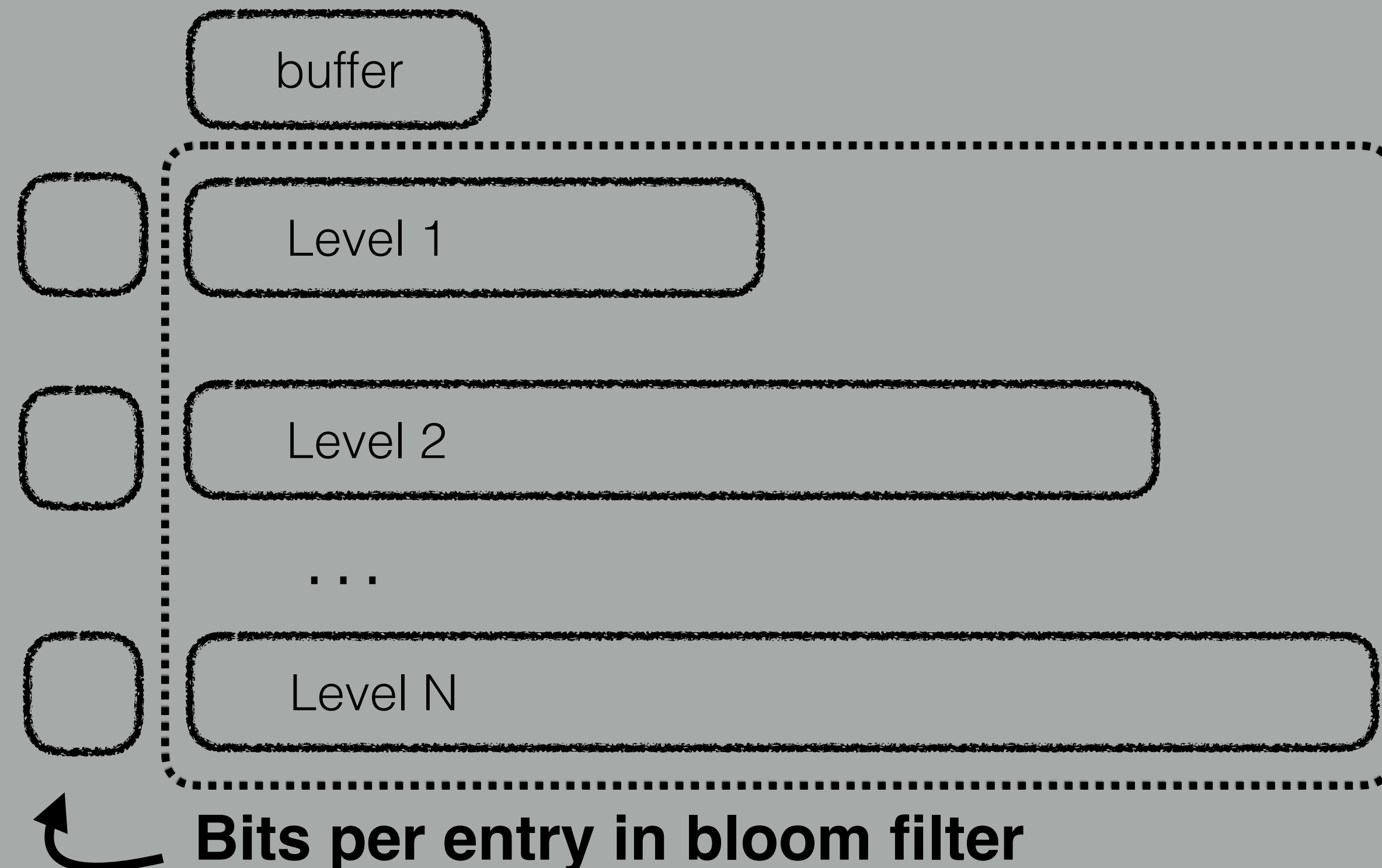
**Monkey: Optimal Navigable**

**Key**-Value Store

standard design: fixed per run

at most one I/O per level

worst case I/O: sum of false positive rates





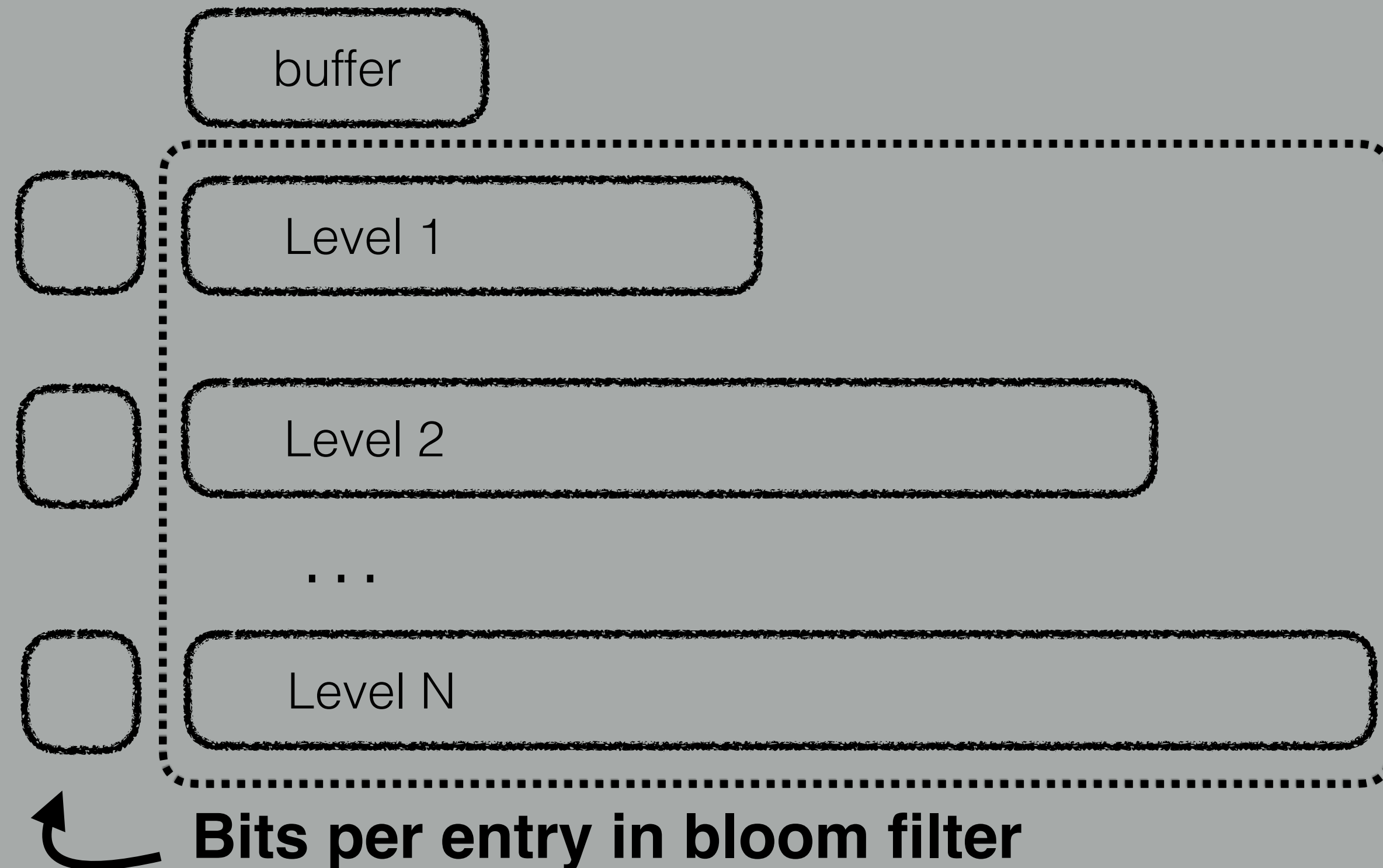
**bits per entry in filters**

**monkey**@SIGMOD2017

**Monkey: Optimal Navigable**

**Key**-Value Store

minimize sum of FPRs  
***“move” filters memory budget  
from big to small levels***







**bits per entry in filters**

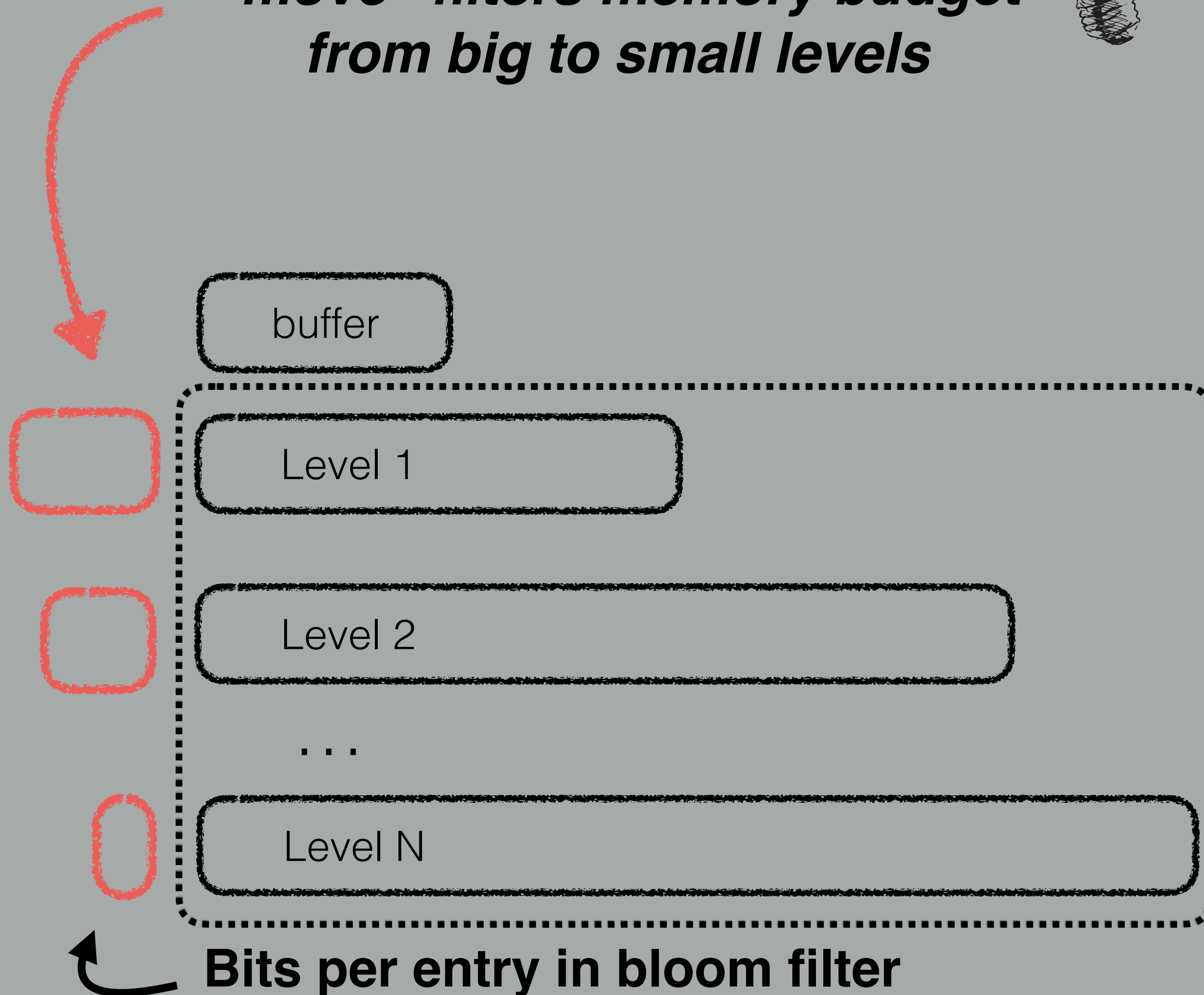
**monkey**@SIGMOD2017

**Monkey: Optimal Navigable**

**Key**-Value Store

minimize sum of FPRs

***“move” filters memory budget  
from big to small levels***





**bits per entry in filters**

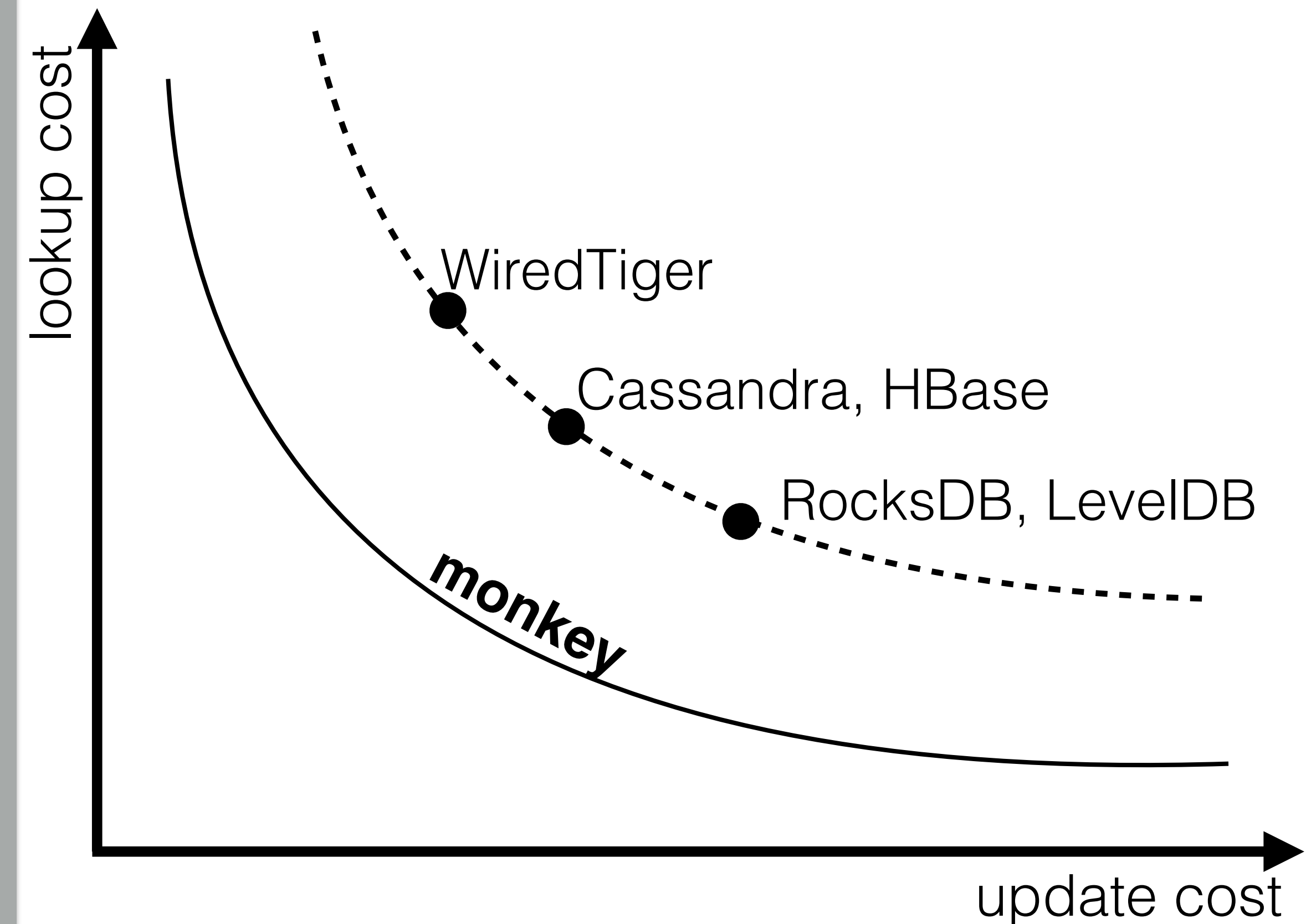
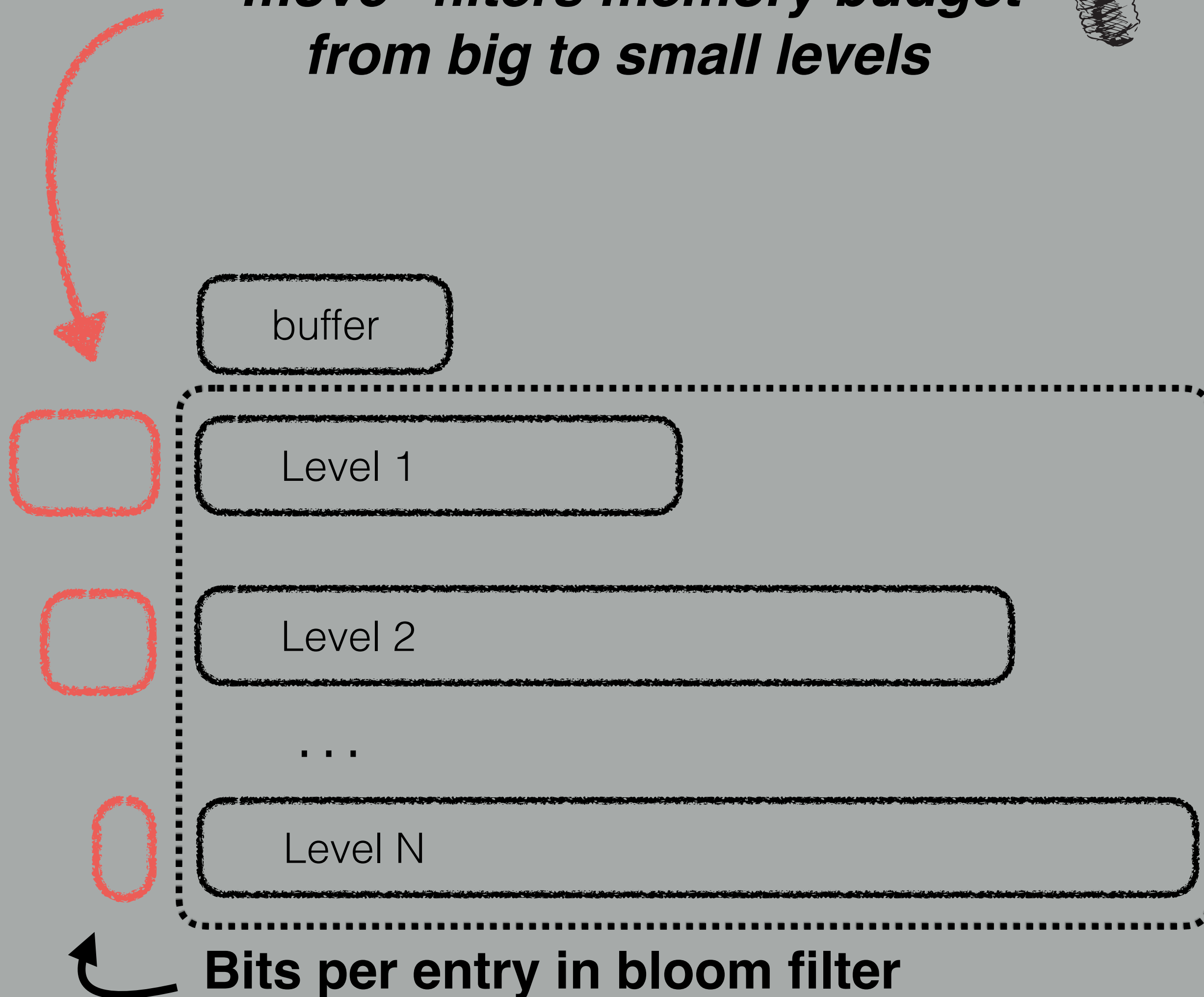
**monkey**@SIGMOD2017

**Monkey: Optimal Navigable**

**Key**-Value Store

minimize sum of FPRs

***“move” filters memory budget  
from big to small levels***





**bits per entry in filters**

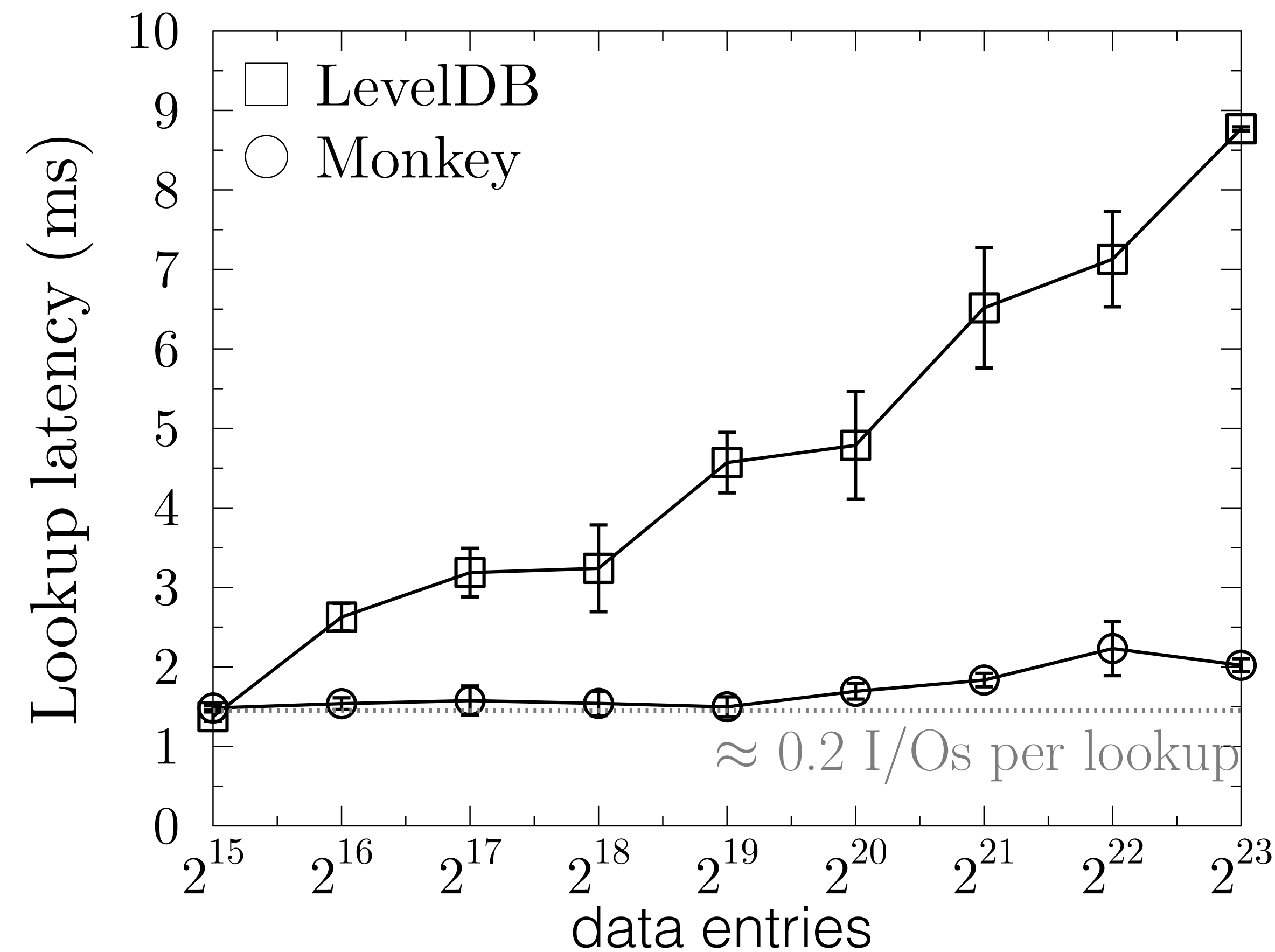
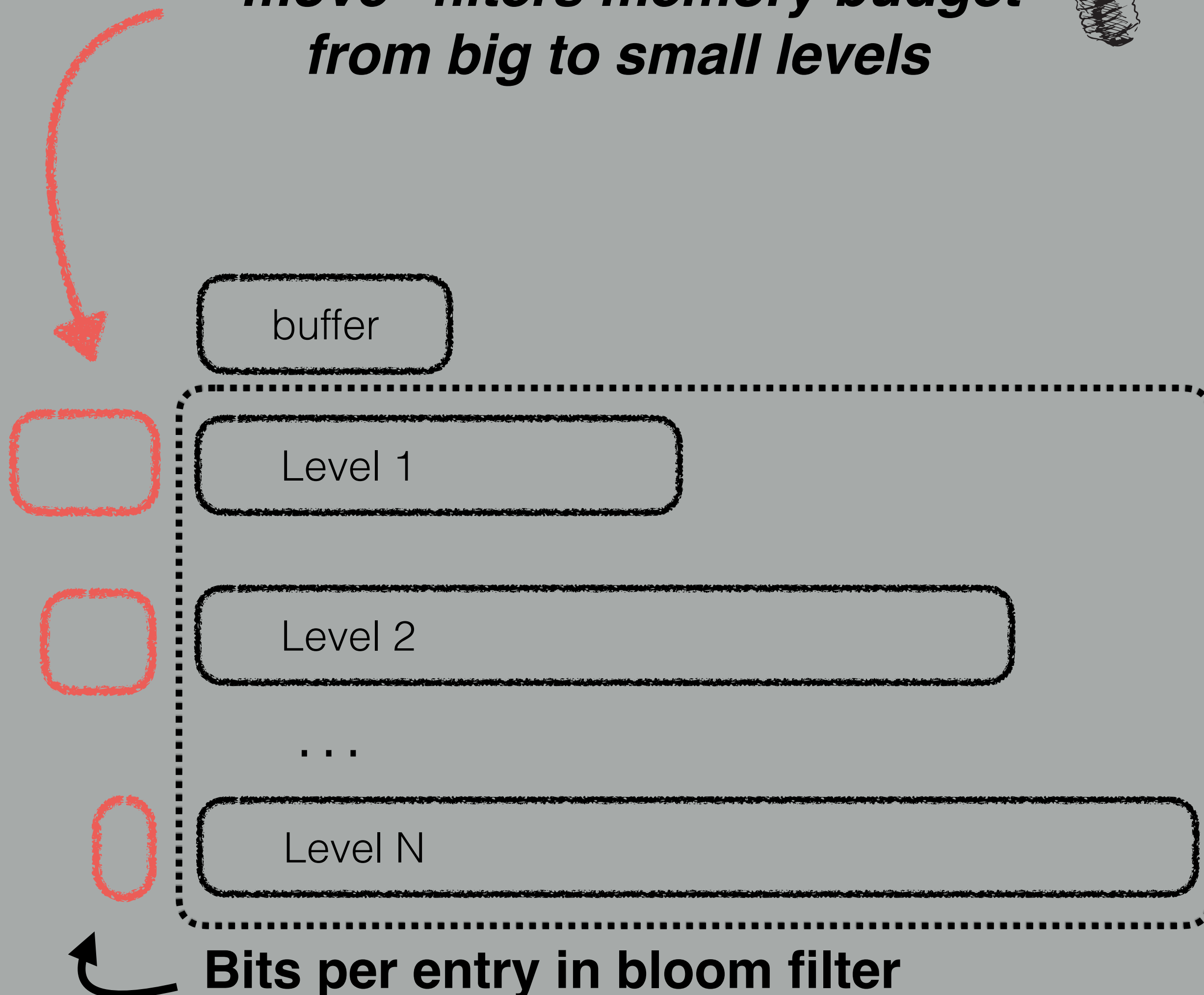
**monkey**@SIGMOD2017

**Monkey: Optimal Navigable**

**Key**-Value Store

minimize sum of FPRs

***“move” filters memory budget  
from big to small levels***



uniform, zero result, point queries, entry size=1KB



**merge policy**

**dostoevsky@SIGMOD2018**

**D**ostoevsky: **S**pace-**T**ime **O**ptimized

**E**volvable **S**calable **K**ey-Value Store





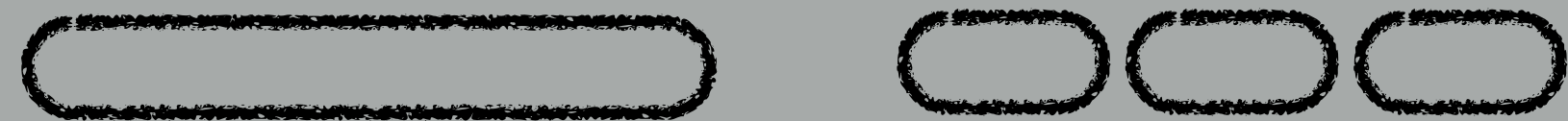
merge policy

dostoevsky@SIGMOD2018

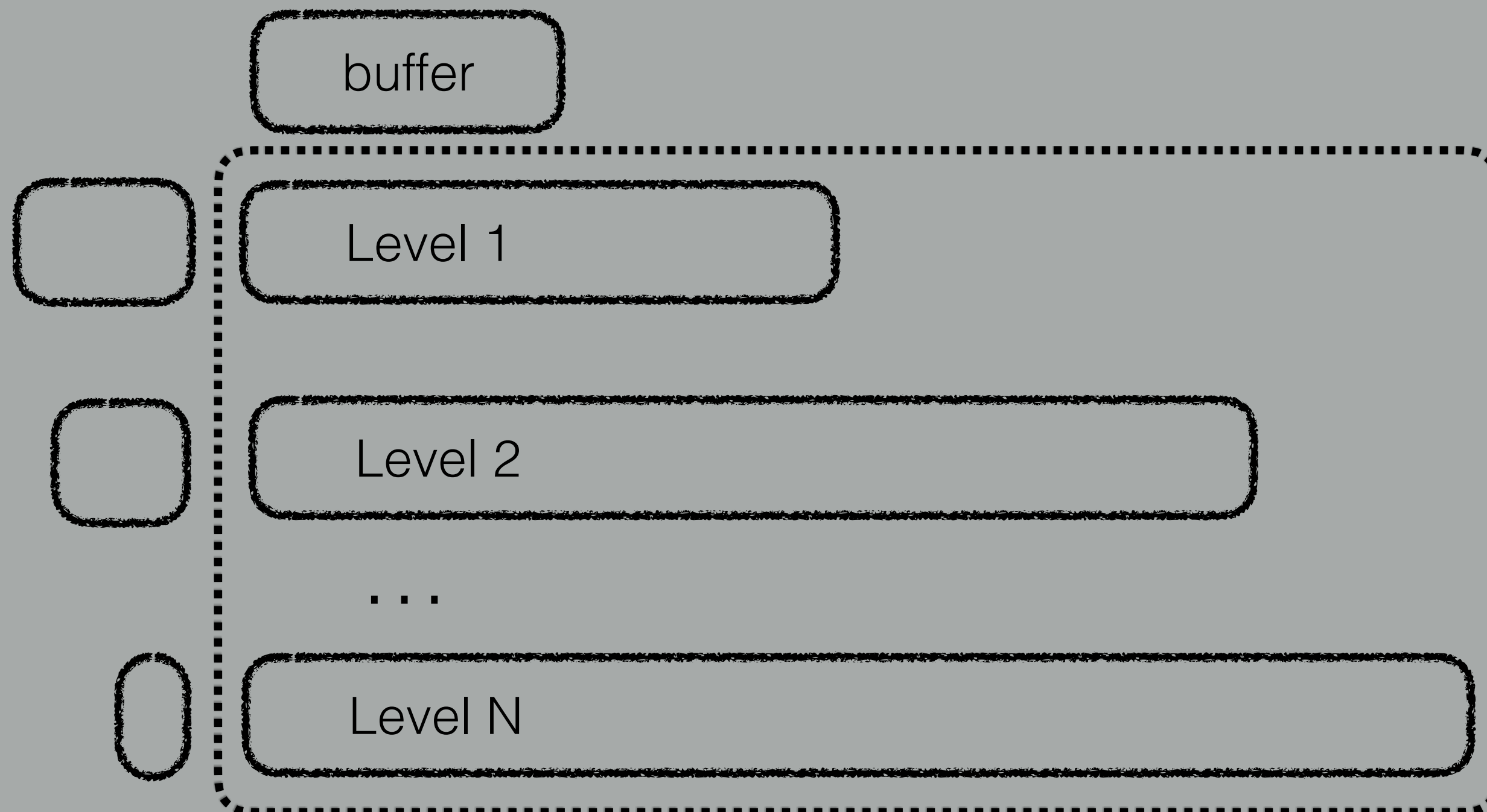
Dostoevsky: **S**pace-**T**ime **O**ptimized

**E**volvable **S**calable **K**ey-Value Store

standard design: fixed across levels



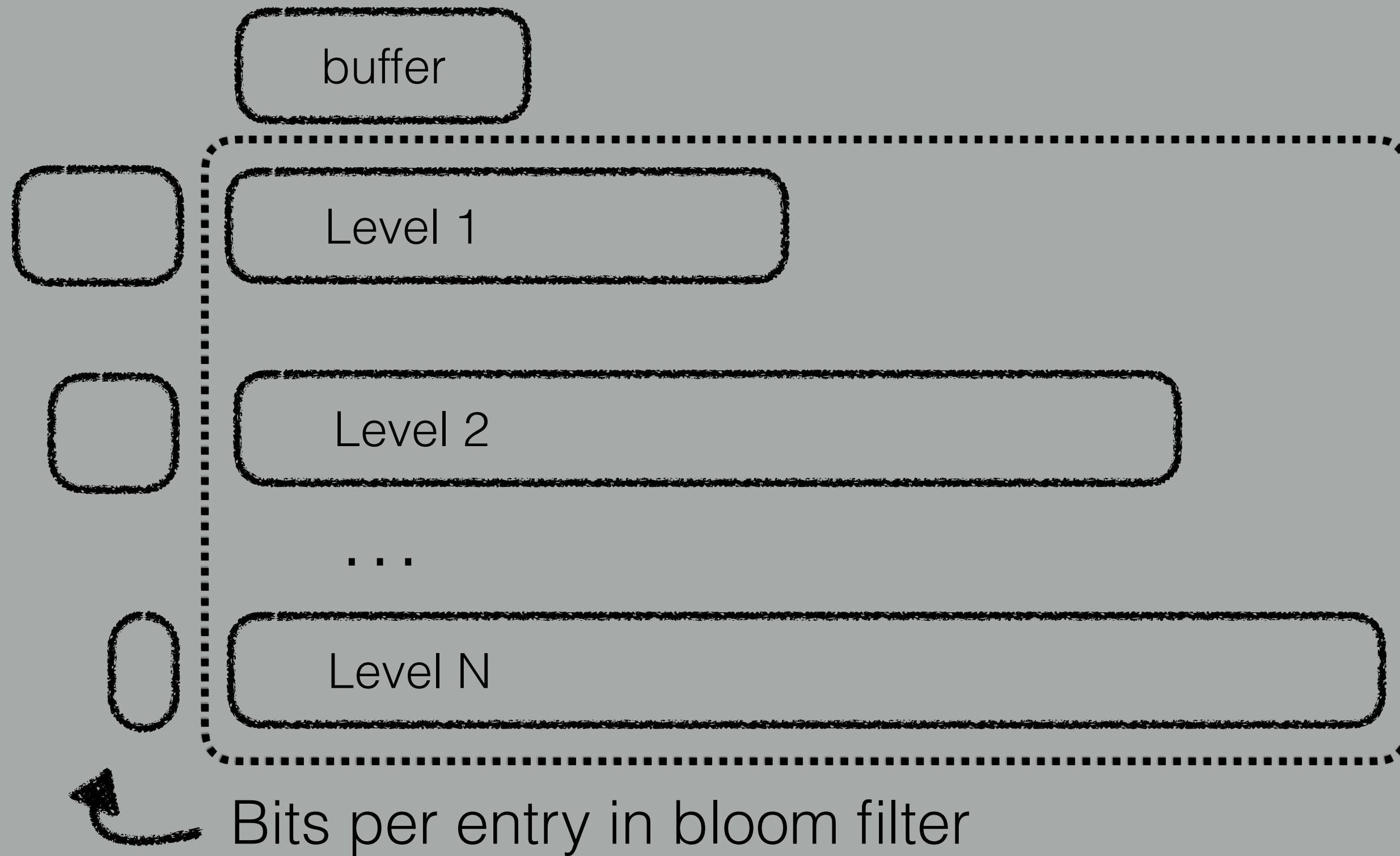
Leveled helps reads; tiered helps writes



Bits per entry in bloom filter



*hybrid merge policy  
tiered for small levels*



**merge policy**

**dostoevsky@SIGMOD2018**

**D**ostoevsky: **S**pace-**T**ime **O**ptimized

**E**volvable **S**calable **K**ey-Value Store



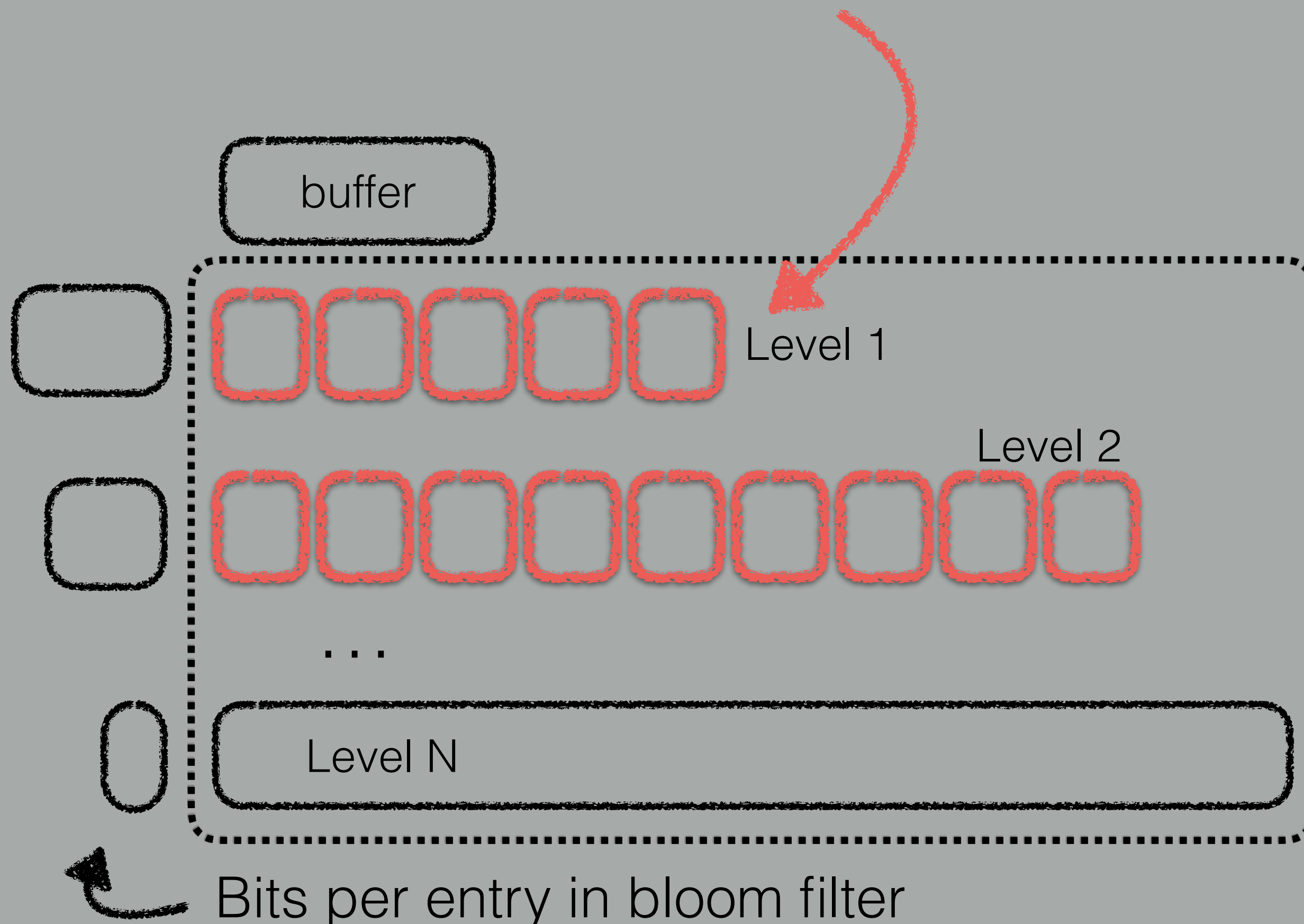
**merge policy**

**dostoevsky**@SIGMOD2018

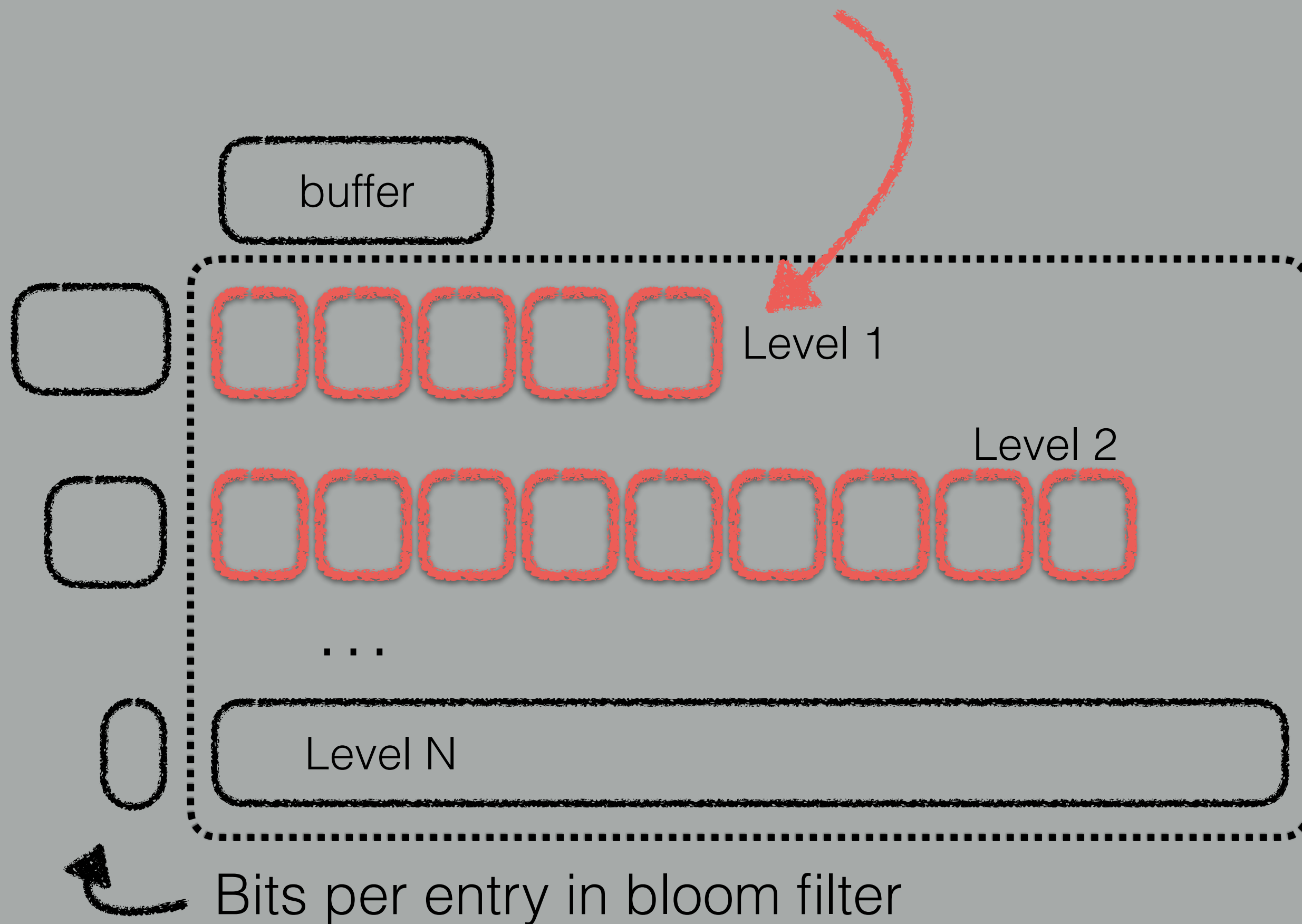
**D**ostoevsky: **S**pace-**T**ime **O**ptimized

**E**volvable **S**calable **K**ey-Value Store

*hybrid merge policy  
tiered for small levels*



*hybrid merge policy  
tiered for small levels*

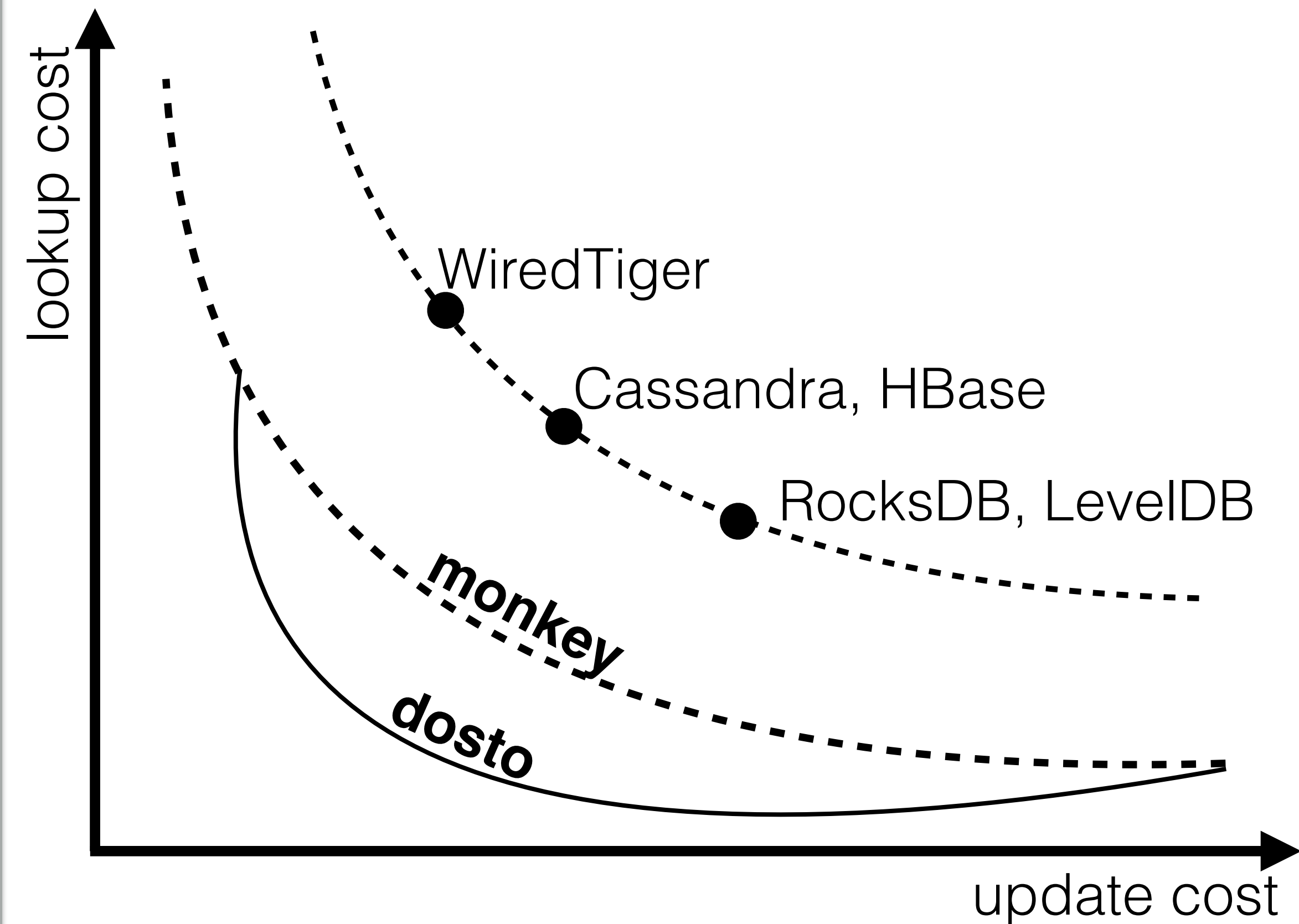


**merge policy**

**dostoevsky@SIGMOD2018**

**D**ostoevsky: **S**pace-**T**ime **O**ptimized

**E**volvable **S**calable **K**ey-Value Store







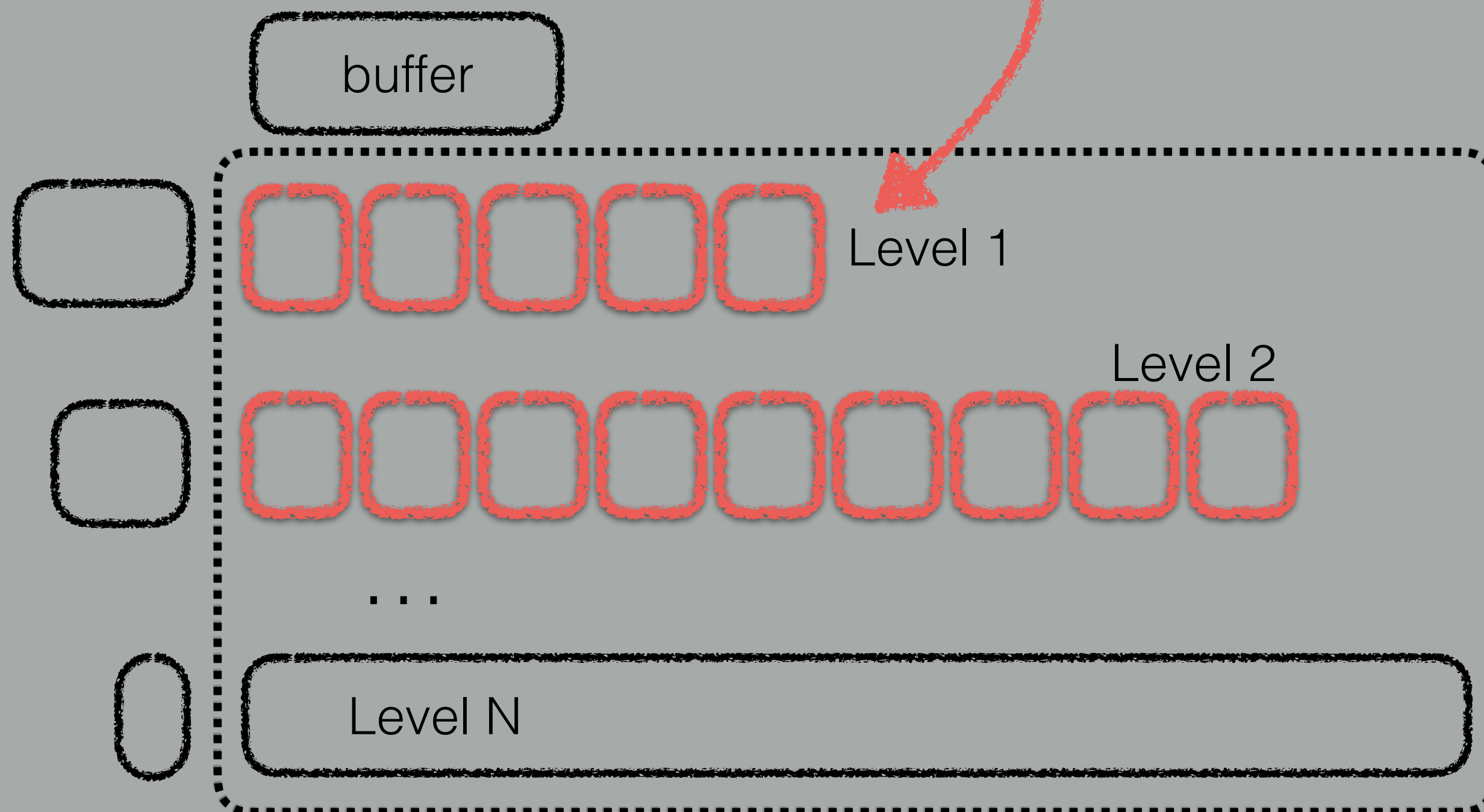
**merge policy**

**dostoevsky@SIGMOD2018**

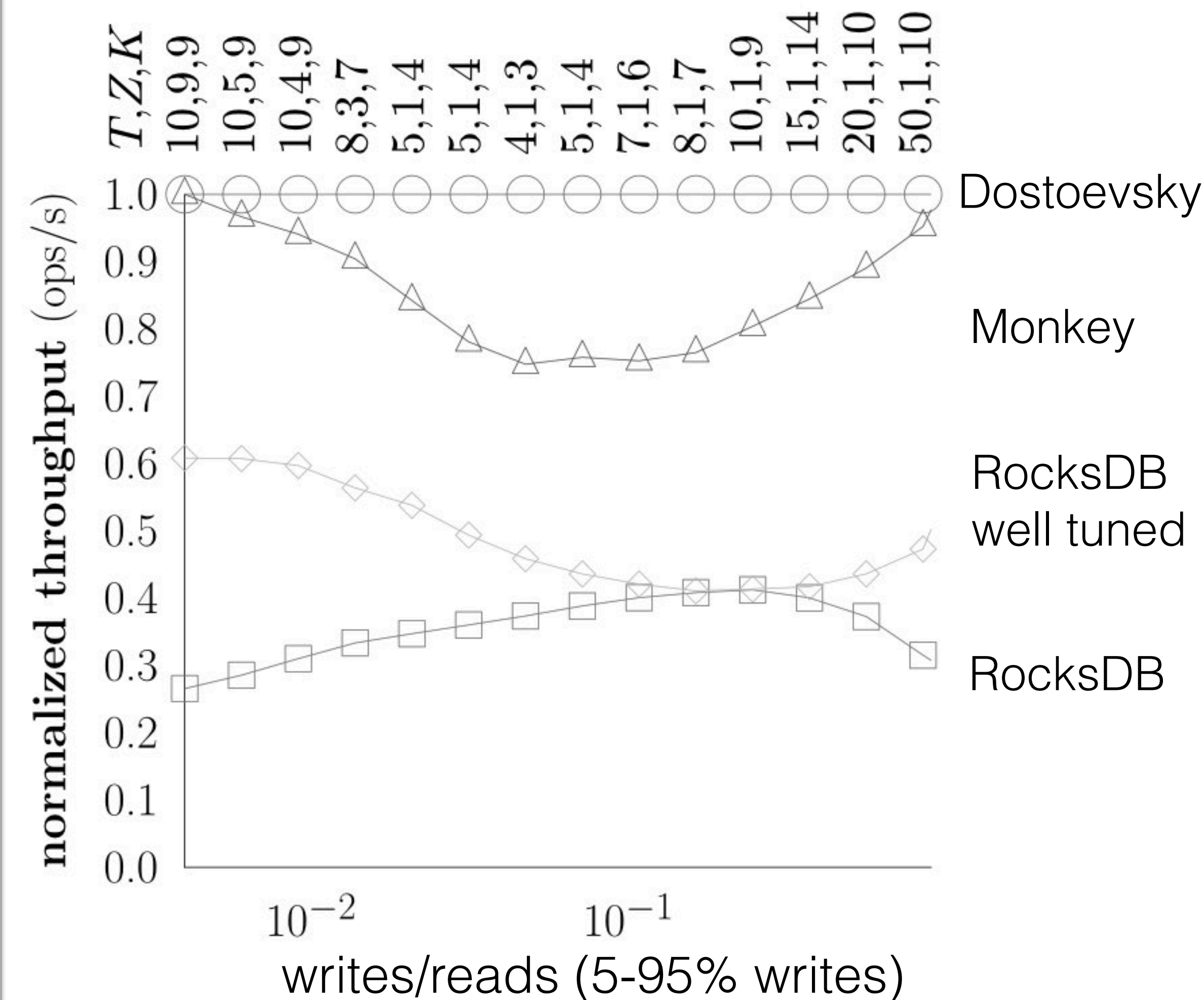
**D**ostoevsky: **S**pace-**T**ime **O**ptimized

**E**volvable **S**calable **K**ey-Value Store

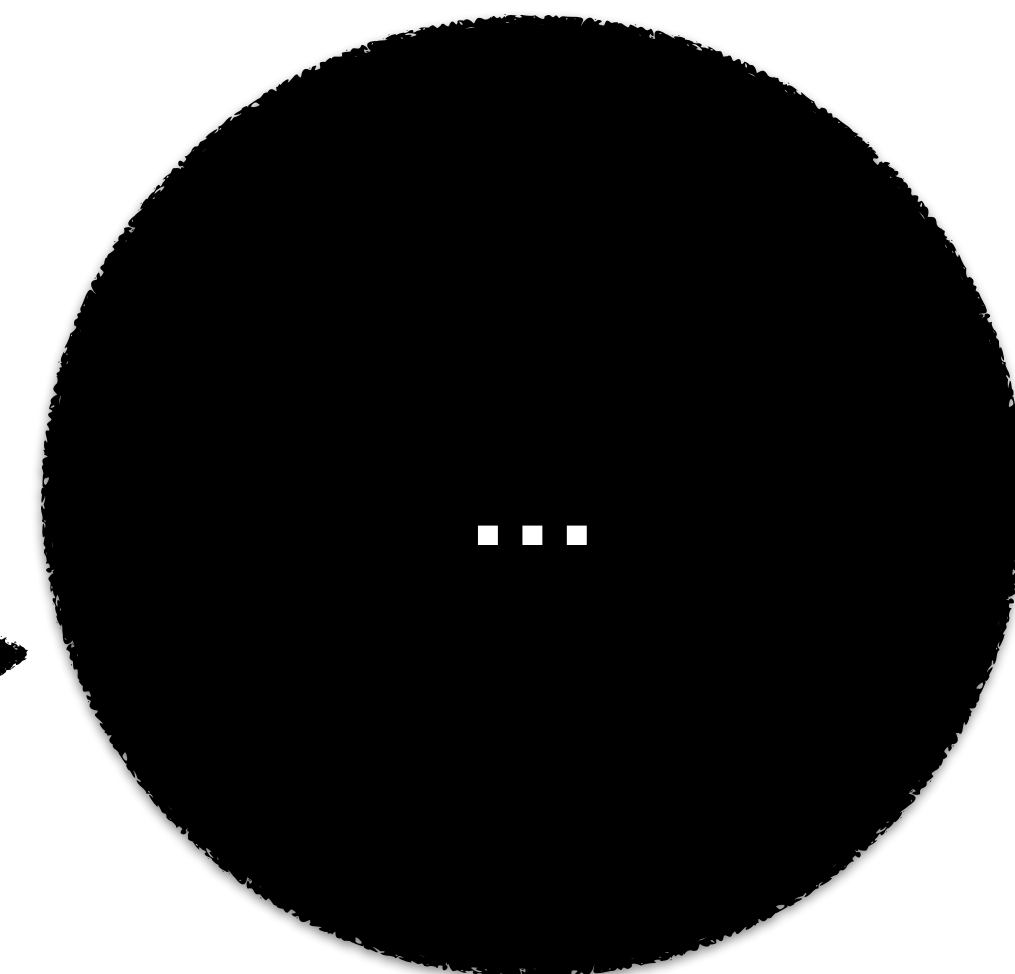
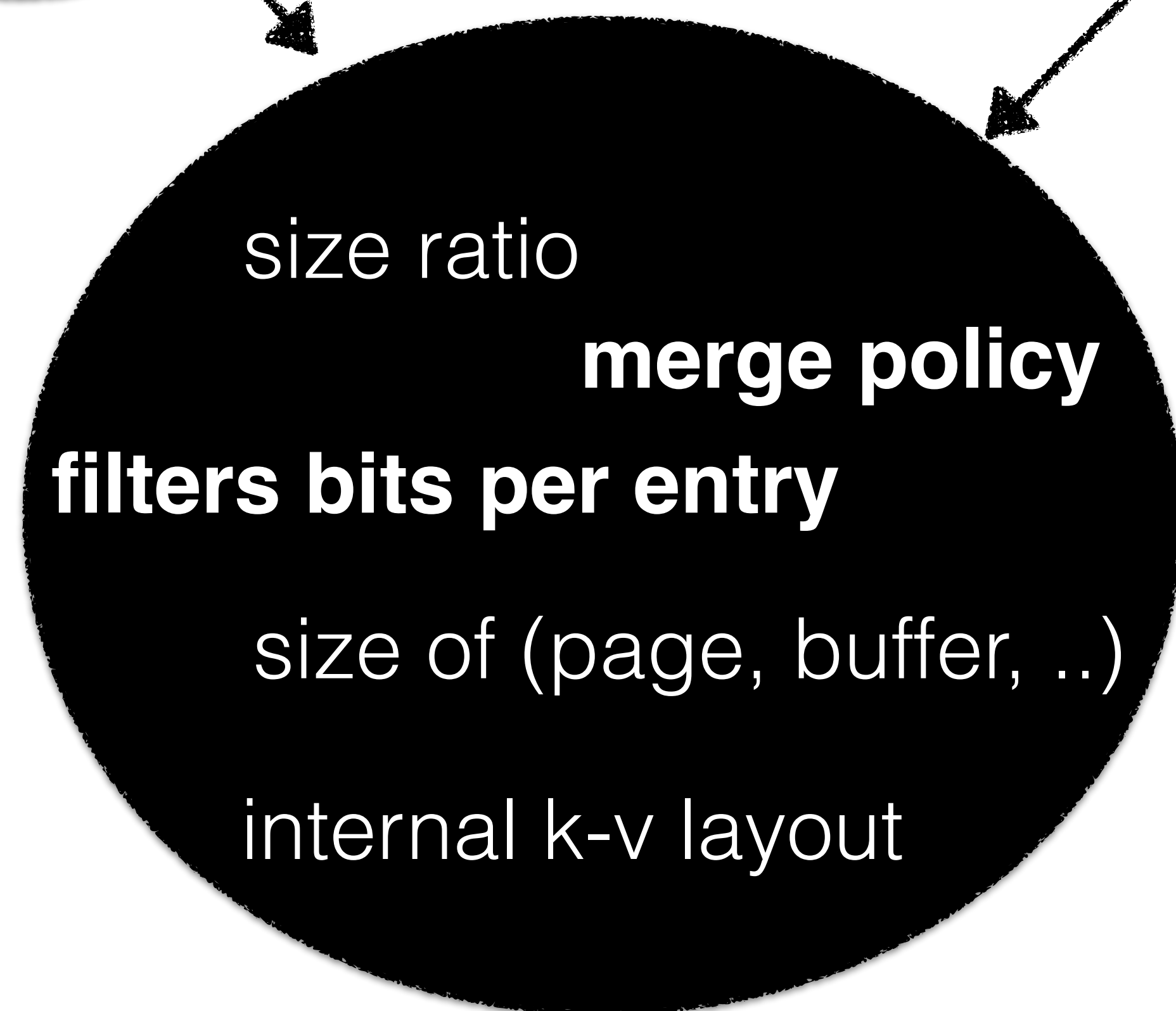
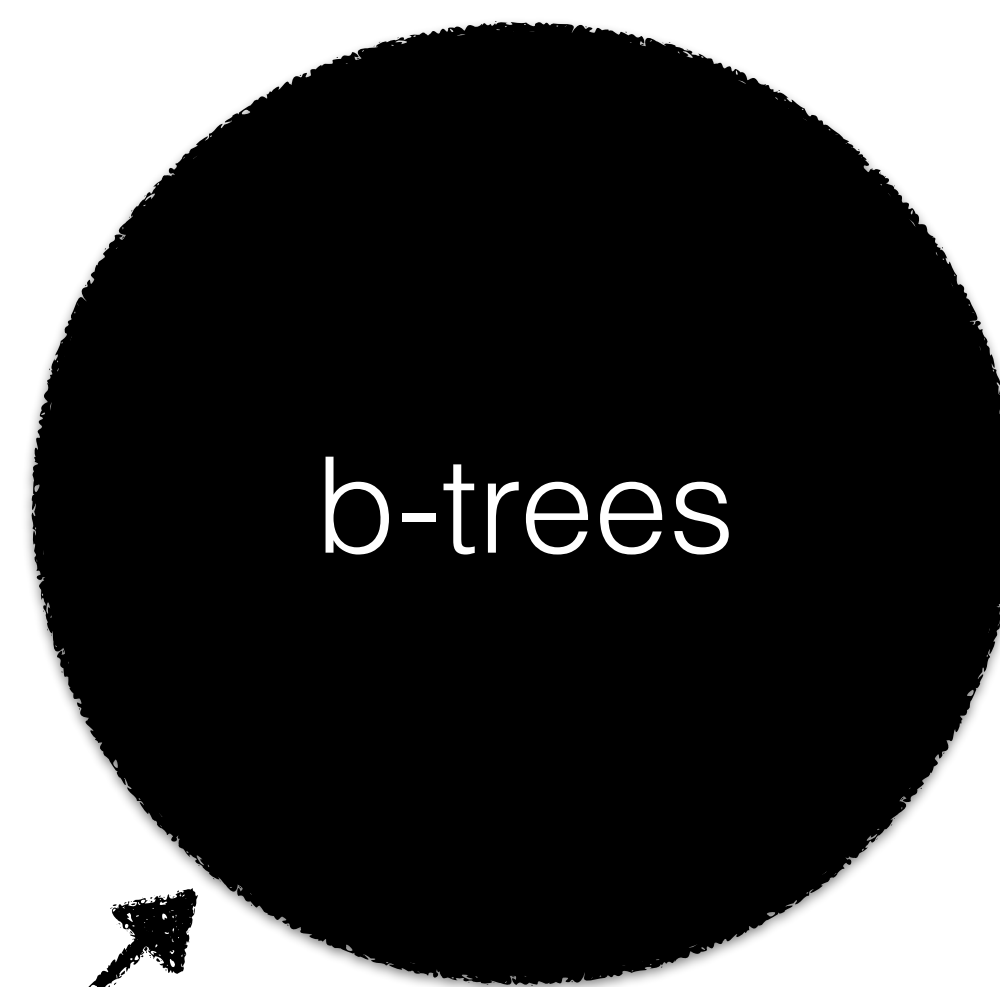
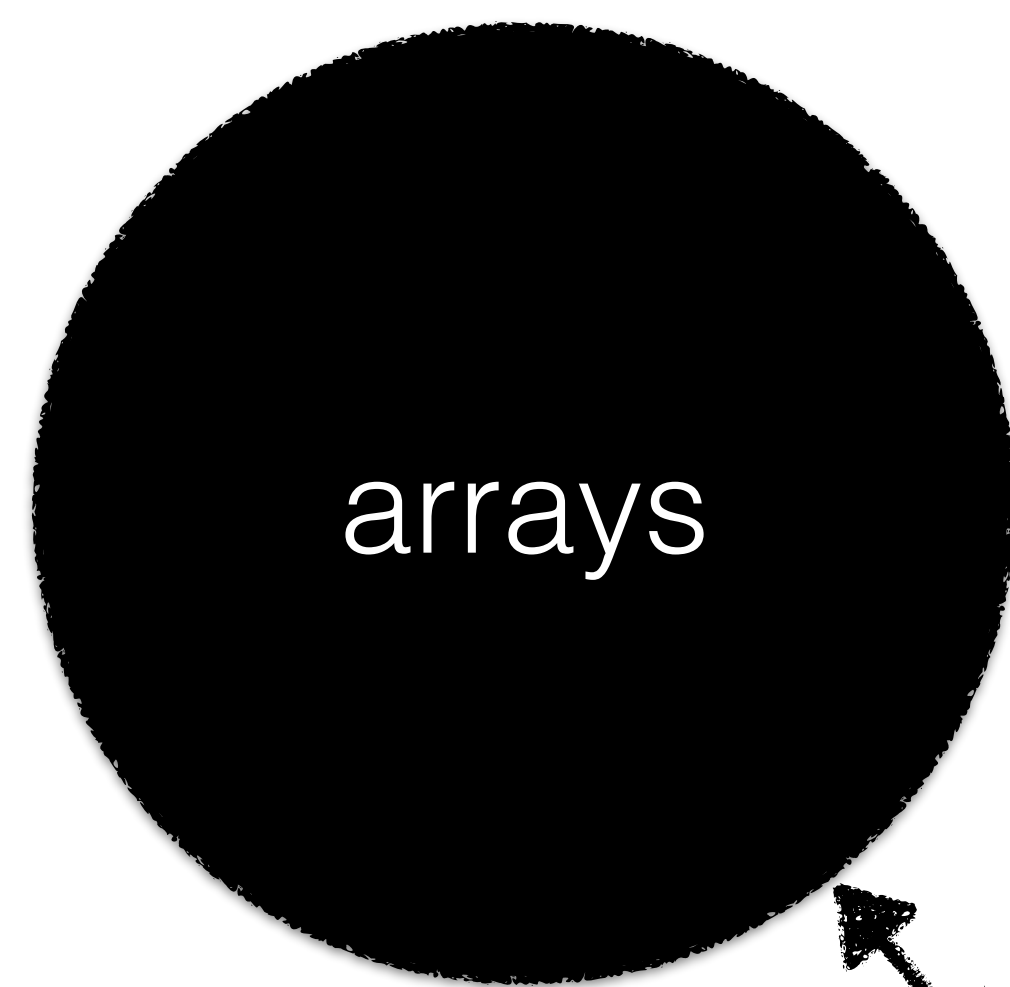
*hybrid merge policy  
tiered for small levels*

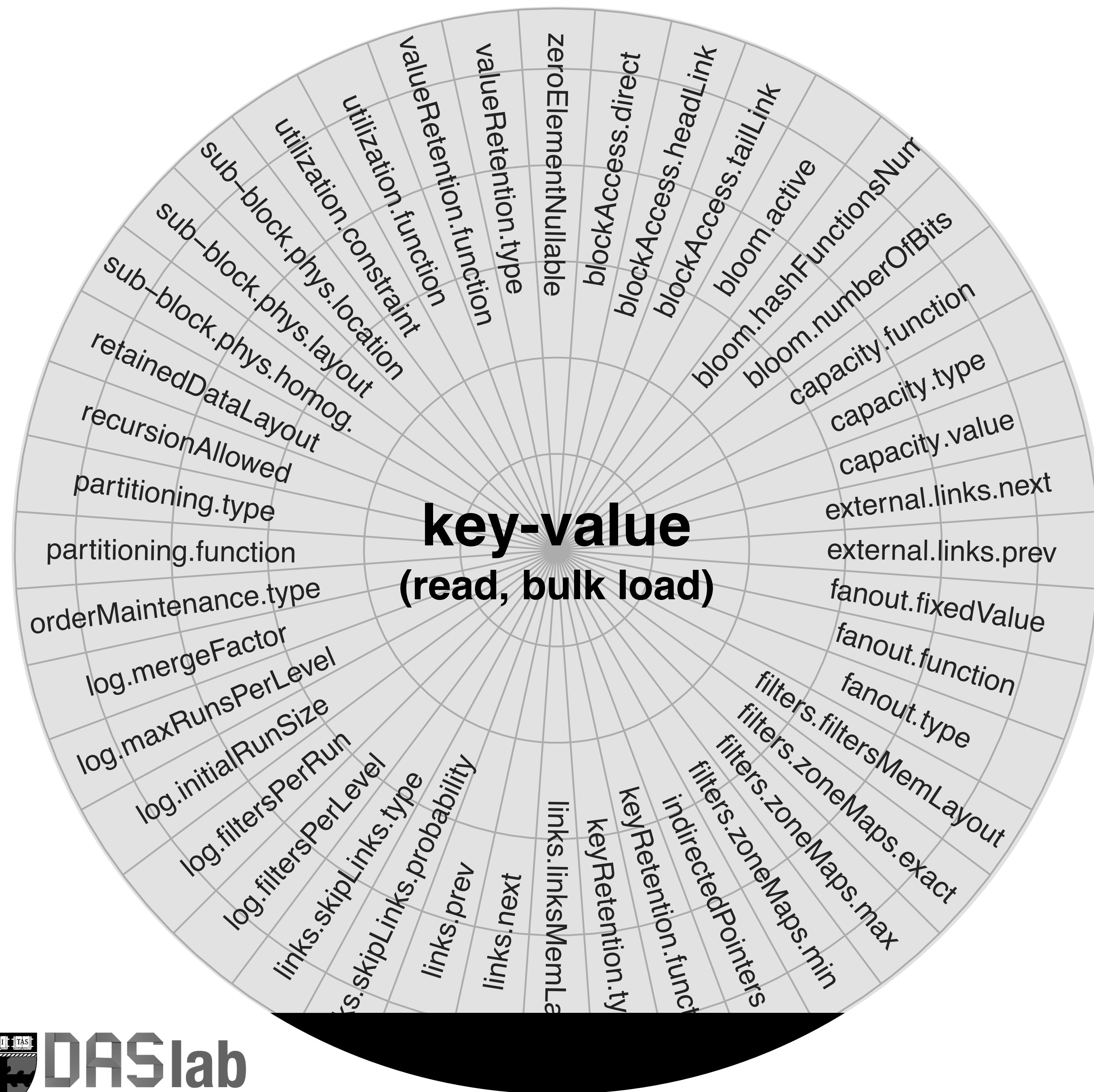


Bits per entry in bloom filter







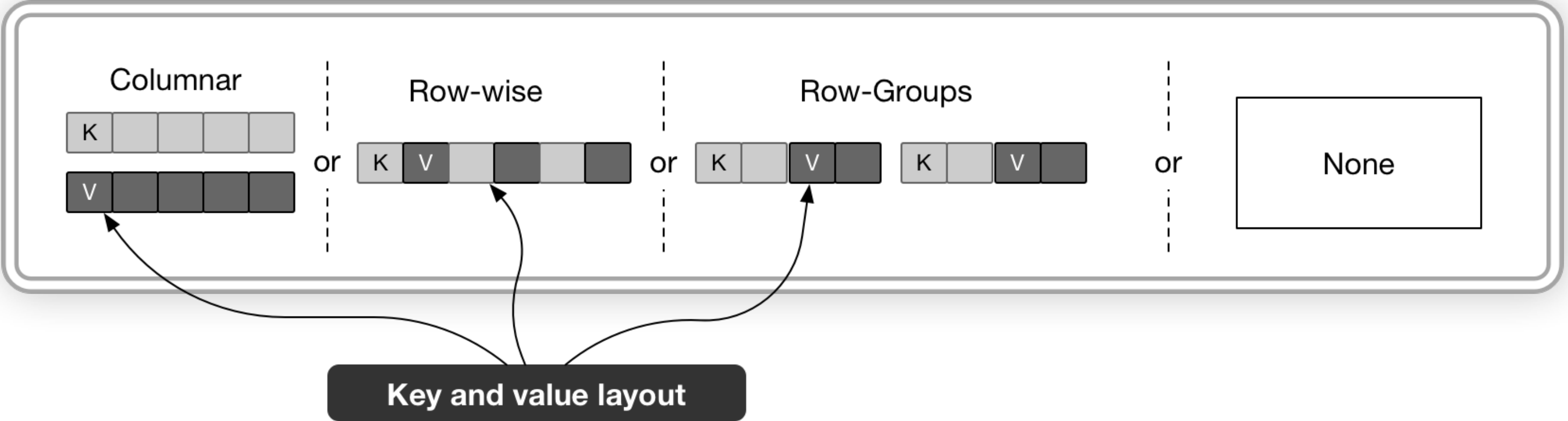


DESCRIBE ONE DATA BLOCK AT A TIME  
AS A SET OF CONCEPTS  
physical layout and domain partitioning

**Are keys retained?** (yes, no, function)

**Are values retained?**

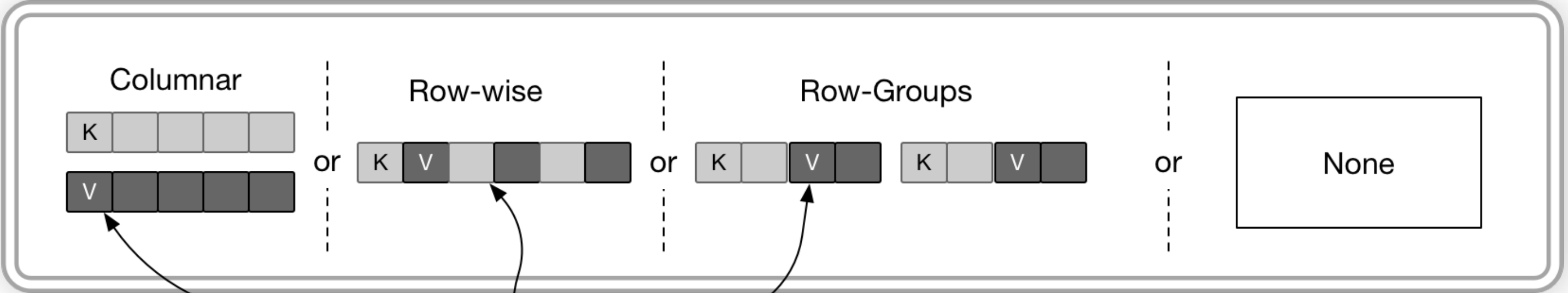
**Utilization?** (e.g., >50%)



**Are keys retained?** (yes, no, function)

**Are values retained?**

**Utilization?** (e.g., >50%)

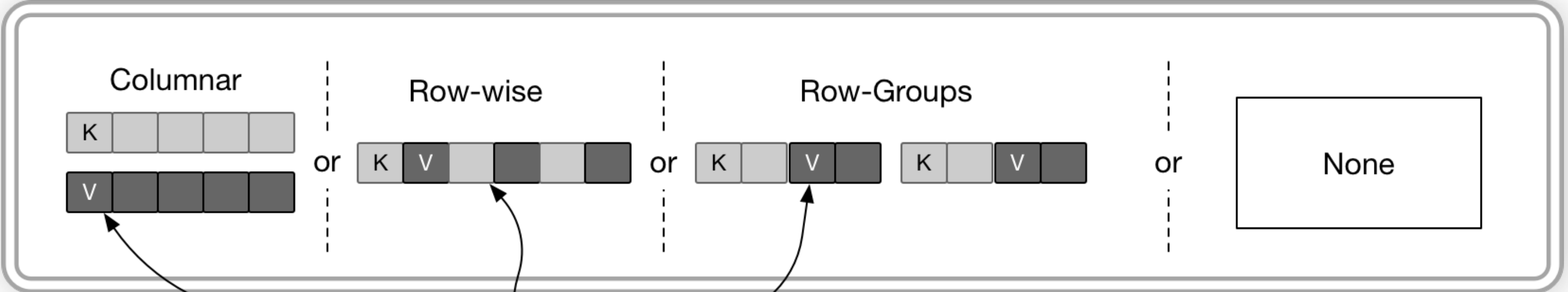


Key and value layout

**Fanout** (fixed/functional | unlimited | terminal |)

**Key partitioning** (none(fw-append | bw-append) | sorted | range() | radix() | function (func) | temporal(...))

**Are keys retained?** (yes, no, function)  
**Are values retained?**  
**Utilization?** (e.g., >50%)



Key and value layout

**Fanout** (fixed/functional | unlimited | terminal |)  
**Key partitioning** (none(fw-append | bw-append) | sorted | range() | radix() | function (func) | temporal(...))

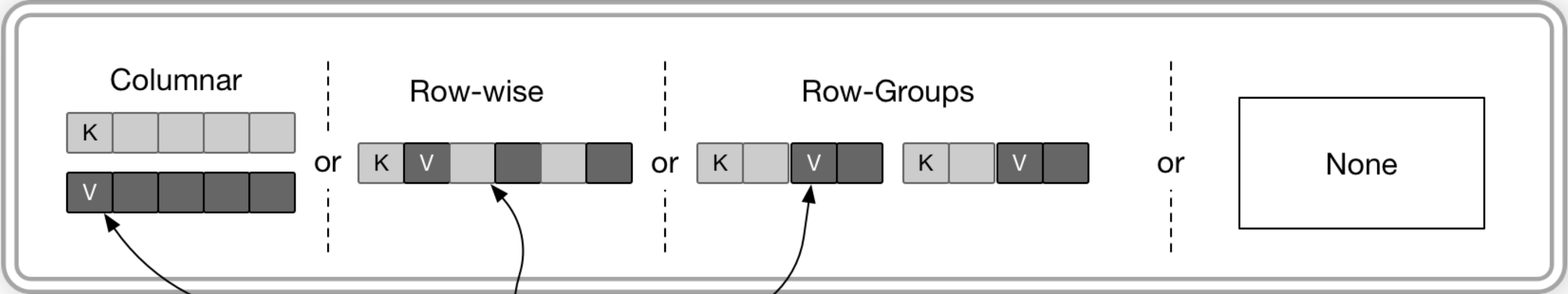
**Intra node access** (direct | head\_link | tail\_link | link\_function(func))



**Are keys retained?** (yes, no, function)

**Are values retained?**

**Utilization?** (e.g., >50%)



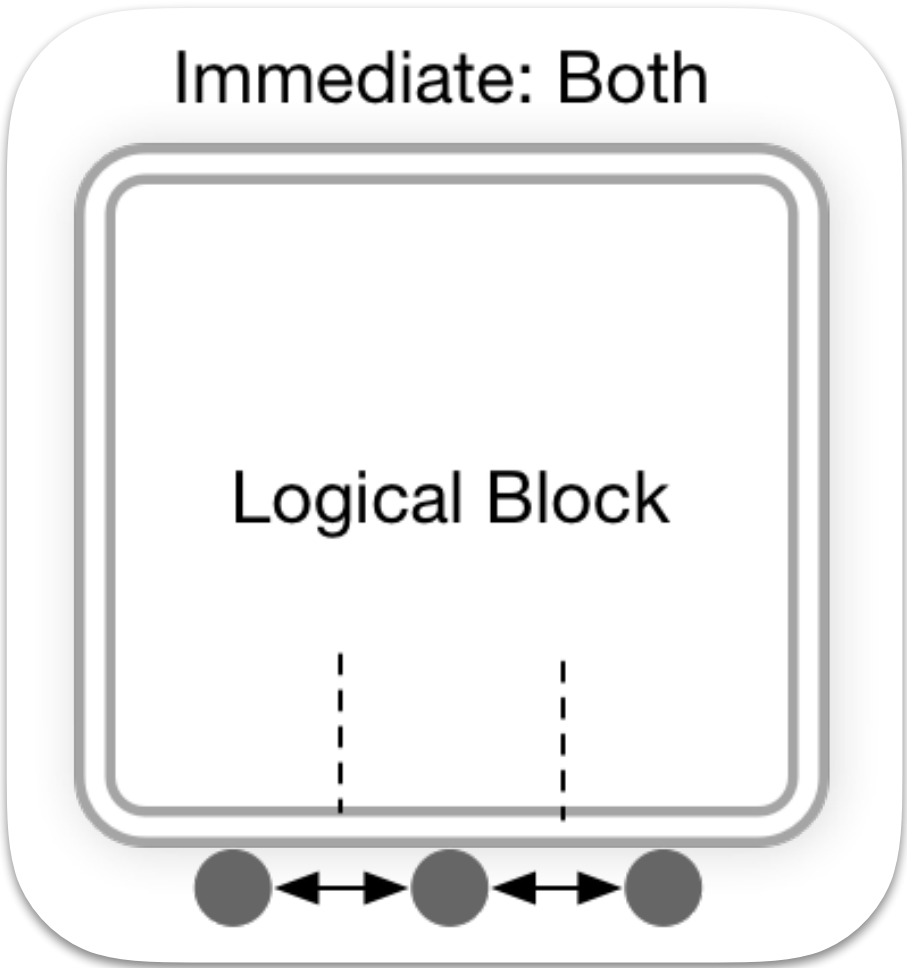
**Fanout** (fixed/functional | unlimited | terminal |)

**Key partitioning** (none(fw-append | bw-append) | sorted | range() | radix() | function (func) | temporal(...))

**Intra node access** (direct | head\_link | tail\_link | link\_function(func))

**Sub block links** (next | previous | both | none)

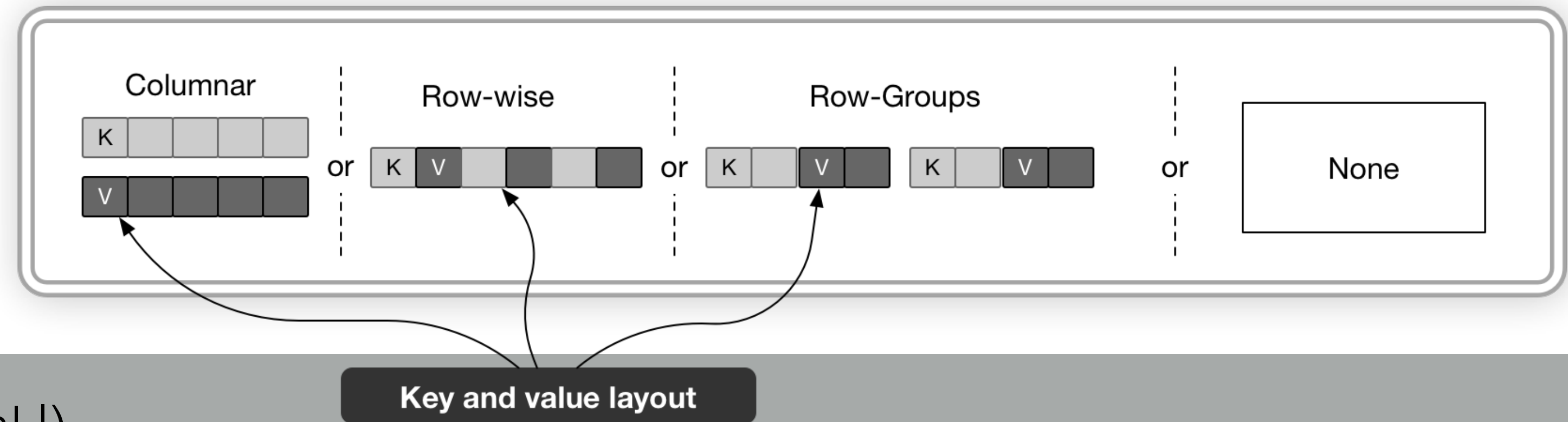
**Sub block skip links** (perfect | randomized(prob: double) | function(func) | none)



**Are keys retained?** (yes, no, function)

**Are values retained?**

**Utilization?** (e.g., >50%)



**Fanout** (fixed/functional | unlimited | terminal |)

**Key partitioning** (none(fw-append | bw-append) | sorted | range() | radix() | function (func) | temporal(...))

**Intra node access** (direct | head\_link | tail\_link | link\_function(func))

**Sub block links** (next | previous | both | none)

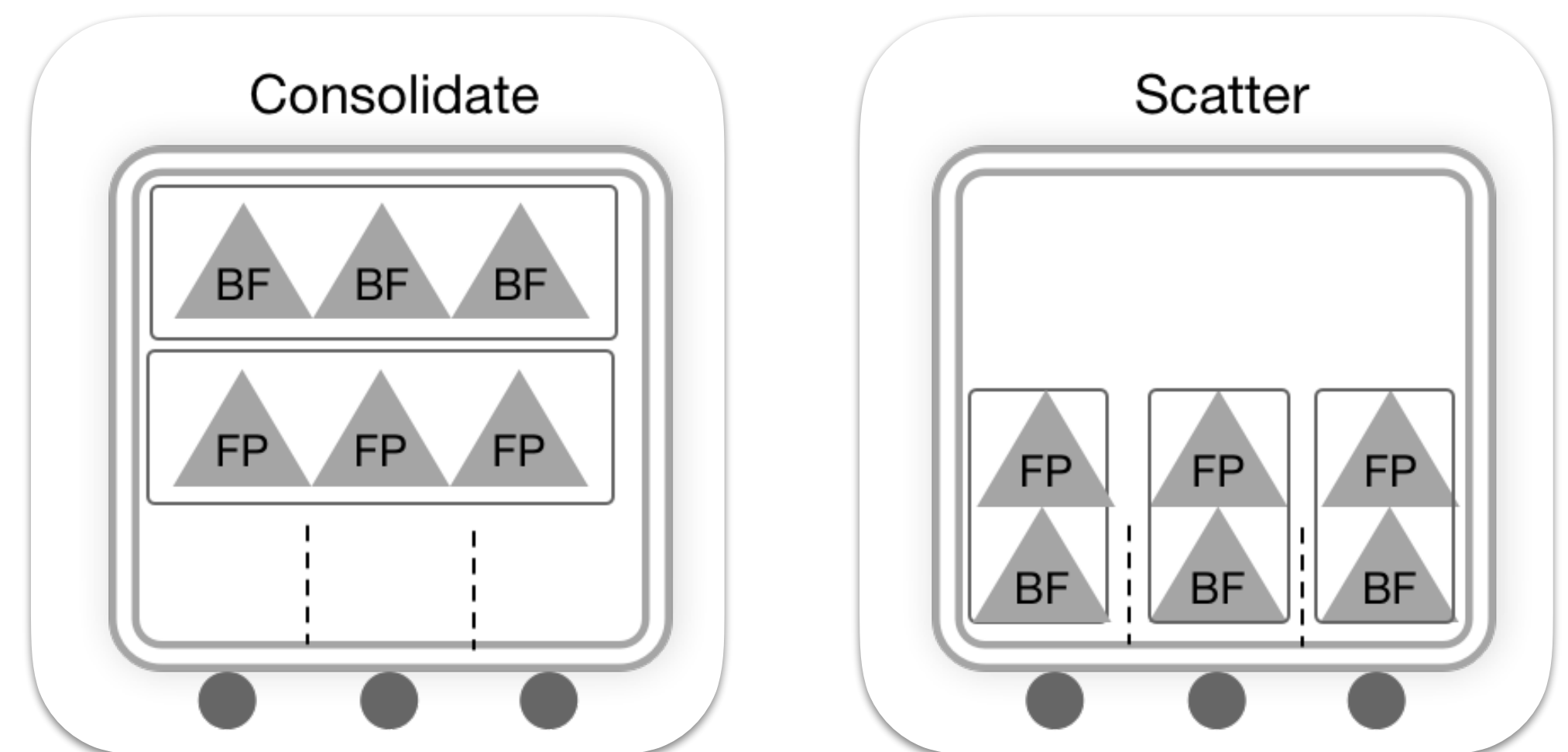
**Sub block skip links** (perfect | randomized(prob: double) | function(func) | none)

**Zone Maps** (min | max | both | exact | off)

**Bloom filters** (off | on(num\_hashes: int, num\_bits: int))

**Filters layout** (consolidate | scatter)

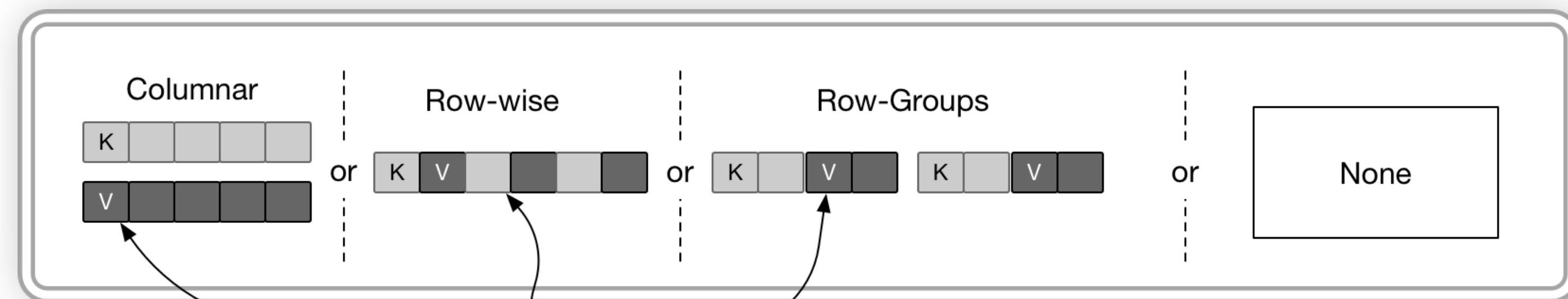
**Links layout** (consolidate | scatter)



**Are keys retained?** (yes, no, function)

**Are values retained?**

**Utilization?** (e.g., >50%)



Key and value layout

**Fanout** (fixed/functional | unlimited | terminal |)

**Key partitioning** (none(fw-append | bw-append) | sorted | range() | radix() | function (func) | temporal(...))

**Intra node access** (direct | head\_link | tail\_link | link\_function(func))

**Sub block links** (next | previous | both | none)

**Sub block skip links** (perfect | randomized(prob: double) | function(func) | none)

**Zone Maps** (min | max | both | exact | off)

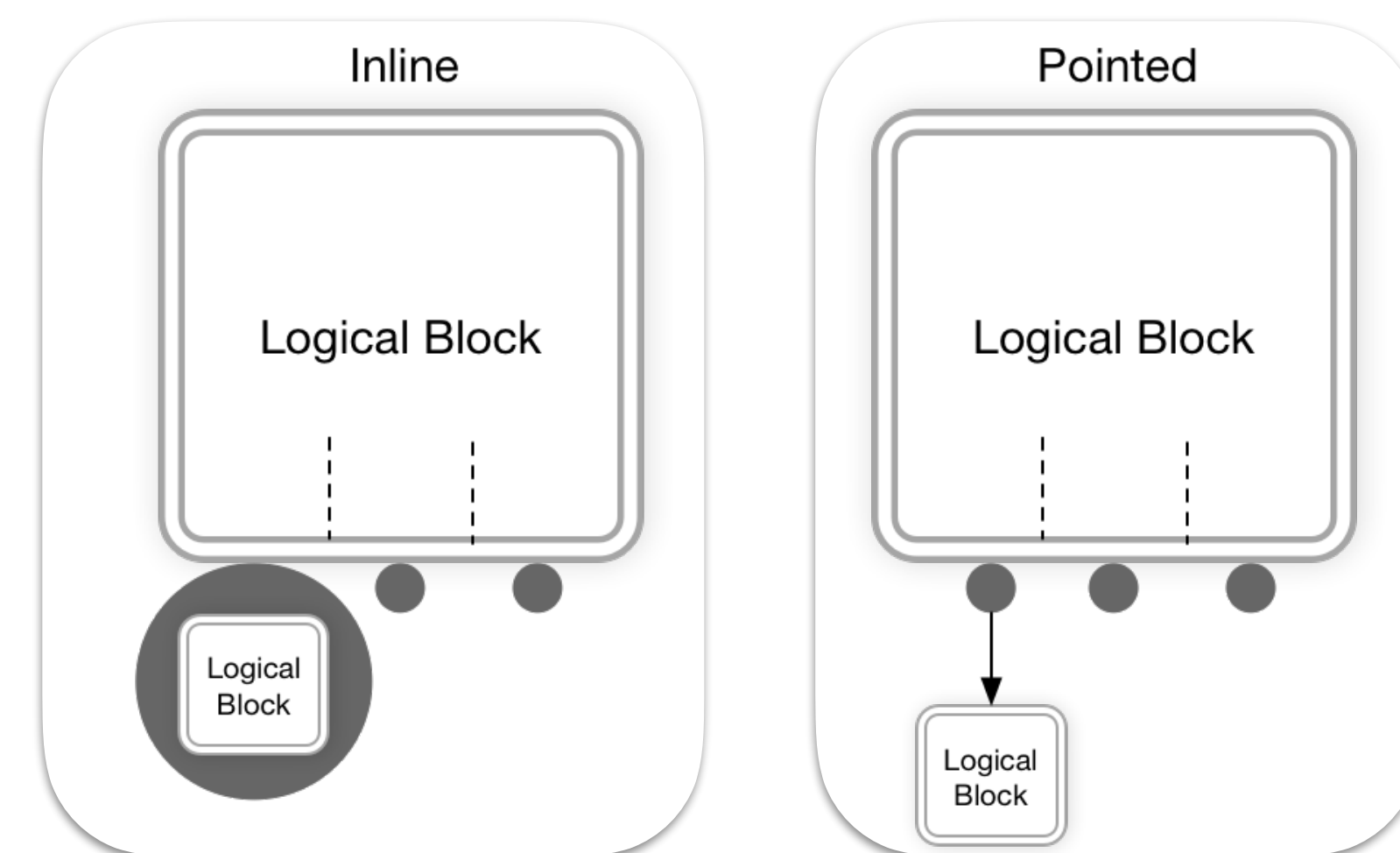
**Bloom filters** (off | on(num\_hashes: int, num\_bits: int))

**Filters layout** (consolidate | scatter)

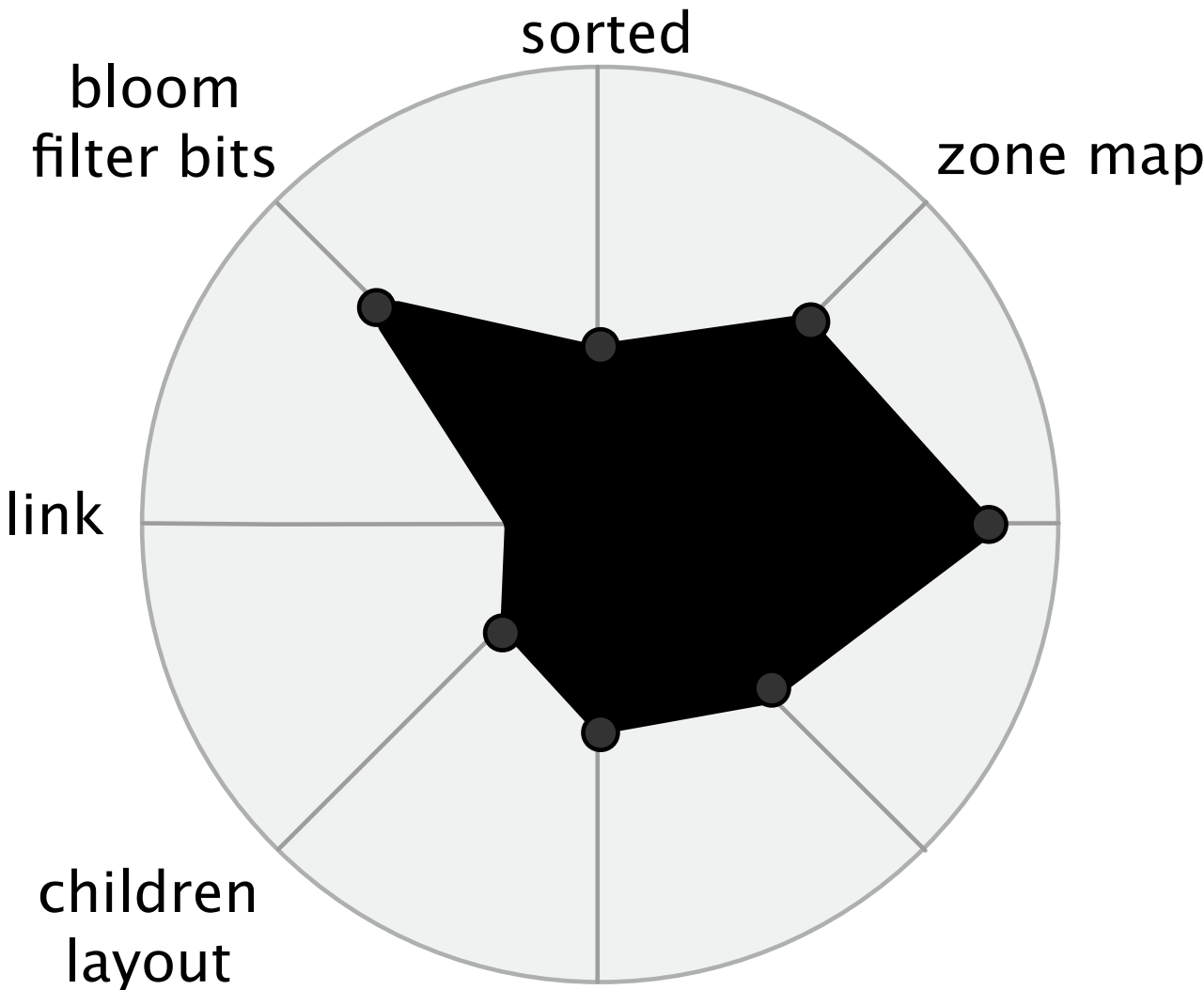
**Links layout** (consolidate | scatter)

**Physical location** (inline | pointed | double- pointed)

**Physical layout** (BFS | scatter)

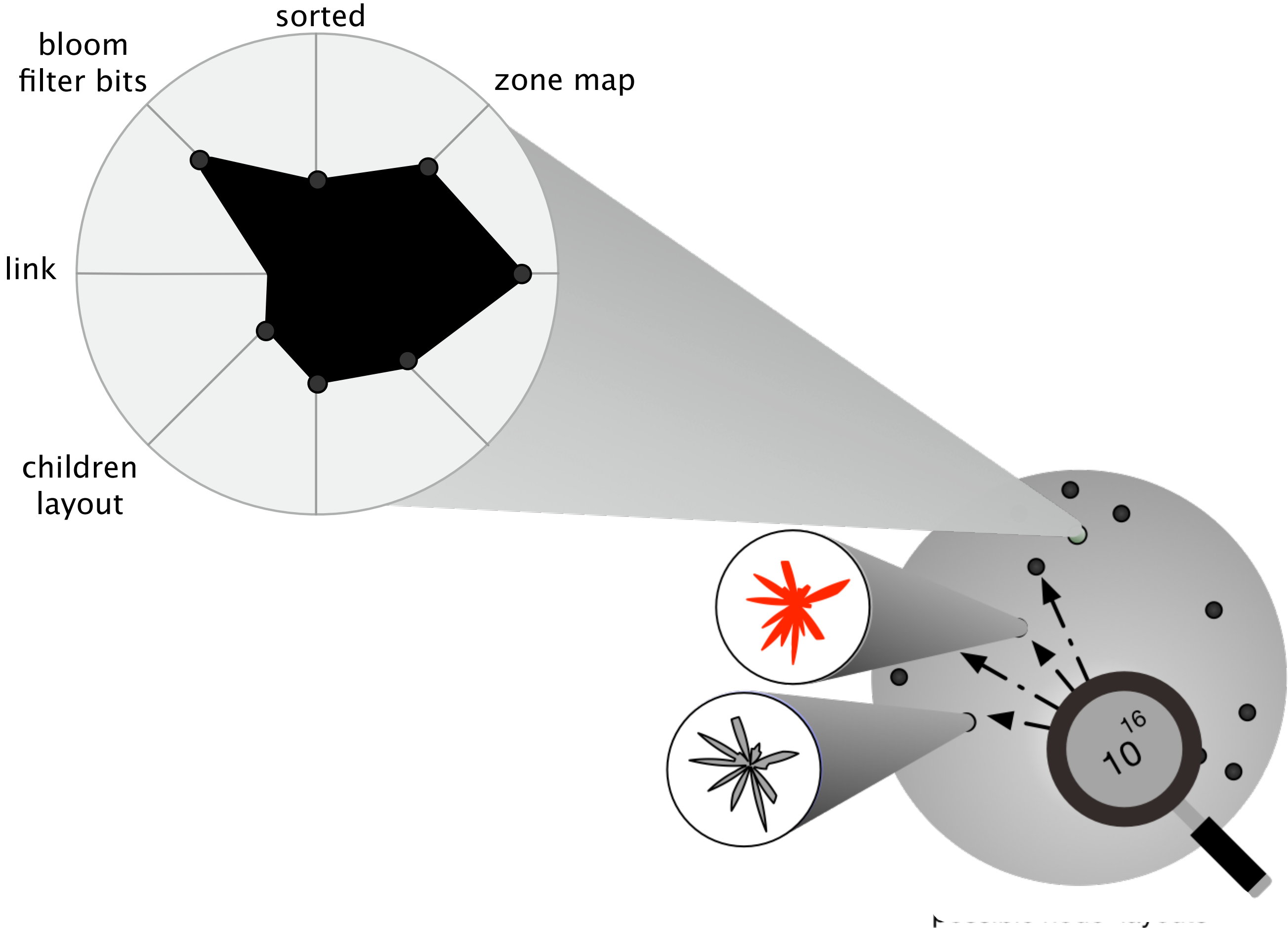


# SETS OF CONCEPTS





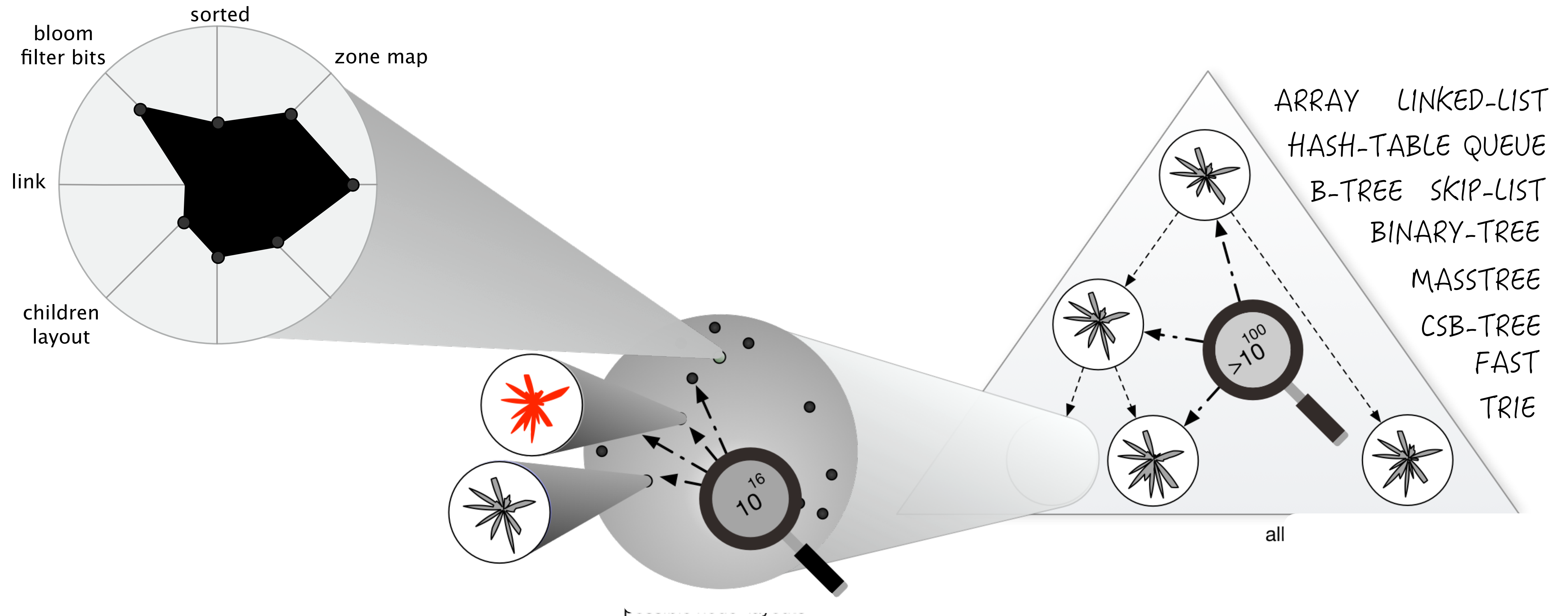
# SETS OF CONCEPTS      POSSIBLE NODE DESIGNS



# SETS OF CONCEPTS

# POSSIBLE NODE DESIGNS

# POSSIBLE STRUCTURES

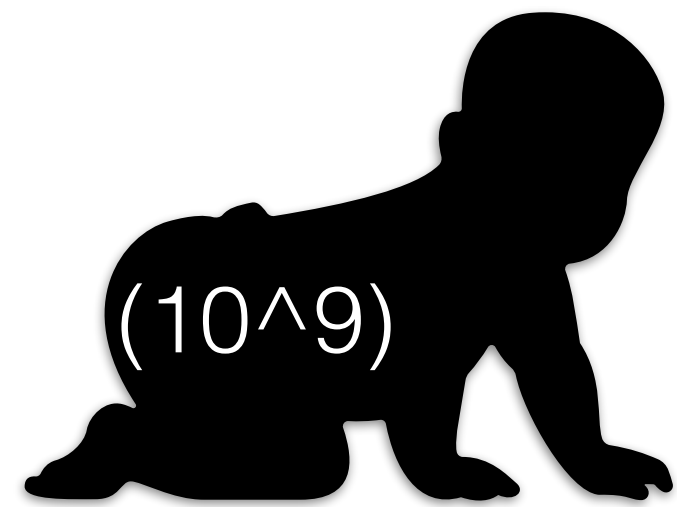


# the periodic table of data structures

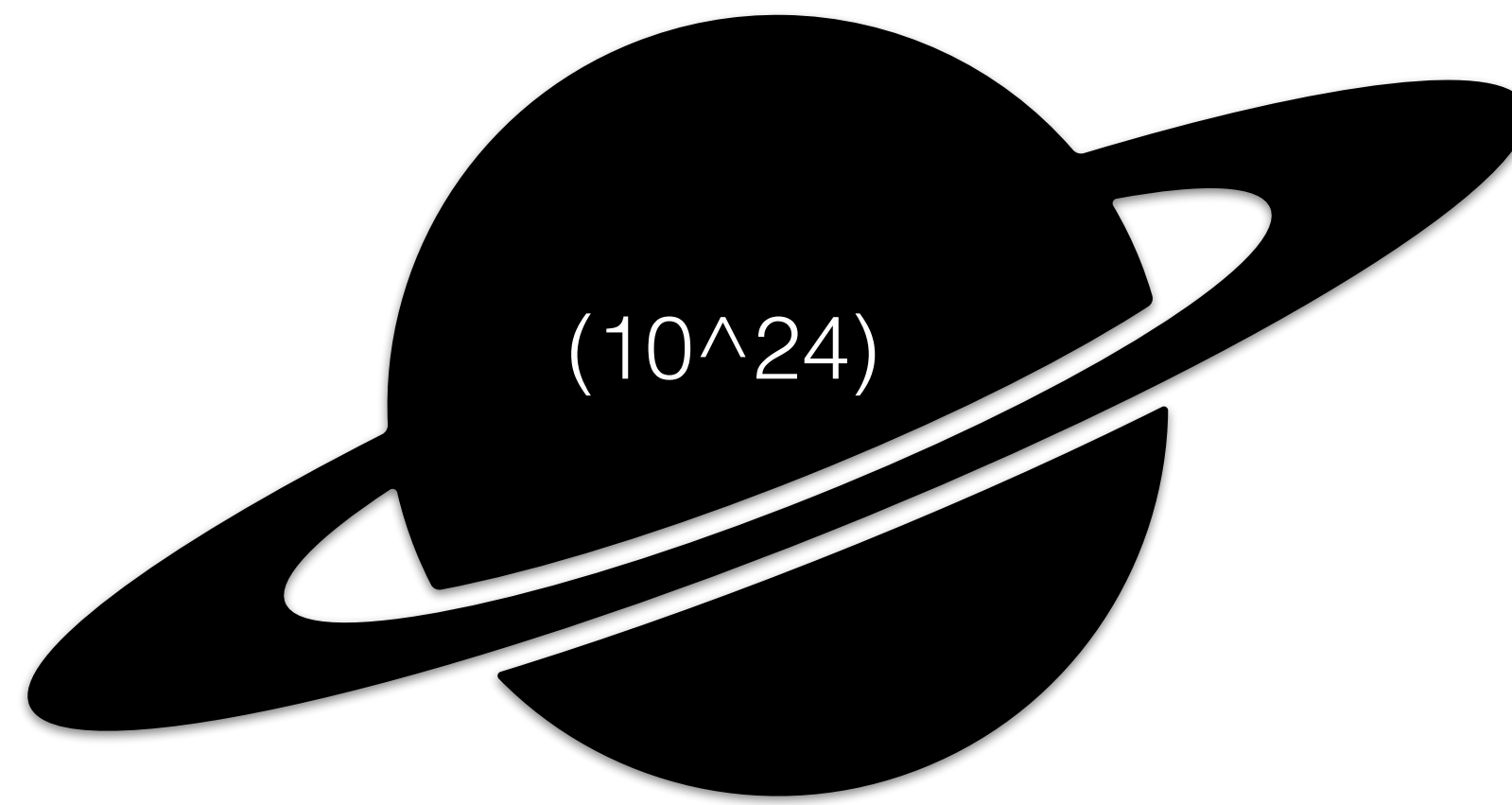
		the periodic table of data structures									
classes of primitives	classes of designs										
	B-trees & Variants	Tries & Variants	LSM-Trees & Variants	Differential Files	Membership Tests	Zone maps & Variants	Bitmaps & Variants	Hashing	Base Data & Columns		
	Partitioning	DONE	DONE	DONE					DONE	DONE	↓↑↑ RUM
	Logarithmic Design	DONE	DONE	DONE							↓↓↑ RUM
	Fractional Cascading	DONE		DONE	DONE						↓↑↑ RUM
	Log-Structured	DONE		DONE	DONE						↑↓↑ RUM
	Buffering	DONE			DONE			DONE			↓♦↑ RUM
	Differential Updates	DONE			DONE						↑↓↓ RUM
	Sparse Indexing	DONE				DONE	DONE				↓♦↑ RUM
Adaptivity	DONE								DONE		



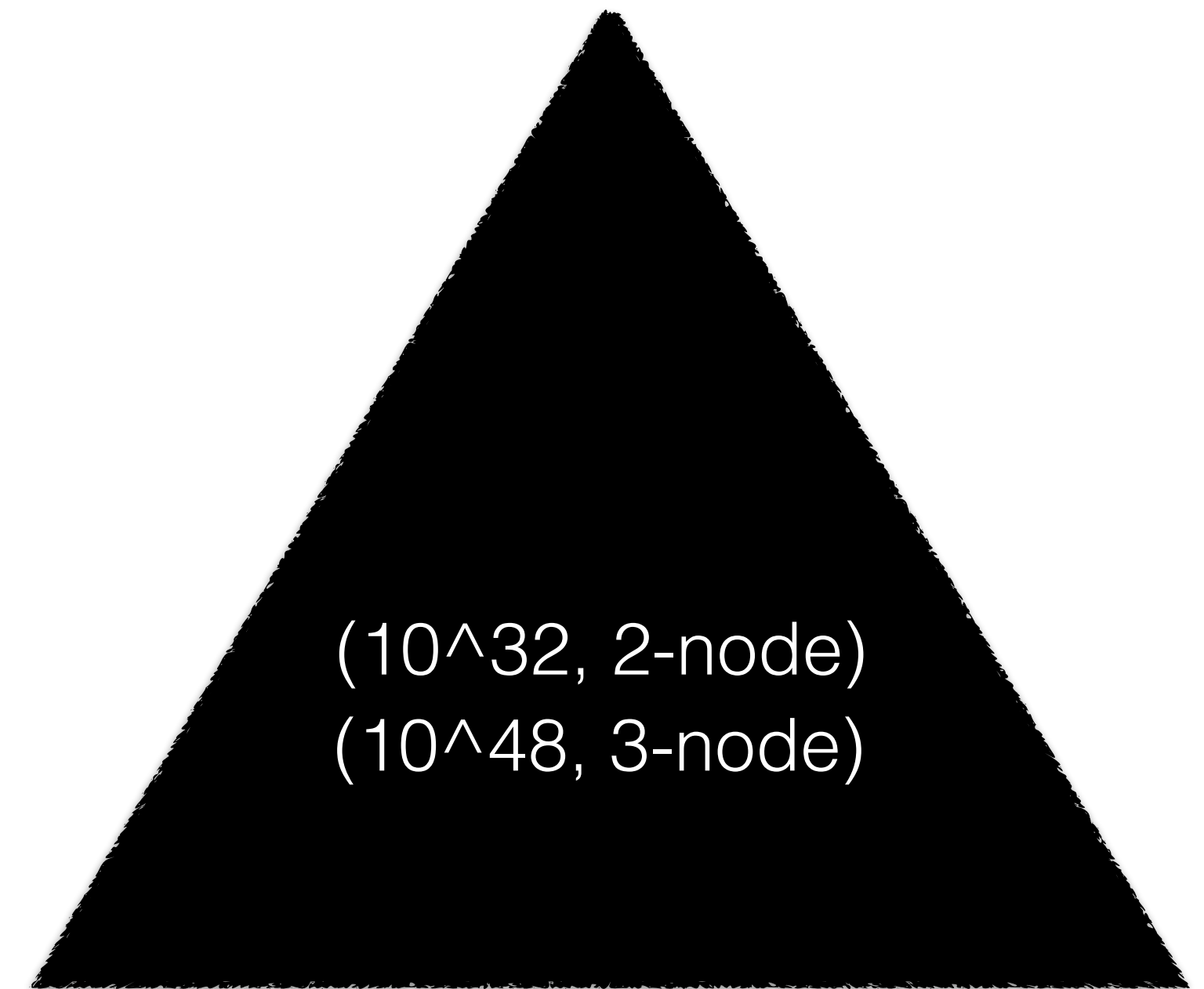




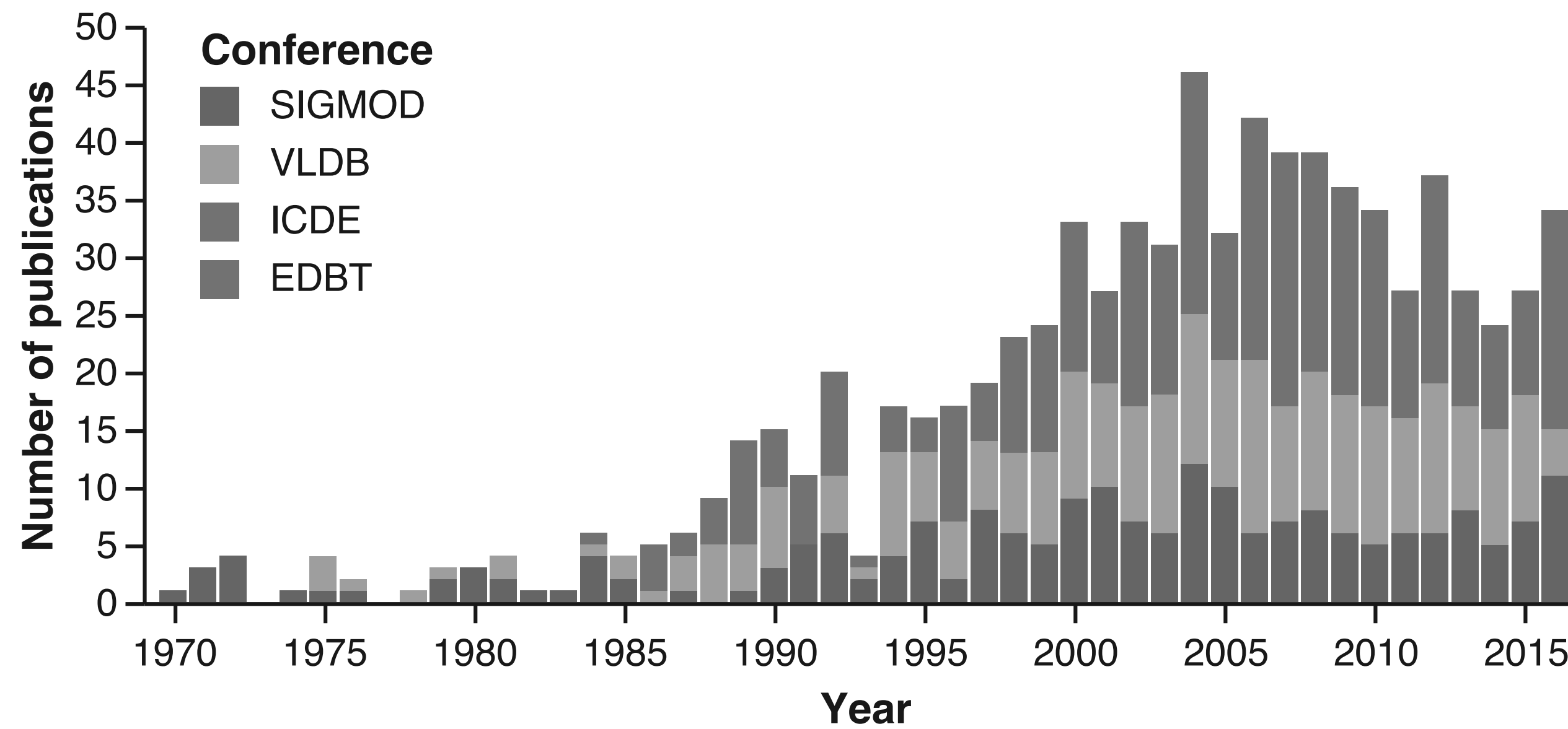
PEOPLE ON EARTH



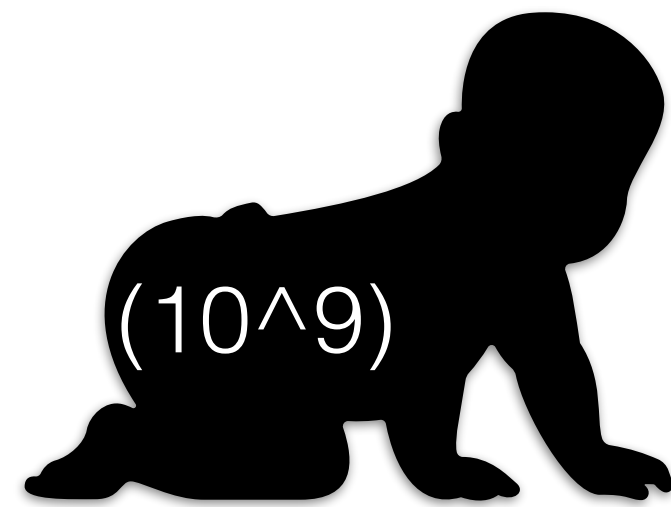
STARS IN THE SKY



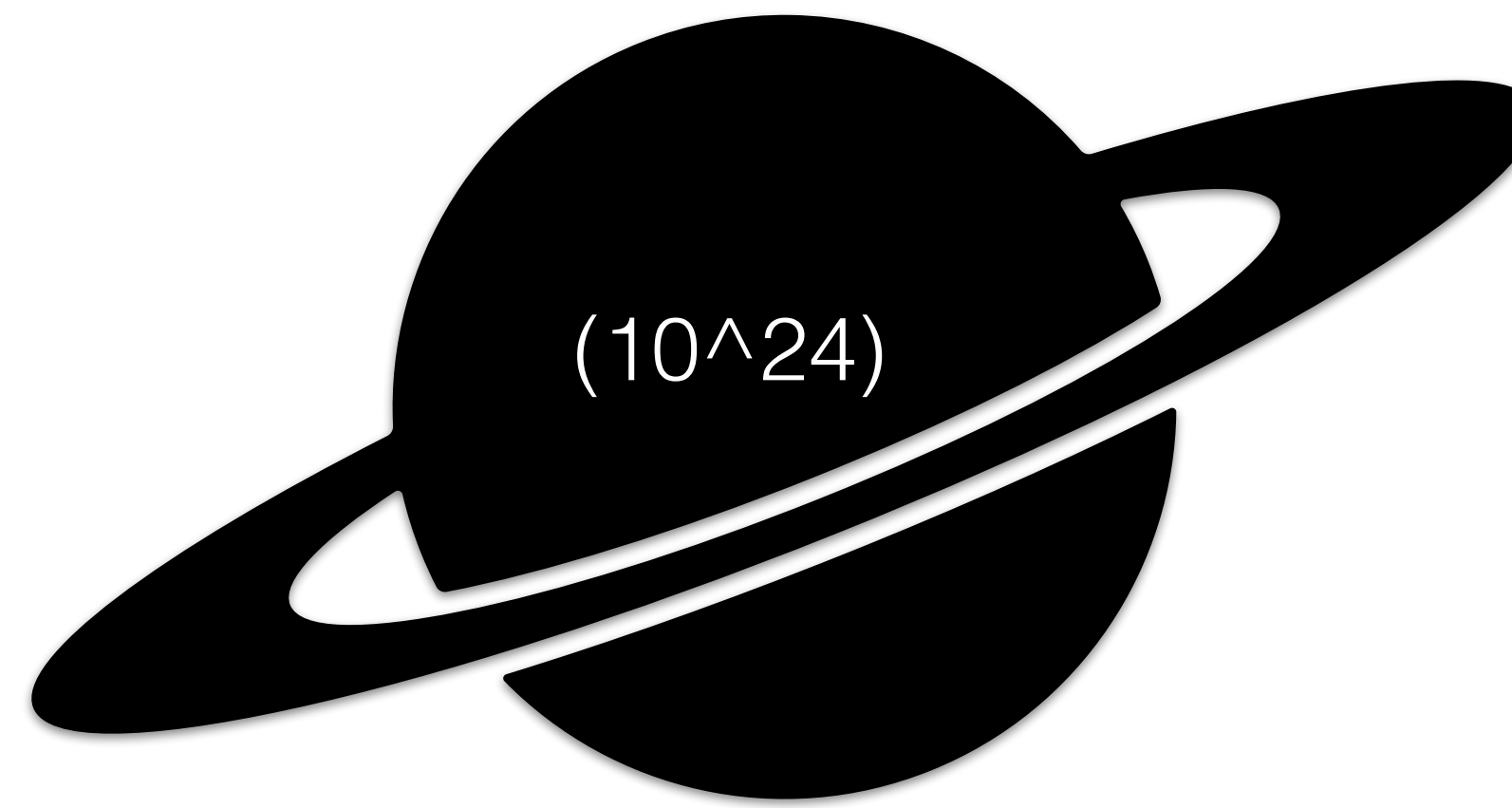
POSSIBLE DATA STRUCTURES



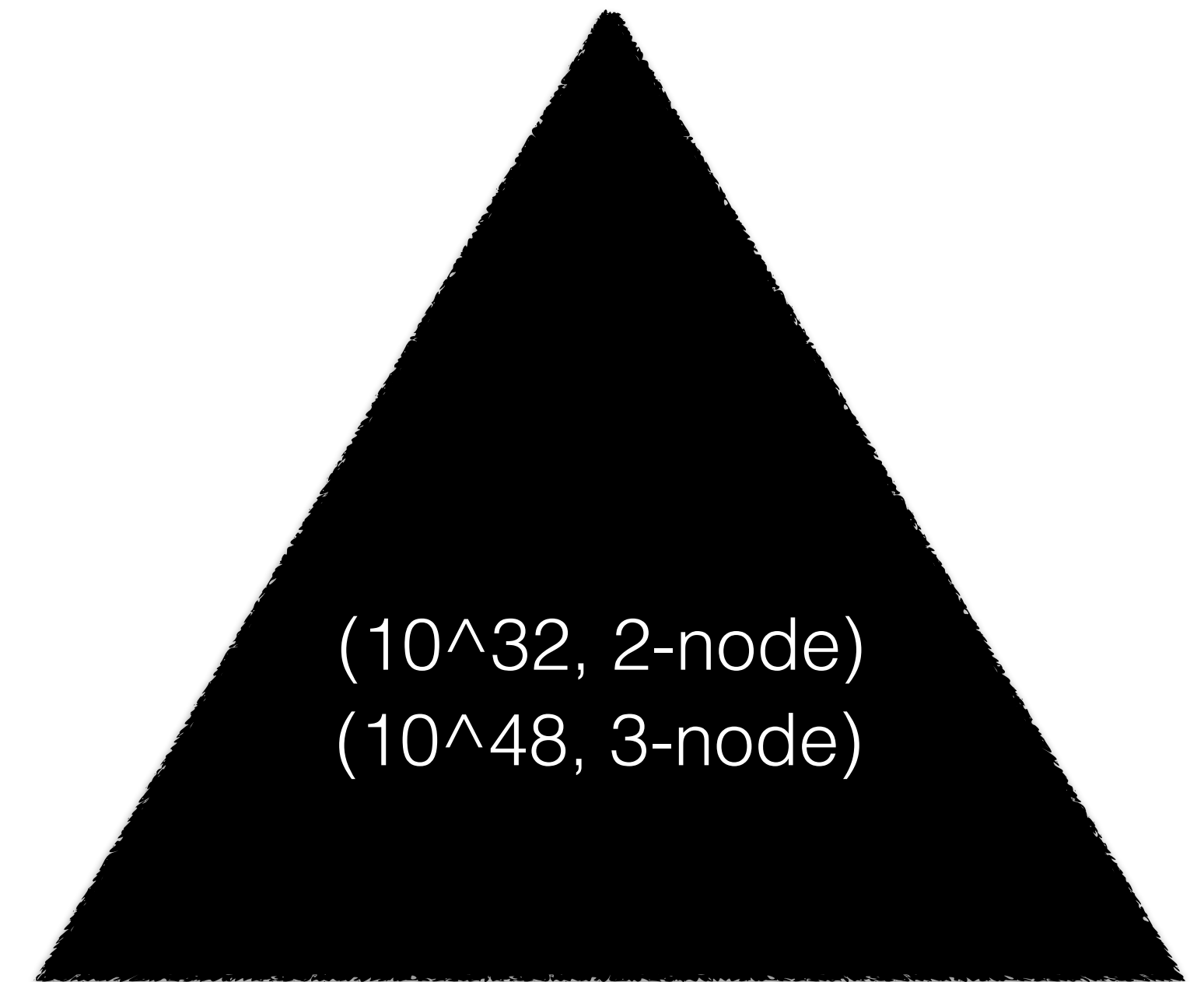
~5K since the dawn of CS



PEOPLE ON EARTH



STARS IN THE SKY

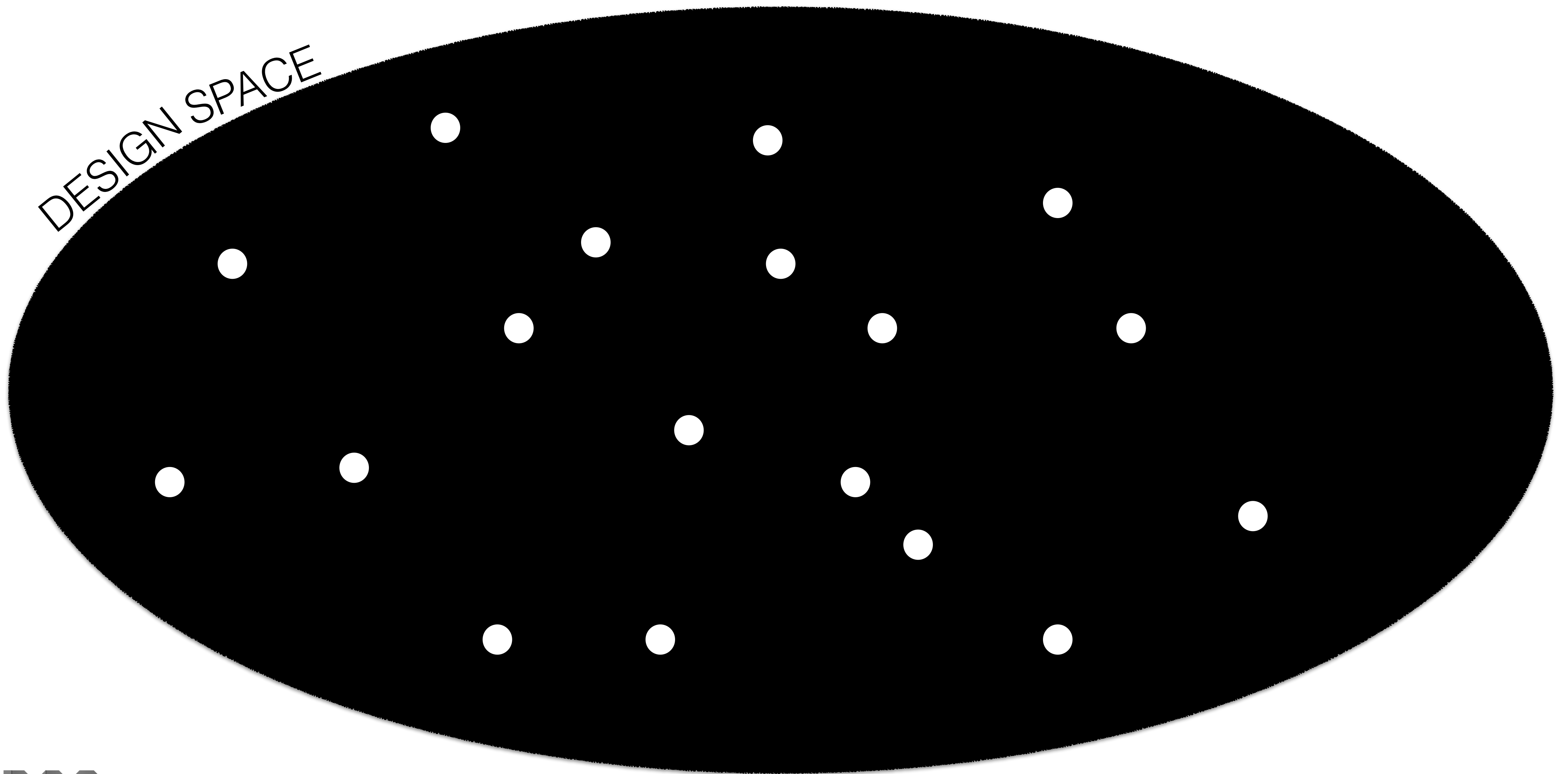


POSSIBLE DATA STRUCTURES

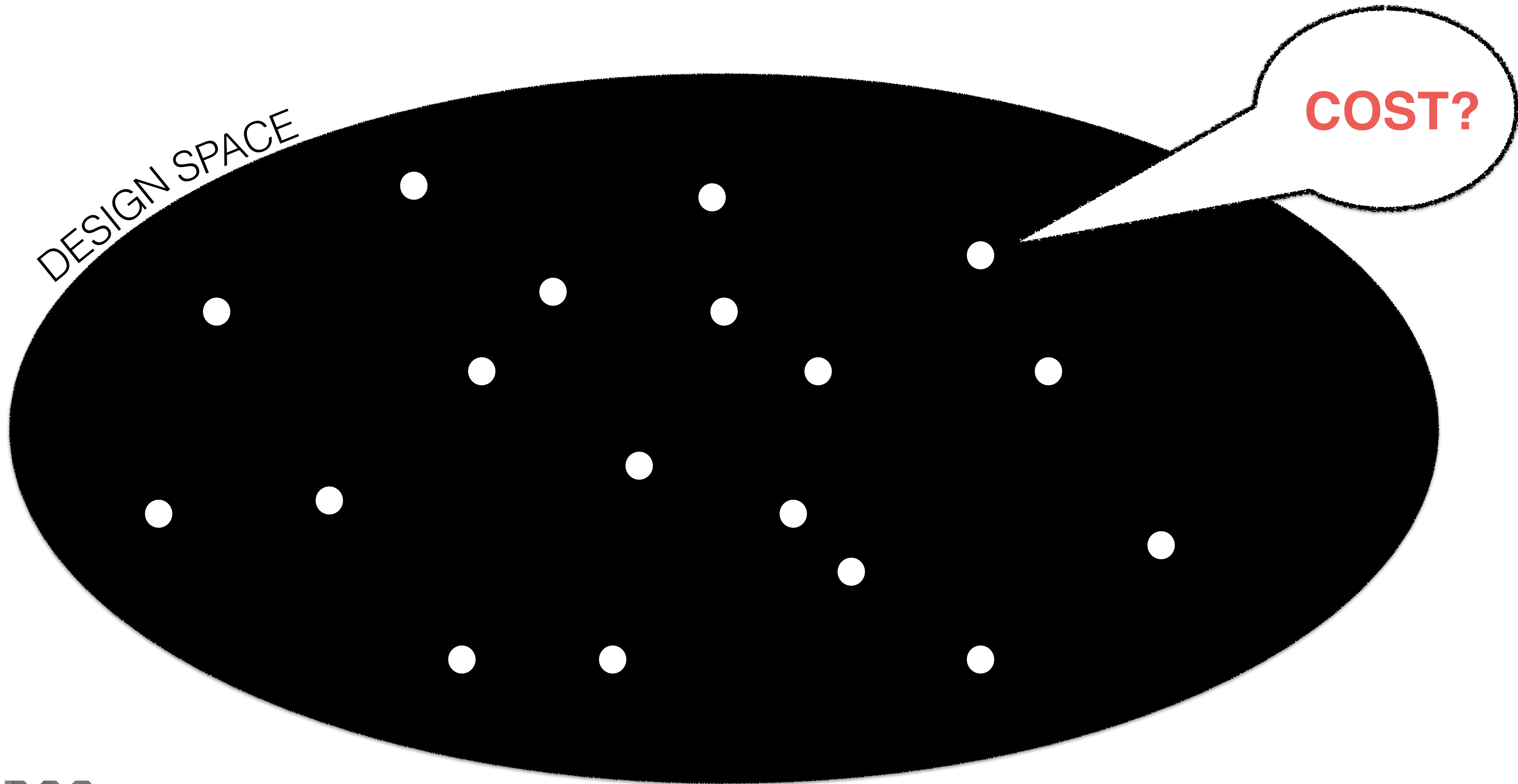
## DESIGN SPACE

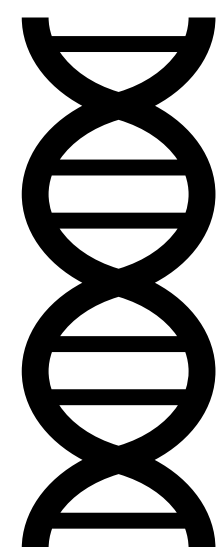
if we know the fundamental building blocks,  
how they combine and their **properties**,

then we can automate the discovery of  
novel combinations and tunings

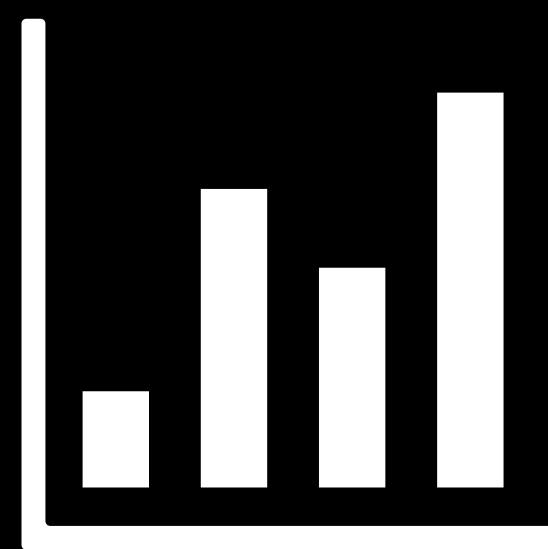




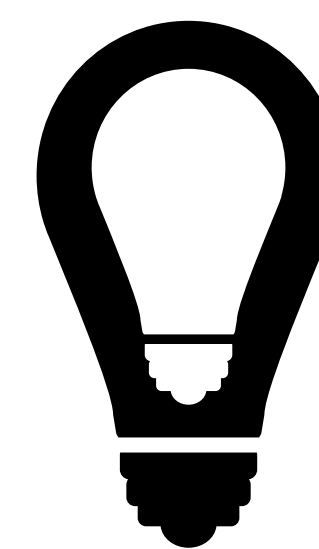




DESIGN SPACE



COST SYNTHESIS



HOW TO USE

# HOW TO JUDGE A DESIGN?



**1**

**COMPLEXITY  
ANALYSIS**



**2**

**IMPLEMENTATION  
& TESTING**

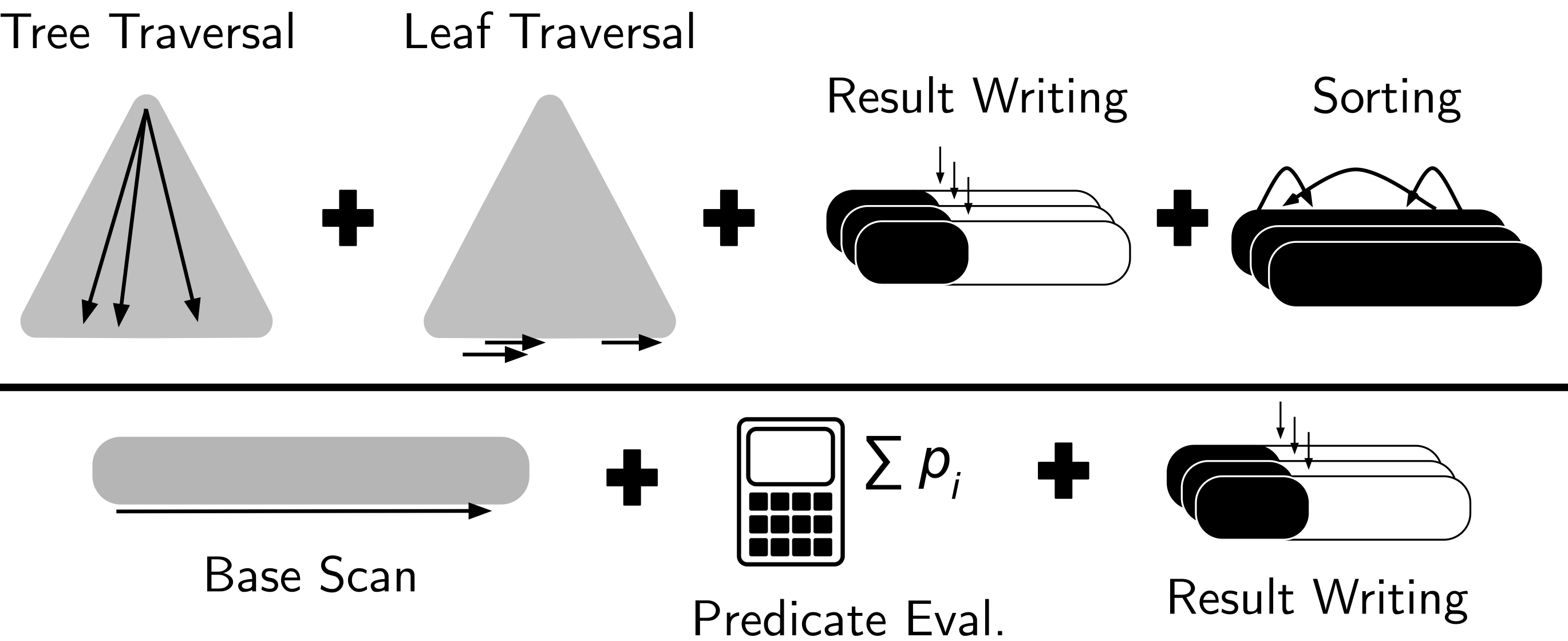


**3**

**GENERALIZED  
MODELS**

# HARD & SLOW

Access path selection @SIGMOD2017

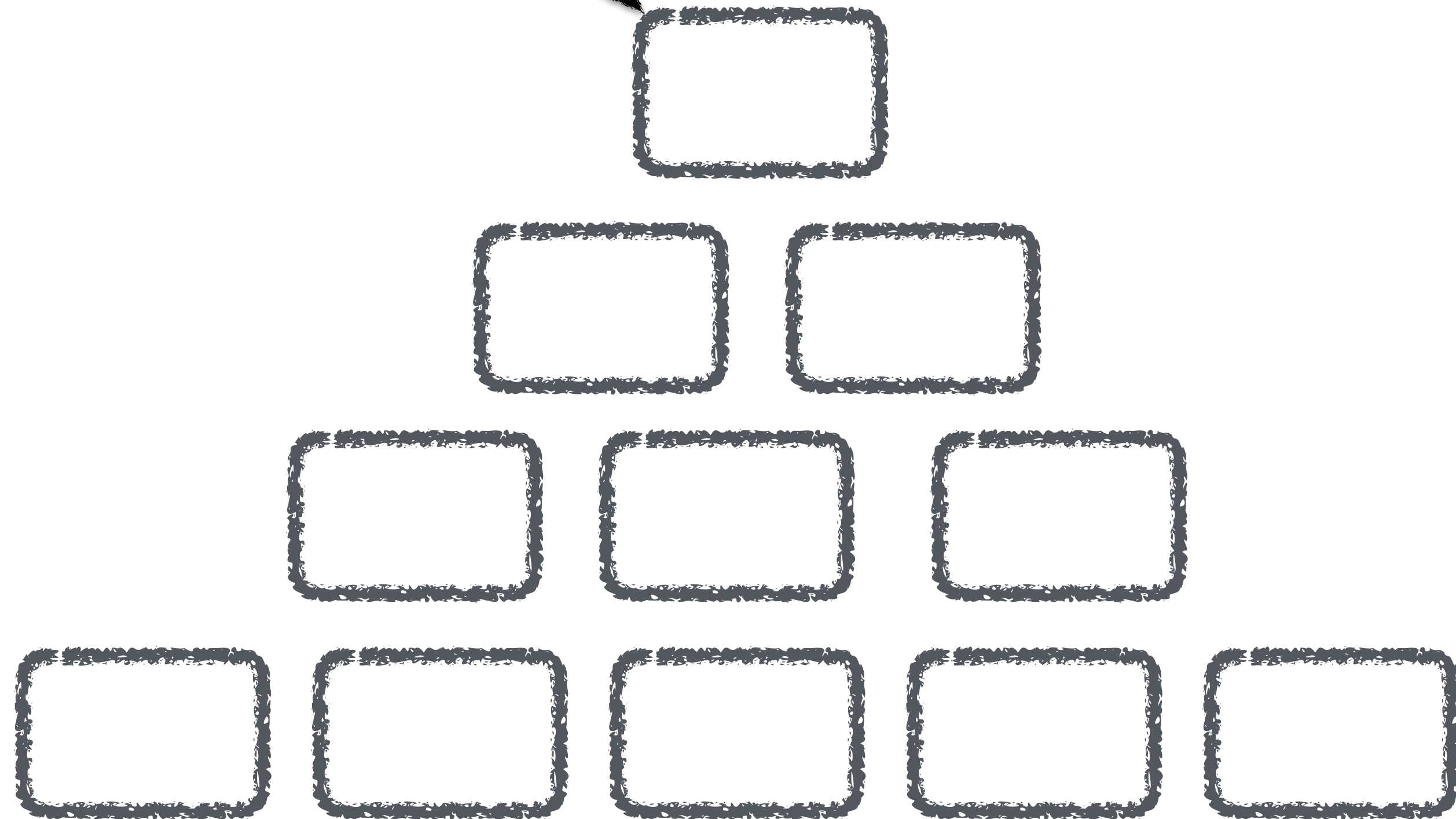


$$\begin{aligned}
 APS(q, S_{tot}) = & \frac{q \cdot \frac{1 + \lceil \log_b(N) \rceil}{N} \cdot \left( BW_S \cdot C_M + \frac{b \cdot BW_S \cdot C_A}{2} + \frac{b \cdot BW_S \cdot f_p \cdot p}{2} \right)}{\max(ts, 2 \cdot f_p \cdot p \cdot q \cdot BW_S) + S_{tot} \cdot rw \cdot \frac{BW_S}{BW_R}} \\
 & + \frac{S_{tot} \left( \frac{BW_S \cdot C_M}{b} + (aw + ow) \cdot \frac{BW_S}{BW_I} + rw \cdot \frac{BW_S}{BW_R} \right)}{\max(ts, 2 \cdot f_p \cdot p \cdot q \cdot BW_S) + S_{tot} \cdot rw \cdot \frac{BW_S}{BW_R}} \\
 & + \frac{S_{tot} \cdot \log_2(S_{tot} \cdot N) \cdot BW_S \cdot C_A}{\max(ts, 2 \cdot f_p \cdot p \cdot q \cdot BW_S) + S_{tot} \cdot rw \cdot \frac{BW_S}{BW_R}}
 \end{aligned}$$

Workload	$q$ $s_i$ $S_{tot}$	number of queries selectivity of query $i$ total selectivity of the workload
Dataset	$N$ $ts$	data size (tuples per column) tuple size (bytes per tuple)
Hardware	$C_A$ $C_M$ $BW_S$ $BW_R$ $BW_I$ $p$ $f_p$	L1 cache access (sec) LLC miss: memory access (sec) scanning bandwidth (GB/s) result writing bandwidth (GB/s) leaf traversal bandwidth (GB/s) The inverse of CPU frequency Factor accounting for pipelining
Scan & Index	$rw$ $b$ $aw$ $ow$	result width (bytes per output tuple) tree fanout attribute width (bytes of the indexed column) offset width (bytes of the index column offset)

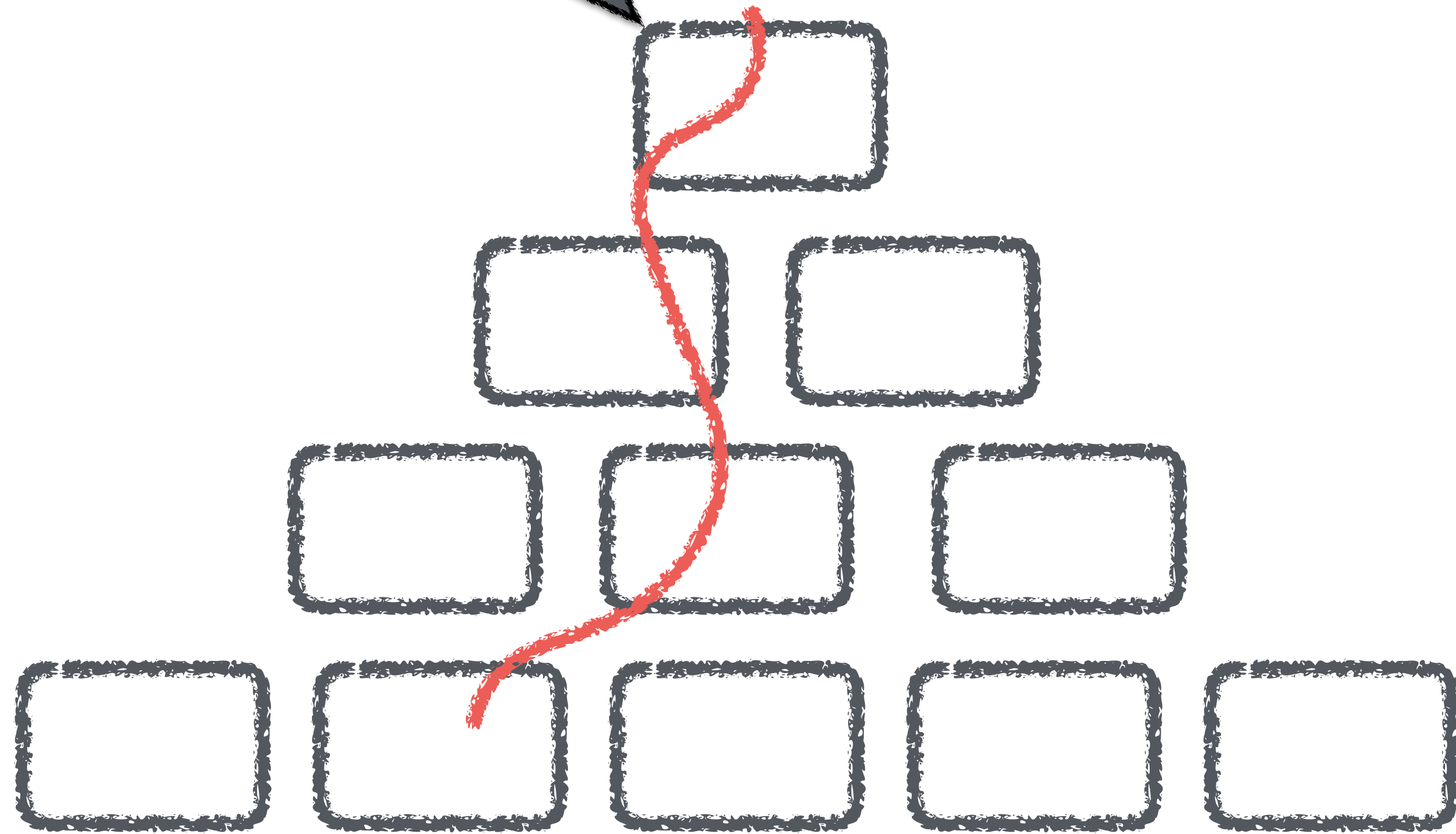


**DESIGN SPACE  
OF POSSIBLE  
STORAGE LAYOUTS**



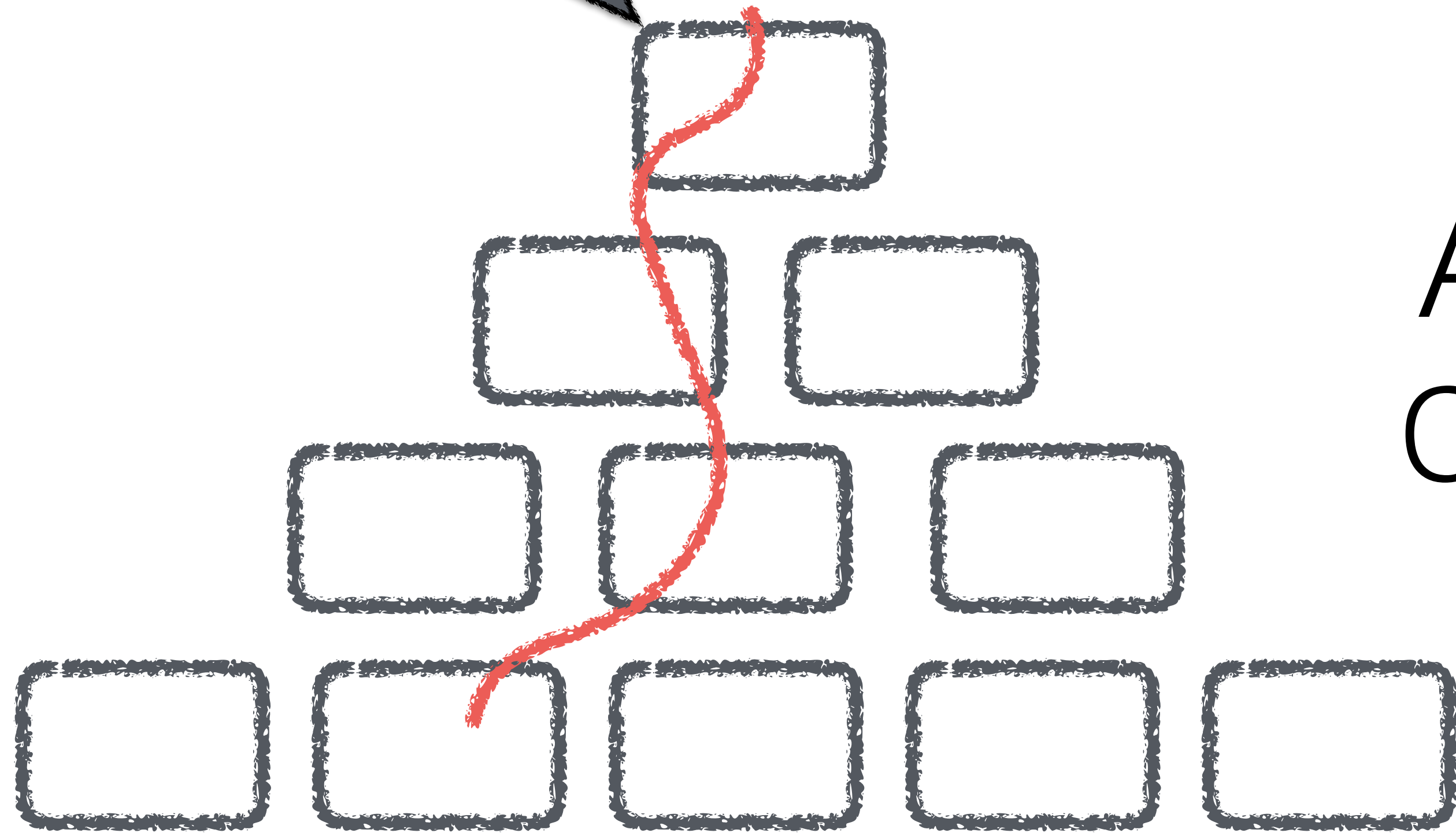
# DESIGN SPACE OF POSSIBLE STORAGE LAYOUTS

operation

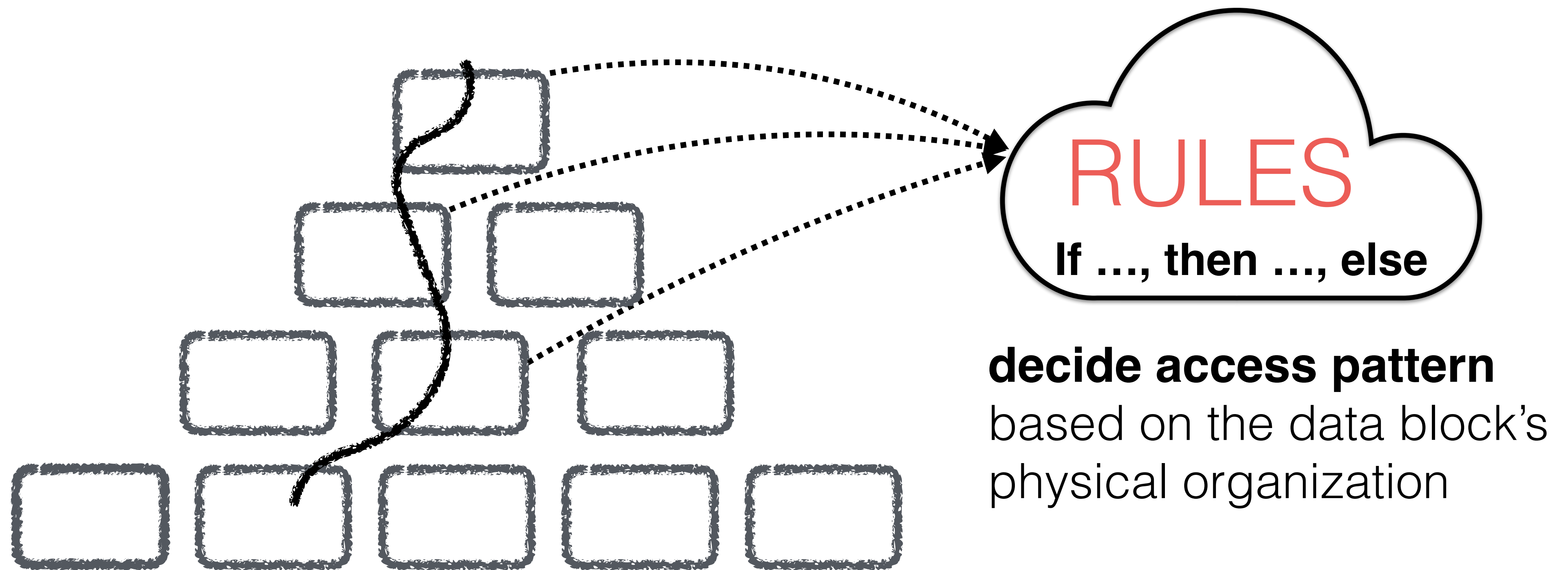


DESIGN SPACE  
OF POSSIBLE  
STORAGE LAYOUTS

operation



ALGORITHM &  
COST SYNTHESIS



**sorted keys**  
**columnar layout**





# DEPENDS ON **HARDWARE ENGINEERING**

sorted keys  
columnar layout

RULES

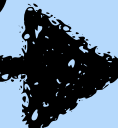


sorted  
search

binary search1  
binary search2  
interpolation search1  
interpolation search2  
using new SIMD  
instruction X  
...

sorted keys  
columnar layout

**RULES**



**sorted  
search**

**batched  
write**

**BF  
probe**

**scan**

...

binary search1  
binary search2  
interpolation search1  
interpolation search2  
using new SIMD  
instruction X  
...

**Rules: access principles**

sorted keys  
columnar layout

**RULES**

sorted  
search

batched  
write

BF  
probe

scan

...

binary search1  
binary search2  
interpolation search1  
interpolation search2  
using new SIMD  
instruction X  
...

code,  
model

code,  
model

**Learning of fine-grained  
access patterns**

**Rules: access principles**

# SYNTHESIS FROM LEARNED MODELS

coding, modeling, generalized models, and a touch of ML

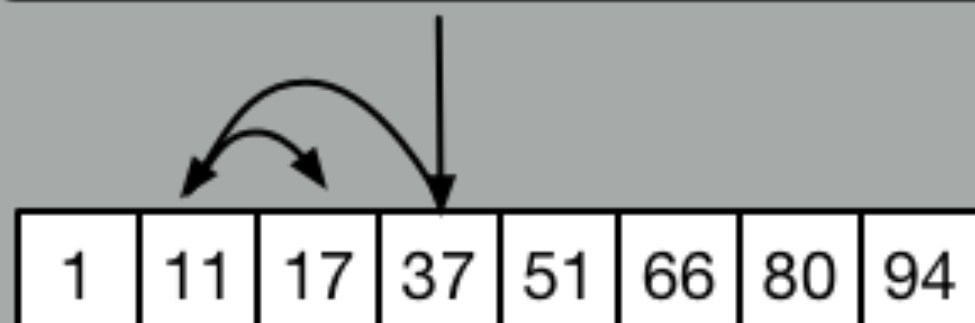


## 1. MINIMAL CODE

e.g., binary search

```
if (data[middle] < search_val) {  
    low = middle + 1;  
} else {  
    high = middle;  
}  
middle = (low + high)/2;
```

C++





# SYNTHESIS FROM LEARNED MODELS

coding, modeling, generalized models, and a touch of ML



## 1. MINIMAL CODE

e.g., binary search

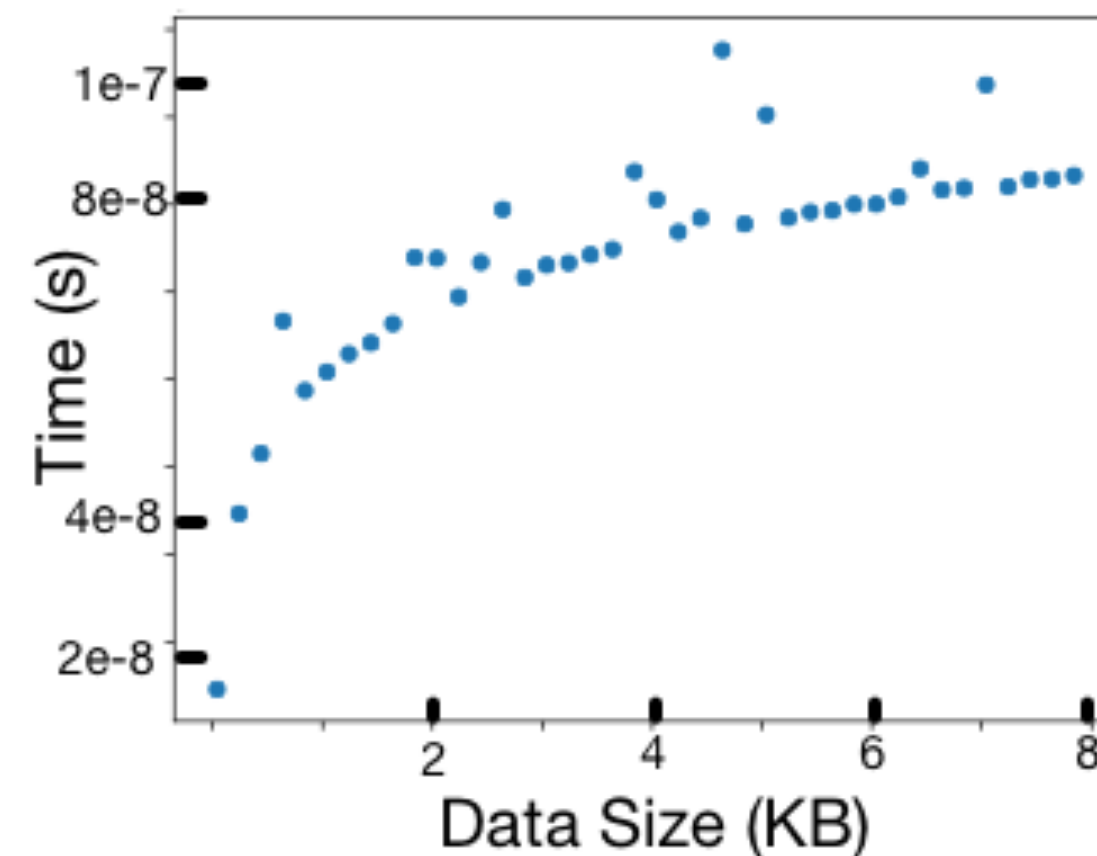
```
C++  
  
if (data[middle] < search_val) {  
    low = middle + 1;  
} else {  
    high = middle;  
}  
middle = (low + high)/2;
```



Run

1	11	17	37	51	66	80	94
---	----	----	----	----	----	----	----

## 2. BENCHMARK



# SYNTHESIS FROM LEARNED MODELS

coding, modeling, generalized models, and a touch of ML



## 1. MINIMAL CODE

e.g., binary search

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if (data[middle] < search_val) {  
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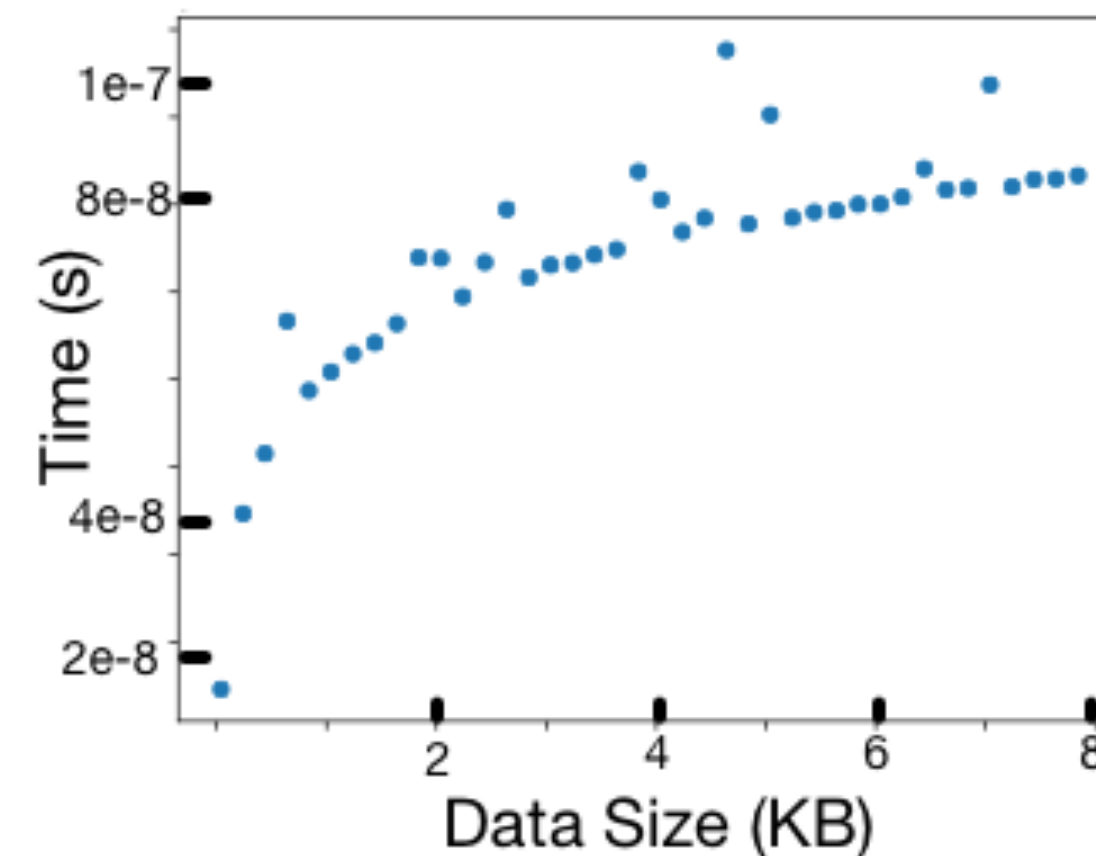
C++

1	11	17	37	51	66	80	94
---	----	----	----	----	----	----	----



Run

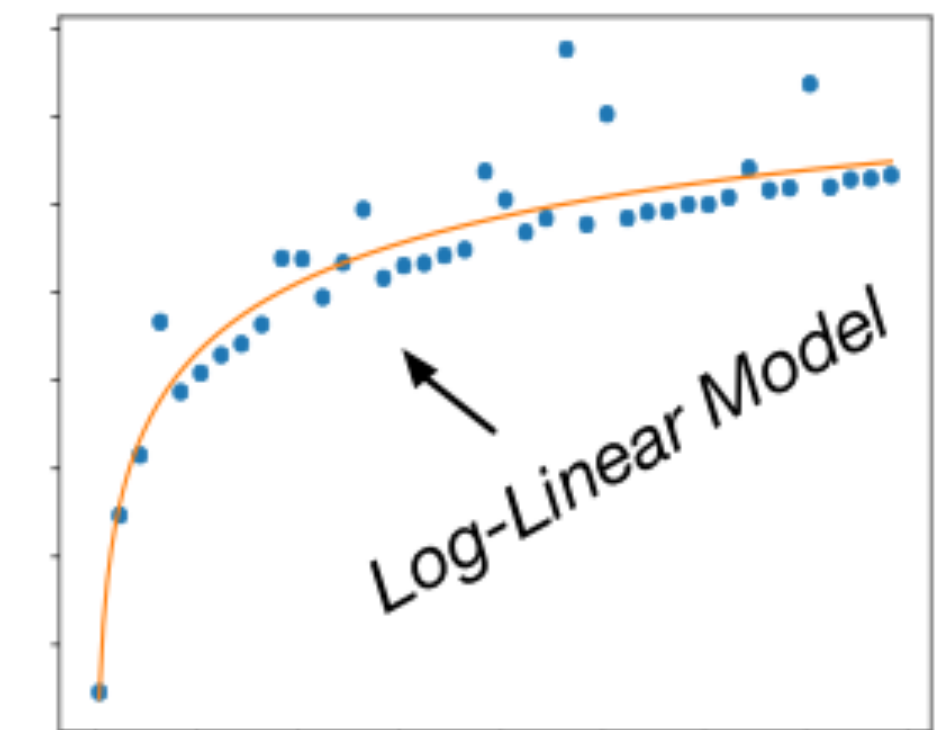
## 2. BENCHMARK



$f(x)$

Train

## 3. FIT MODEL



$$f(x) = ax + b \log x + c$$

# SYNTHESIS FROM LEARNED MODELS

coding, modeling, generalized models, and a touch of ML



## 1. MINIMAL CODE

e.g., binary search

```
if (data[middle] < search_val) {  
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```

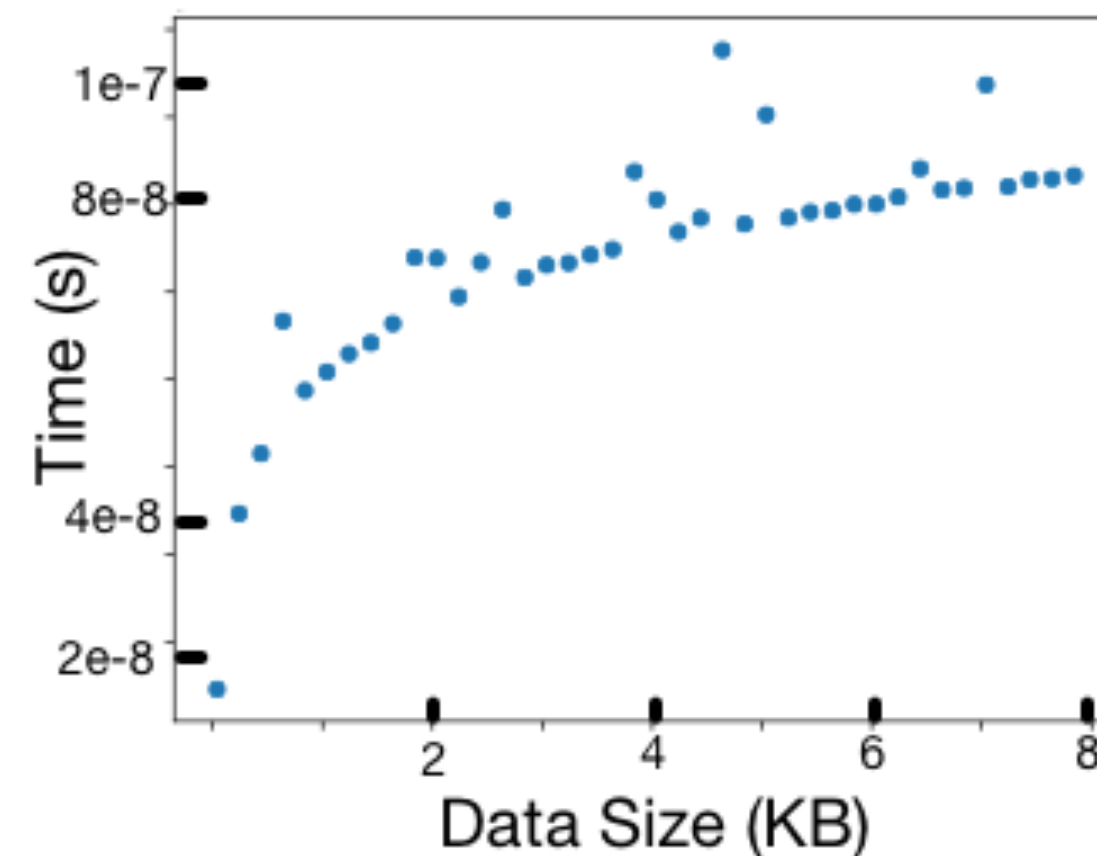
C++

1	11	17	37	51	66	80	94
---	----	----	----	----	----	----	----



Run

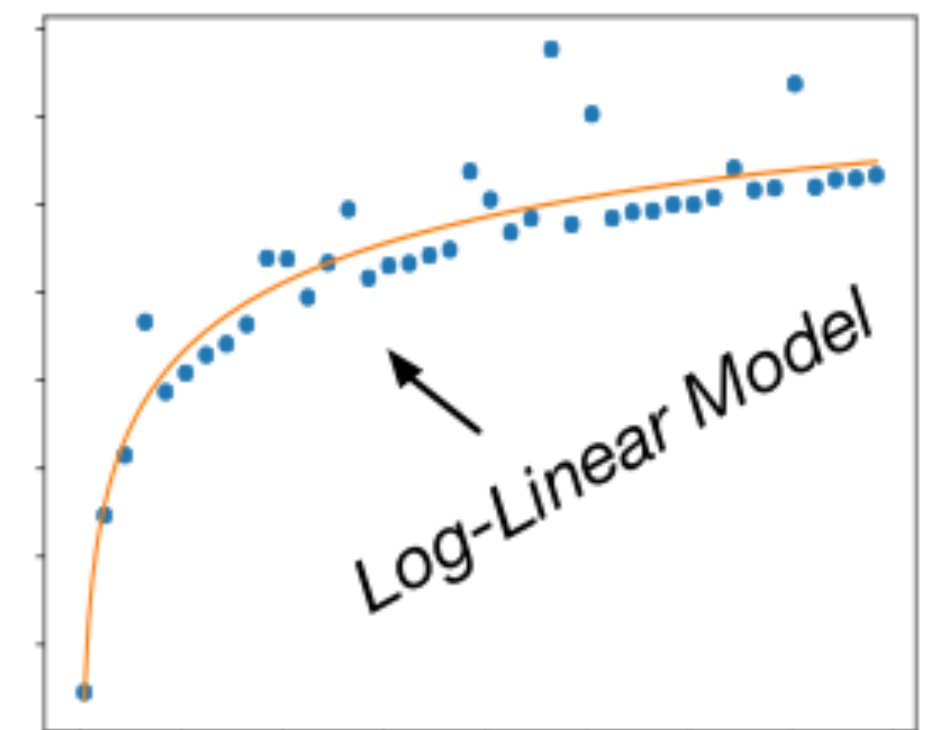
## 2. BENCHMARK



$f(x)$

Train

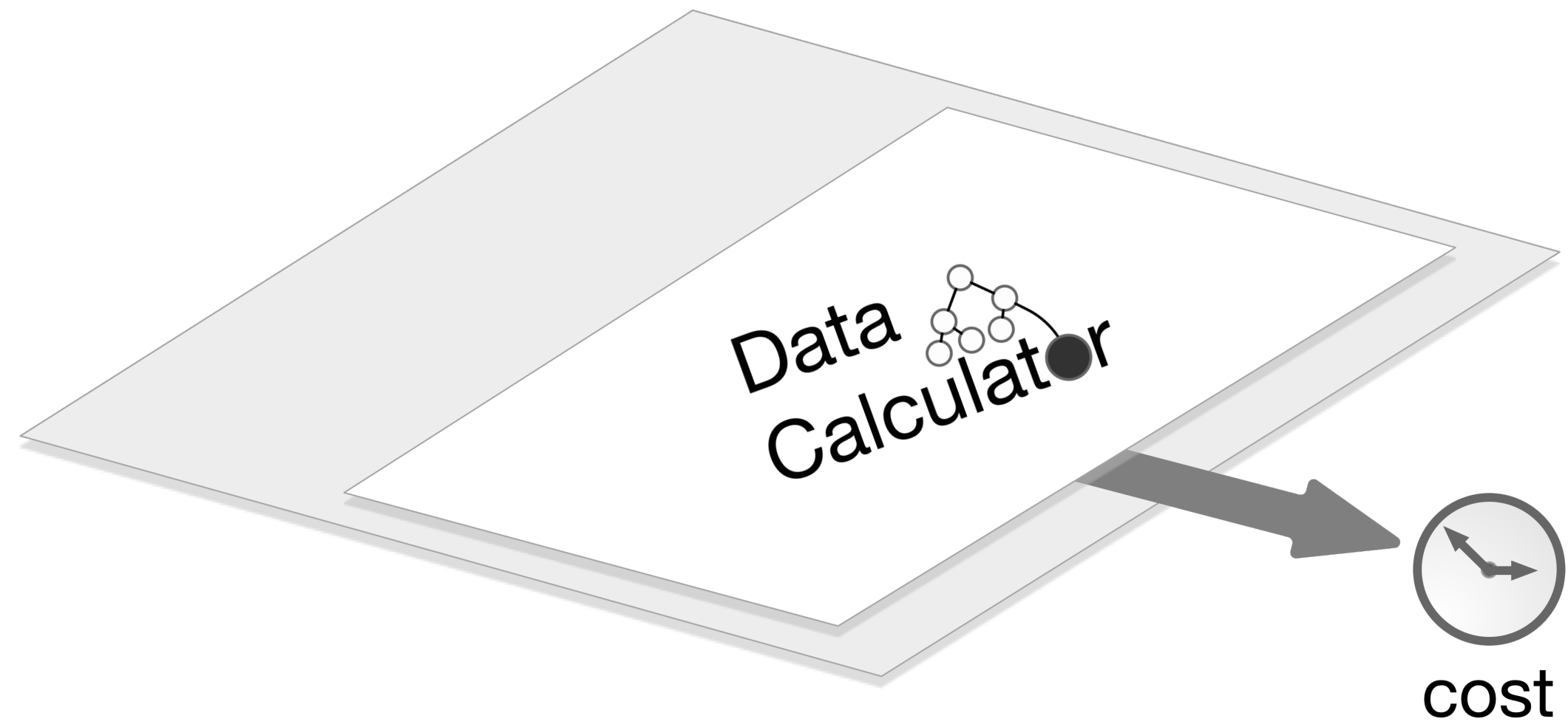
## 3. FIT MODEL



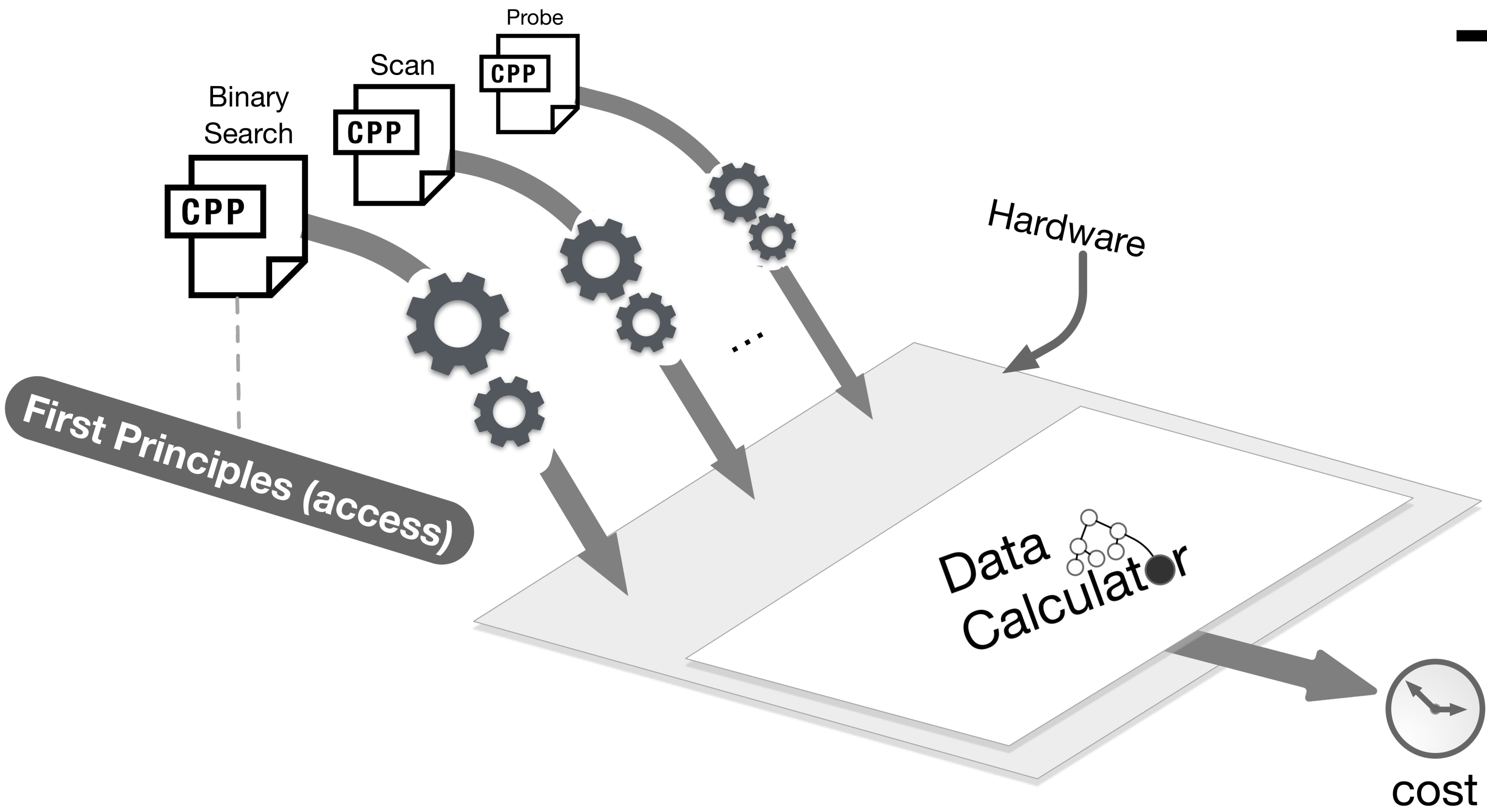
$$f(x) = ax + b \log x + c$$

**FOLDING ALGORITHMIC, ENGINEERING, AND H/W, PROPERTIES INTO THE COEFFICIENTS**

# TRAINING

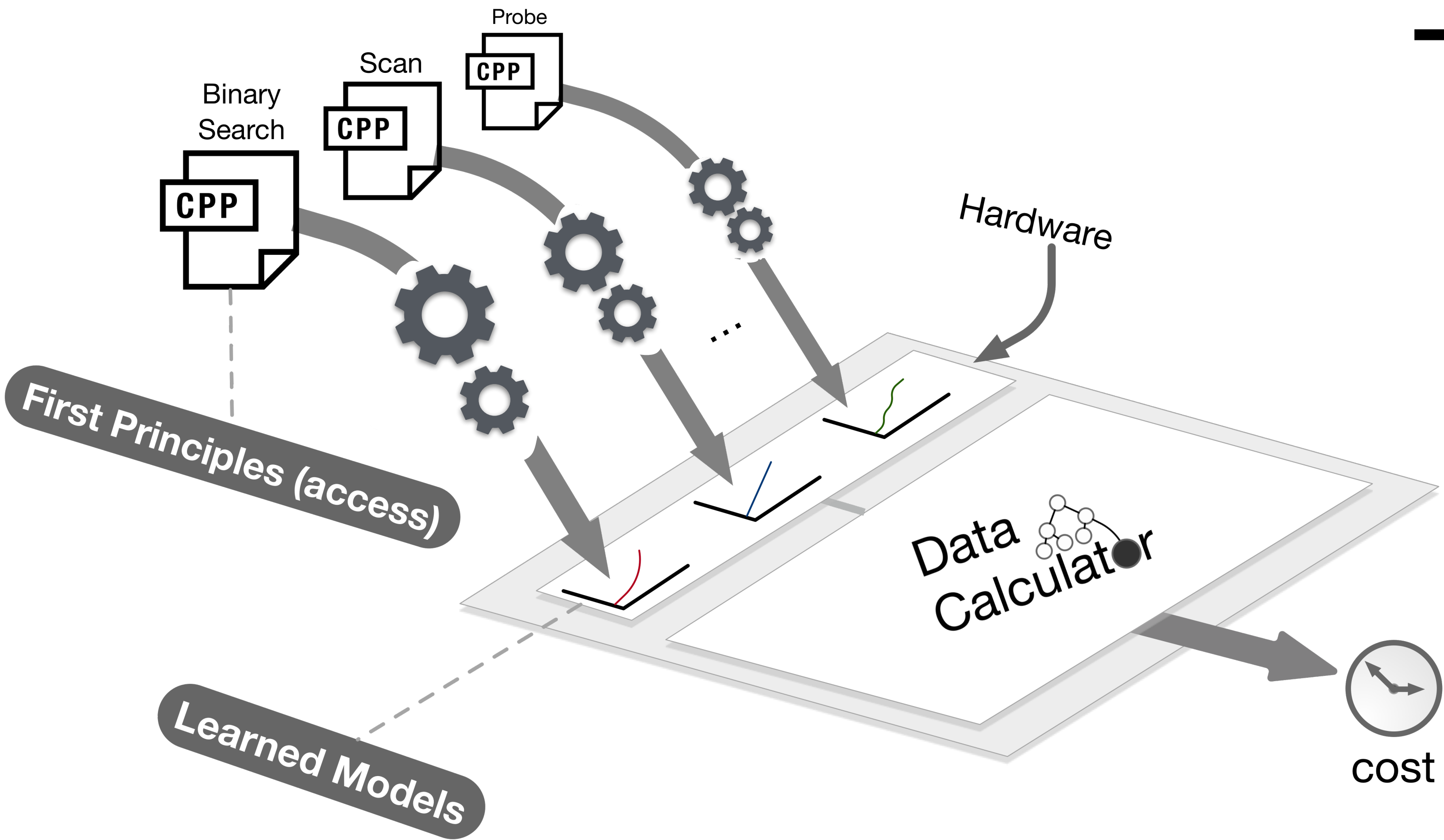


# TRAINING

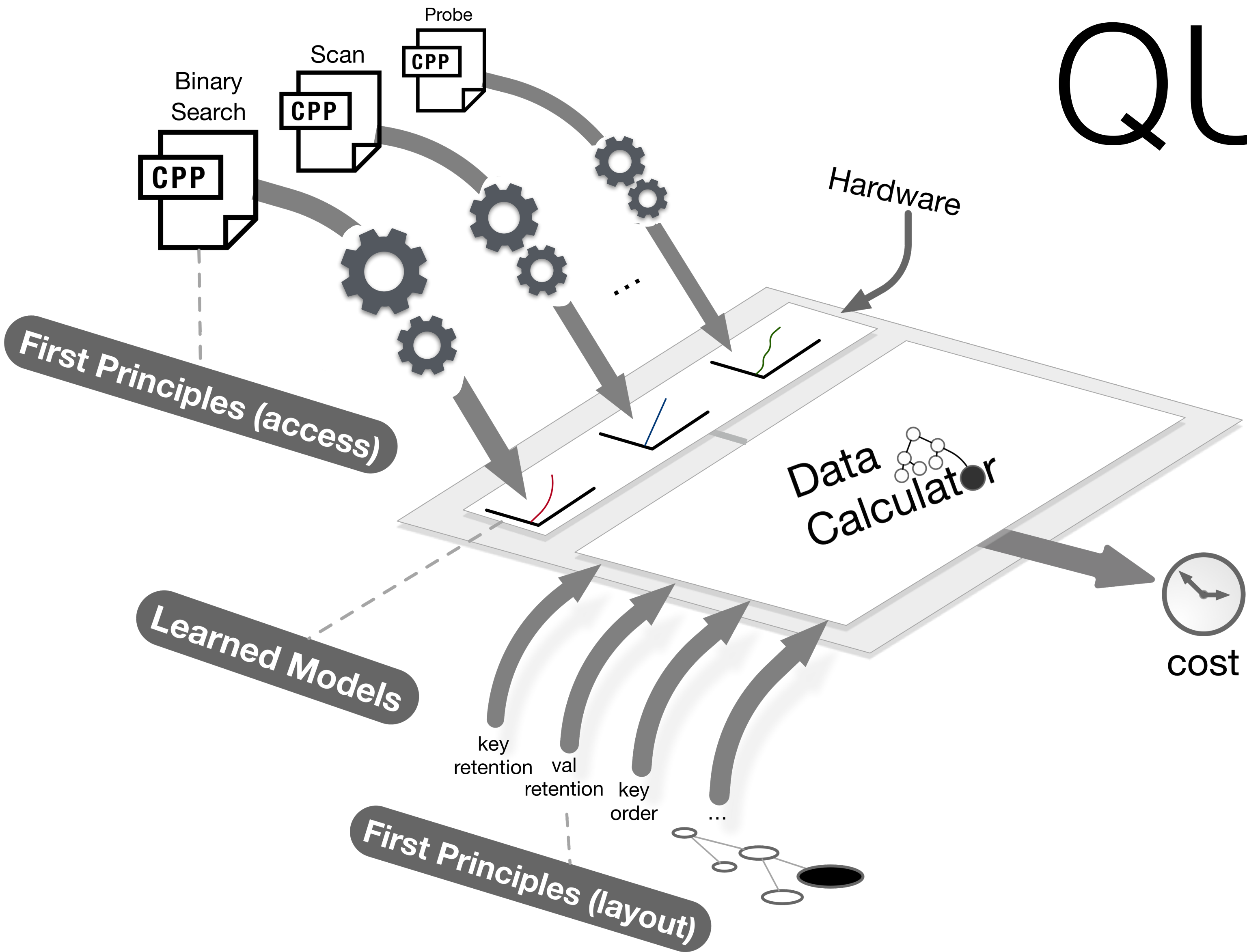




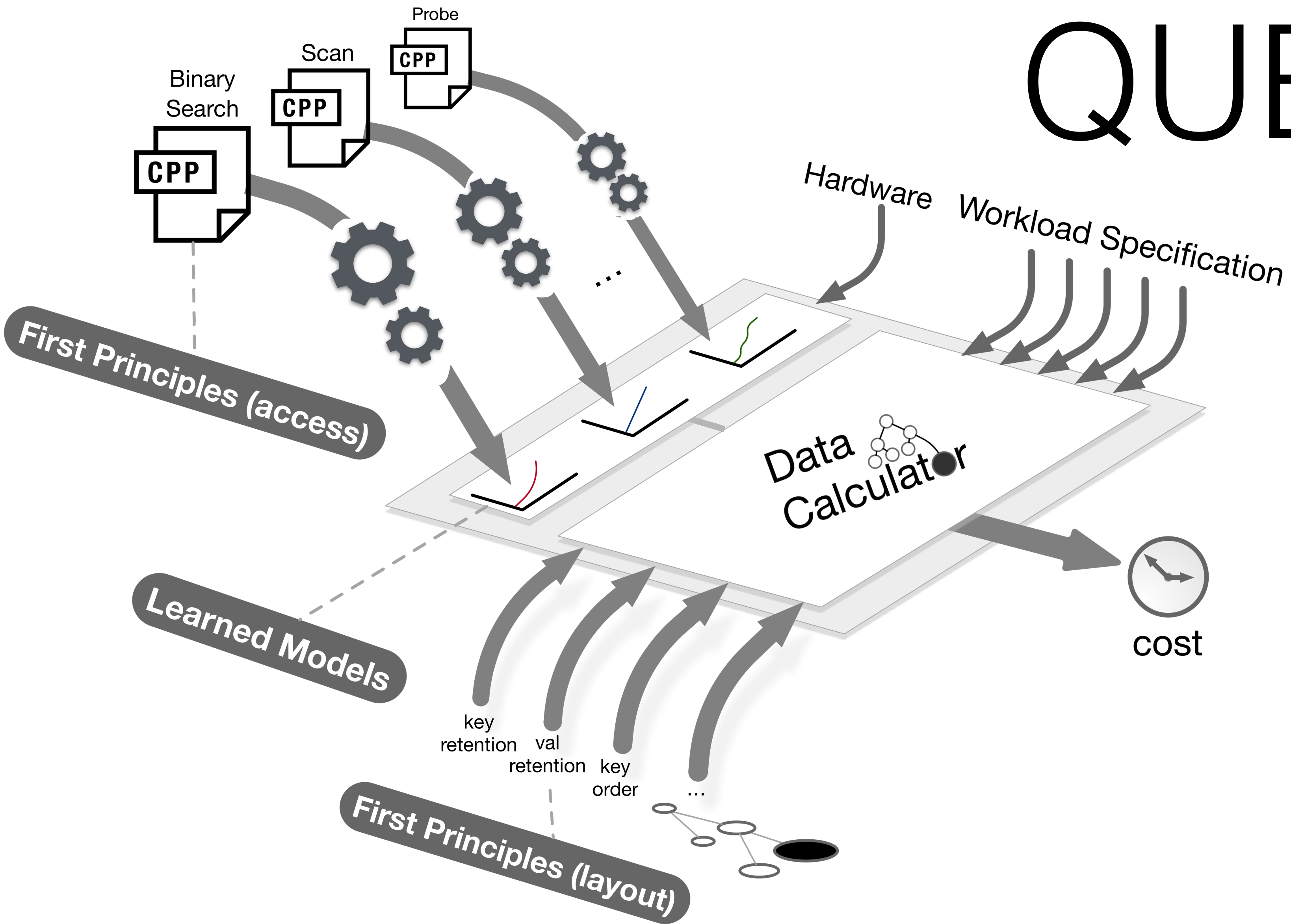
# TRAINING



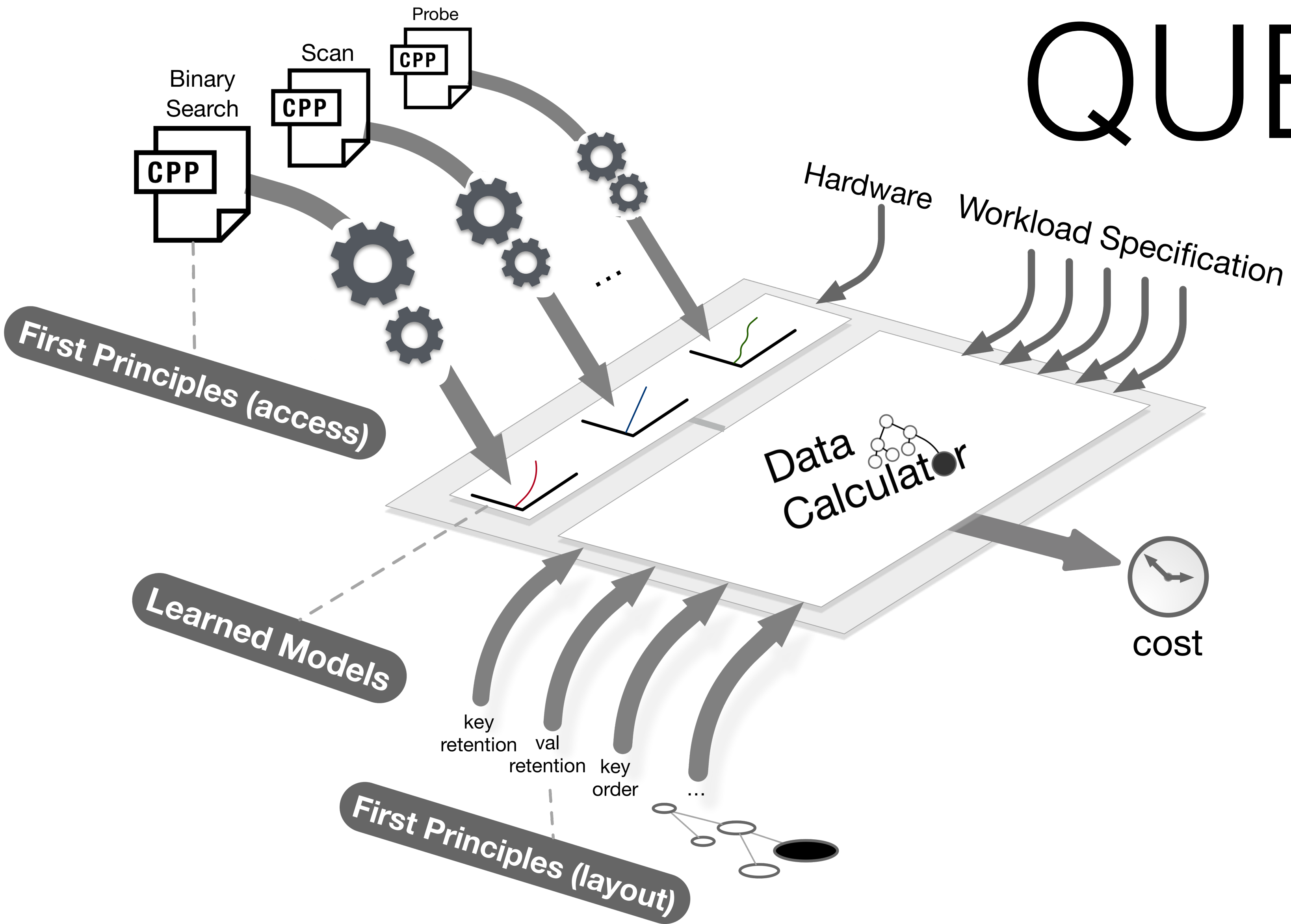
# QUERYRYING



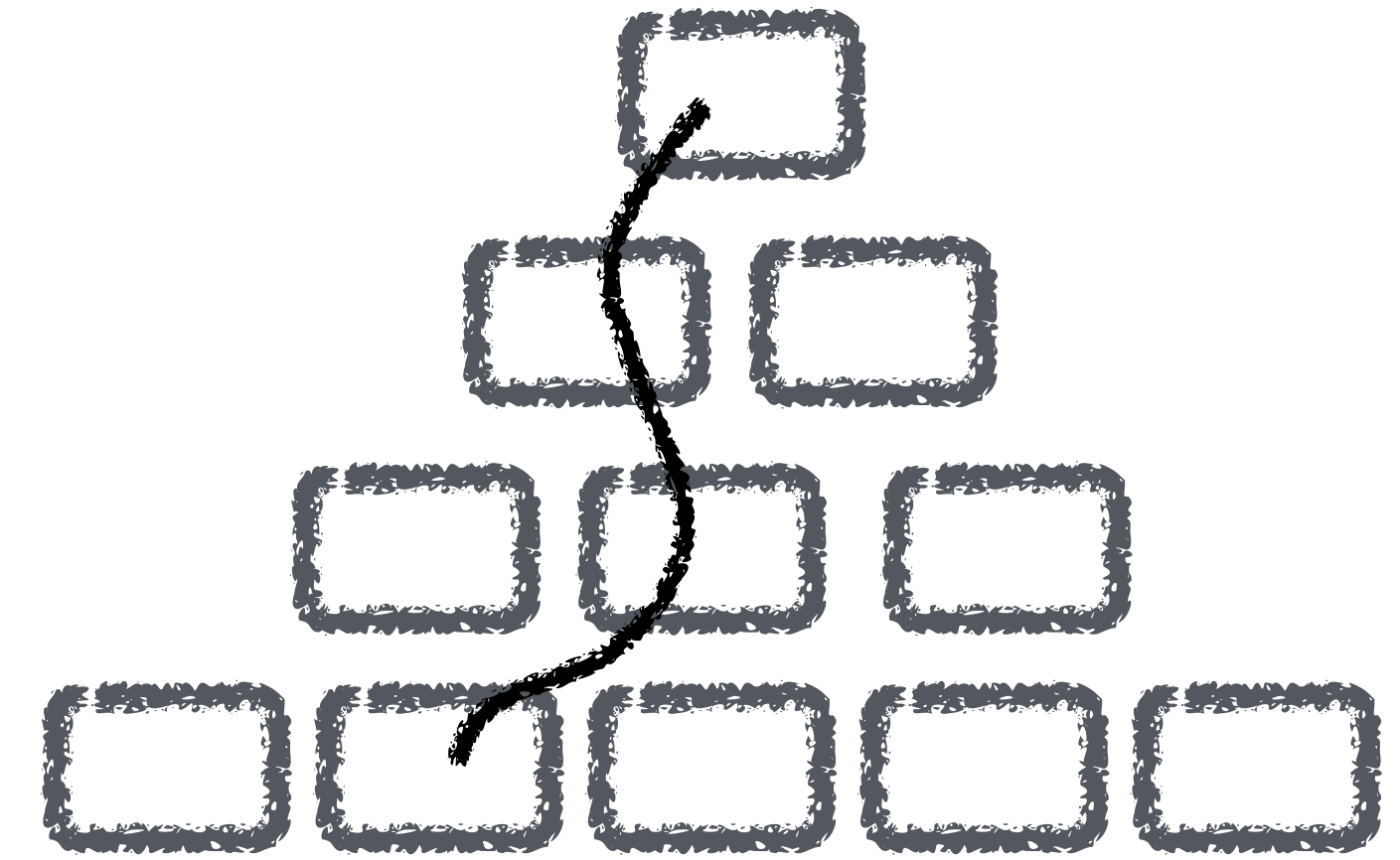
# QUERYRYING



# QUERYRYING

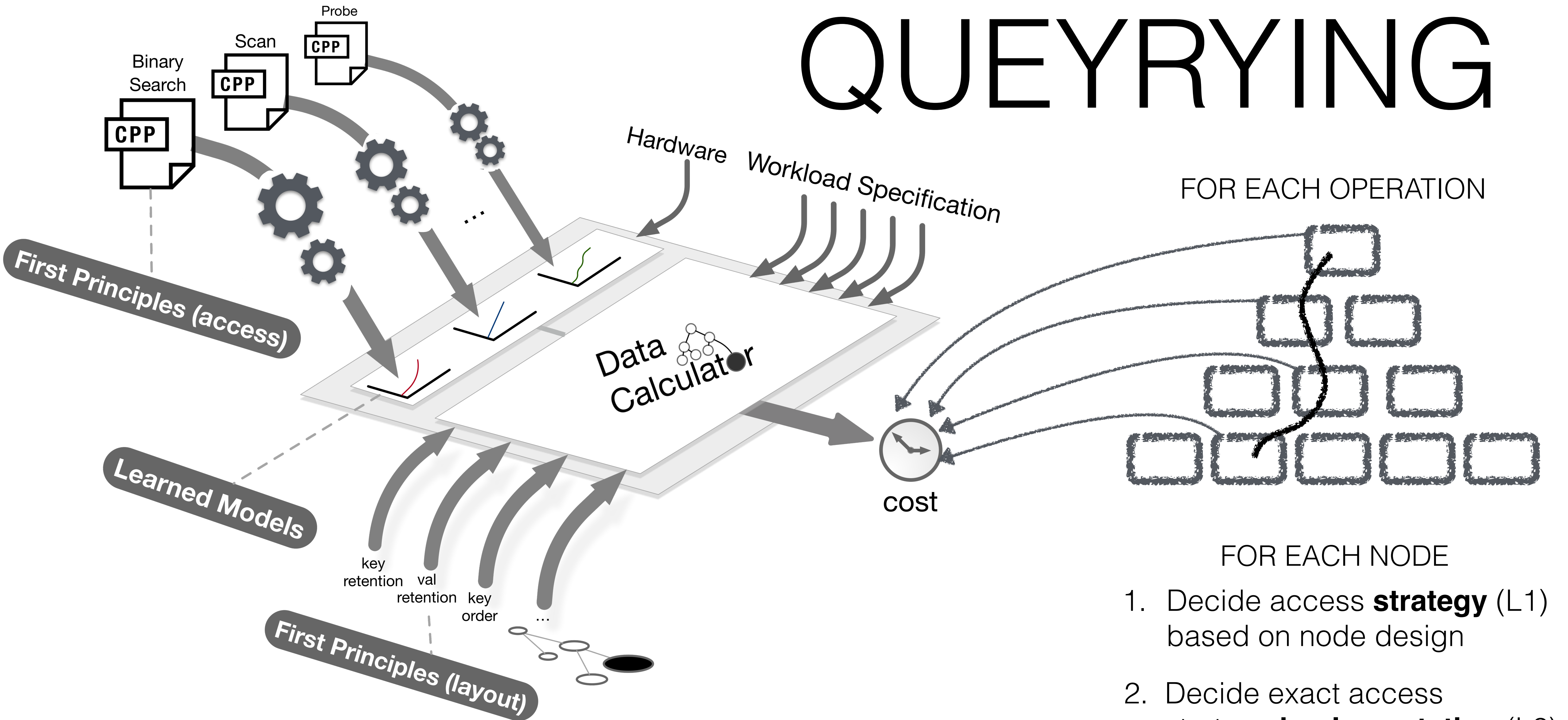


FOR EACH OPERATION



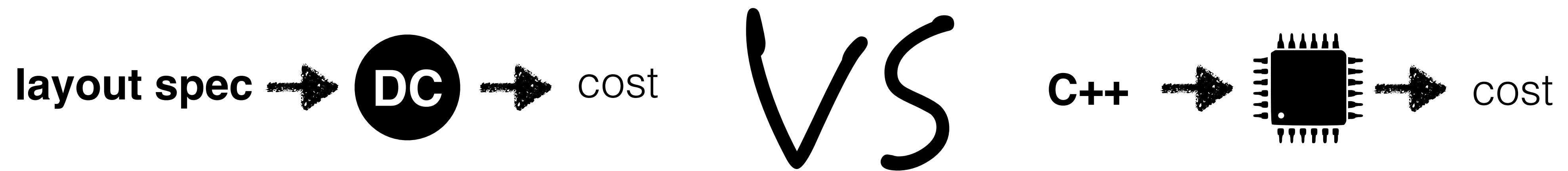


# QUERYRYING

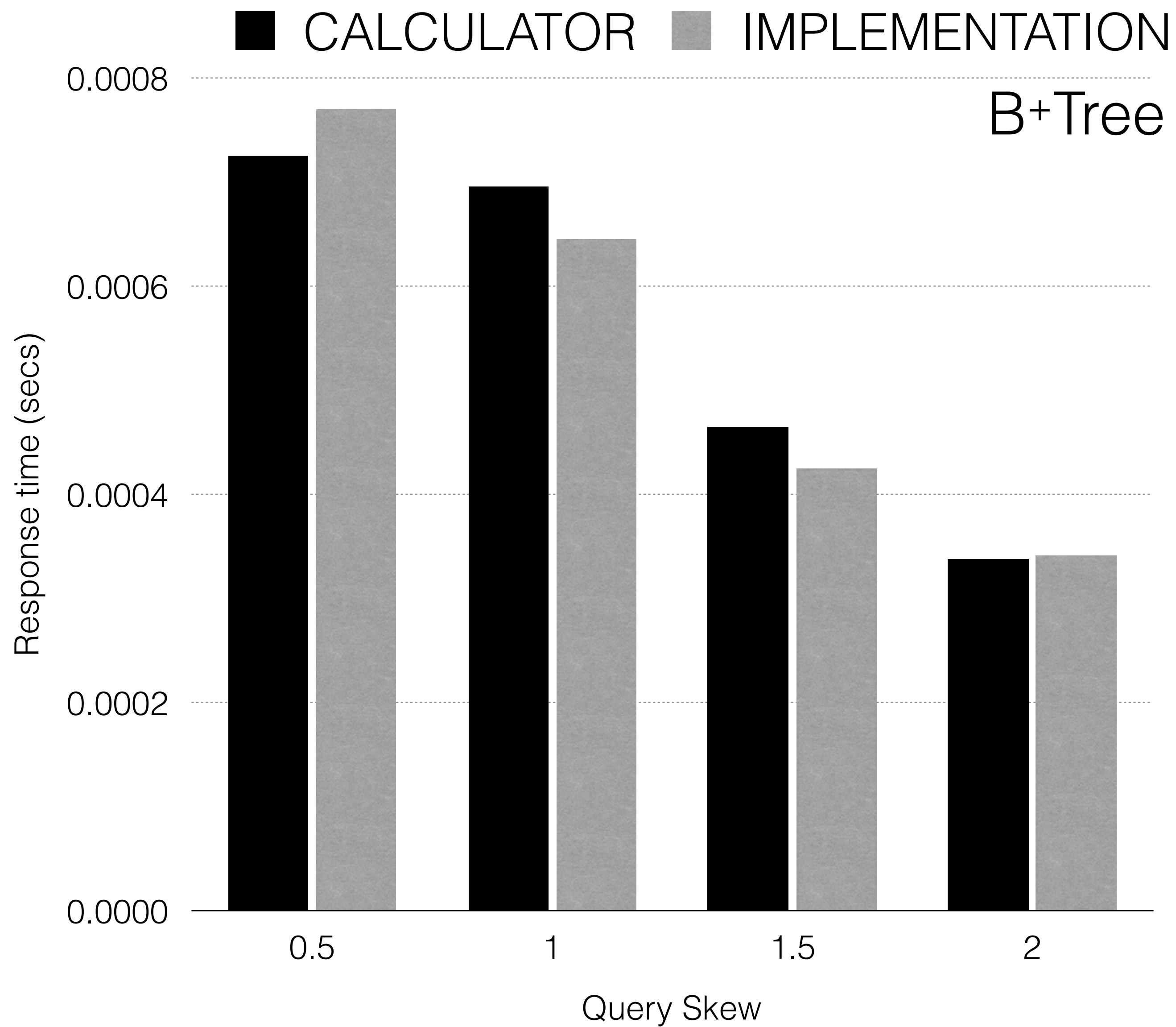


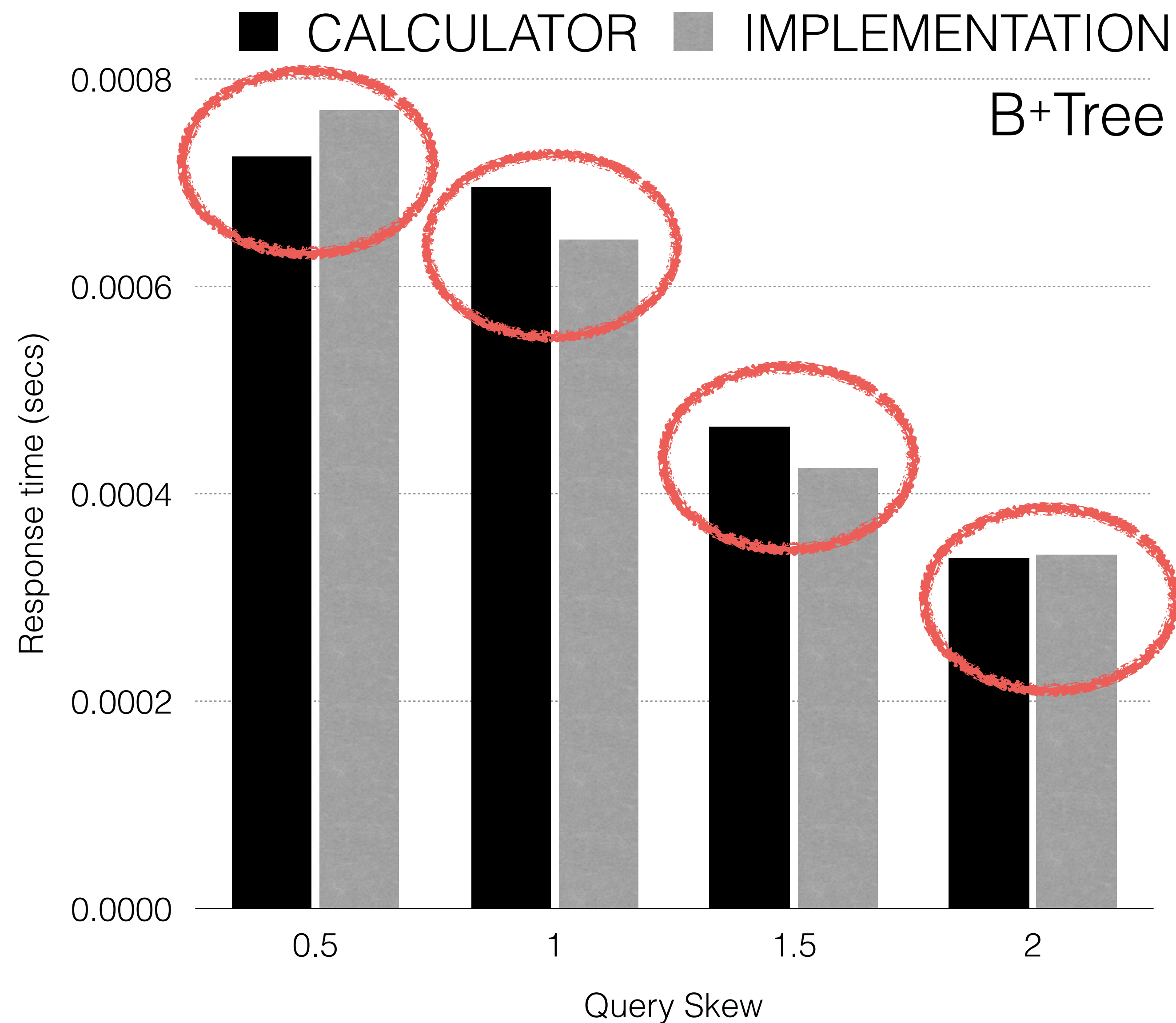


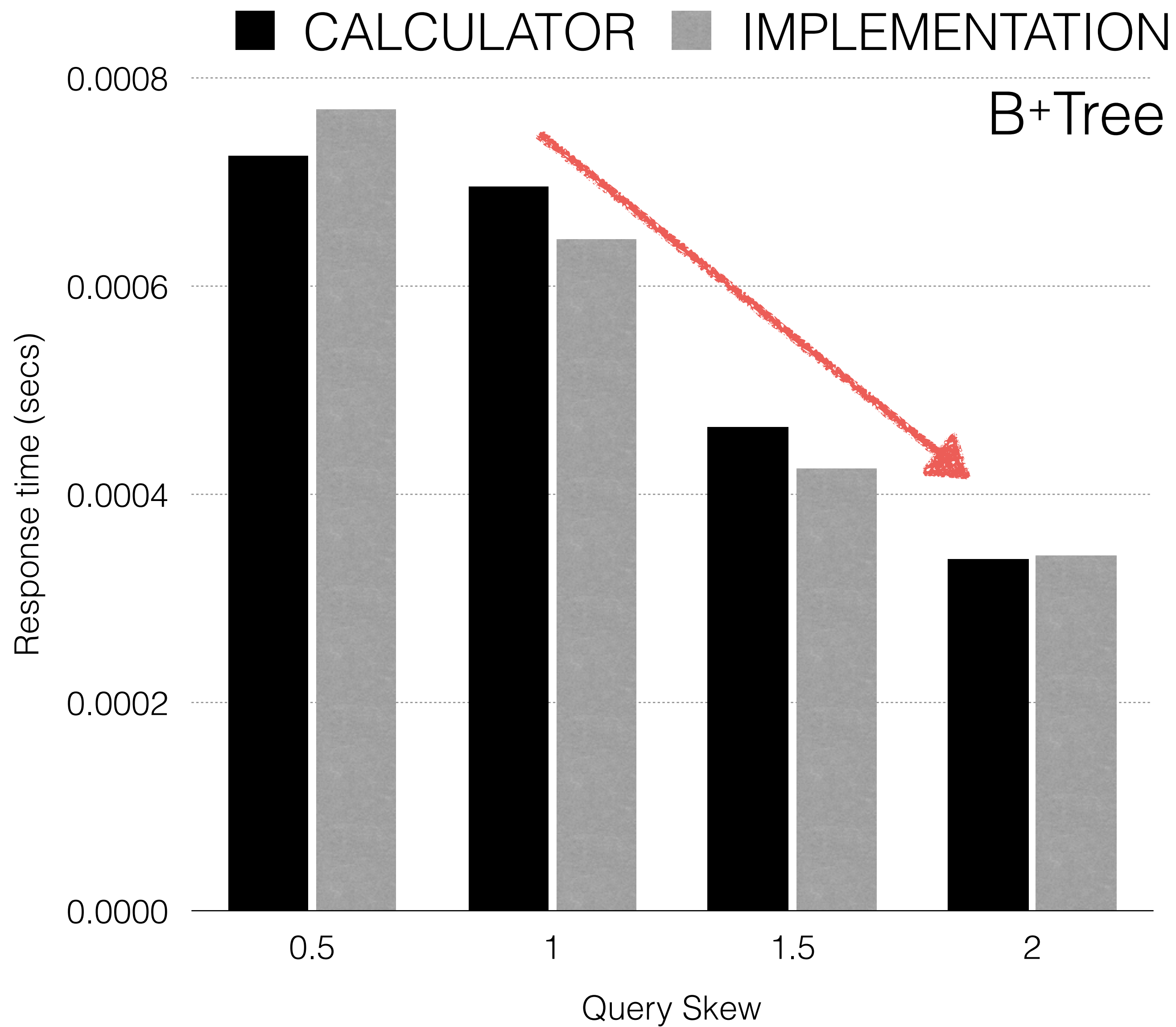
# CAN WE COMPUTE PERFORMANCE ACCURATELY?

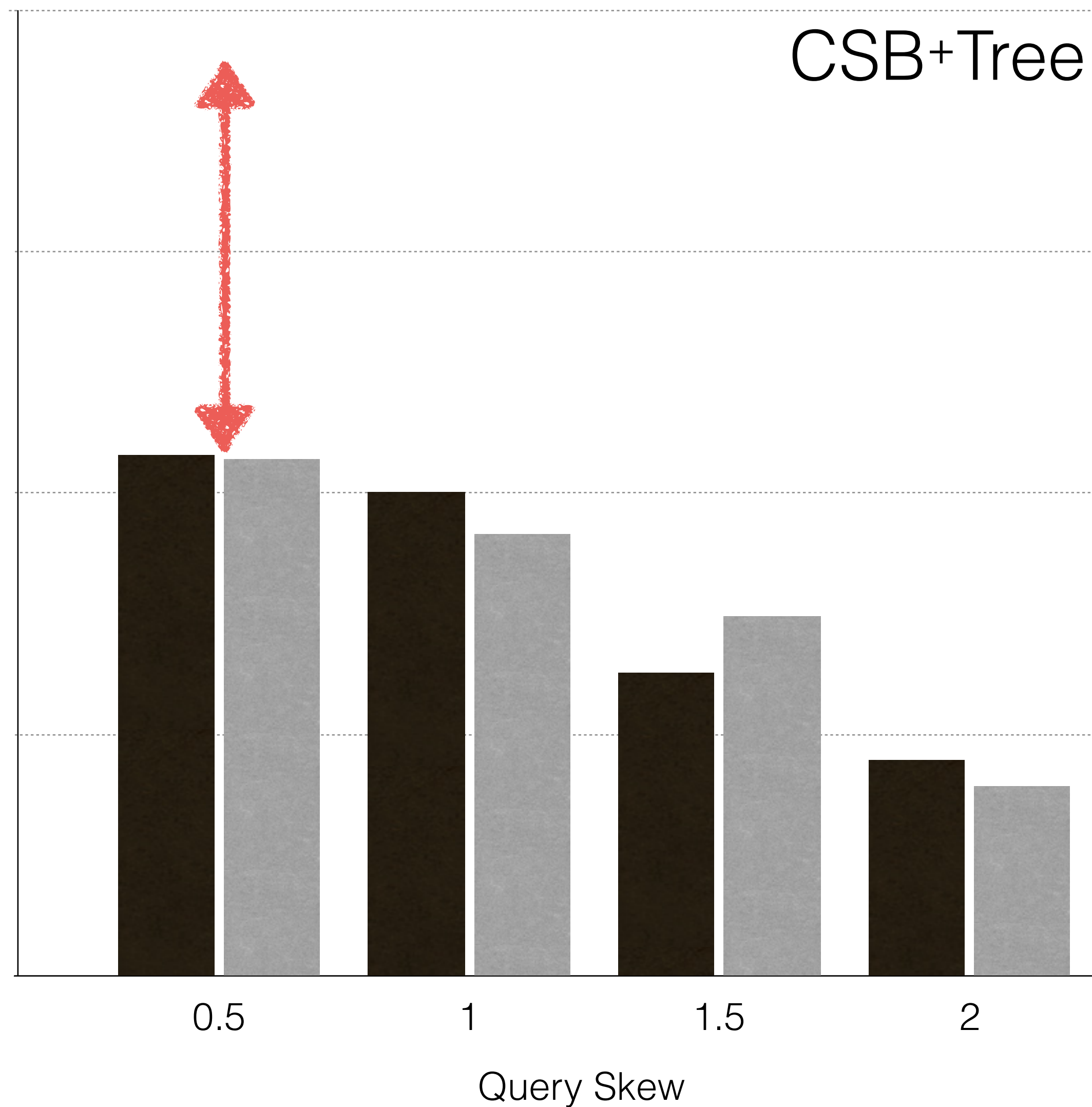
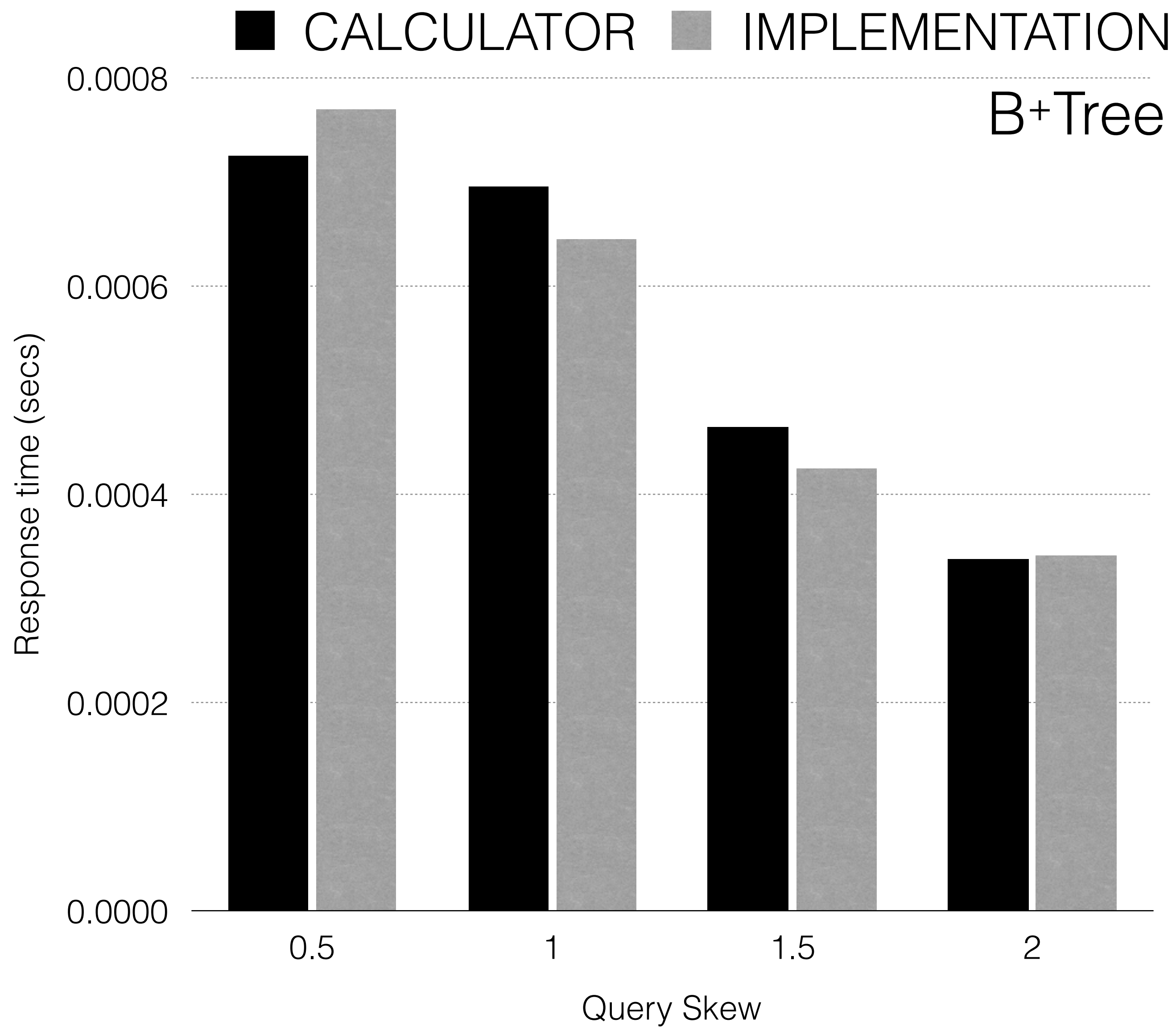


(same workload, hardware, data)



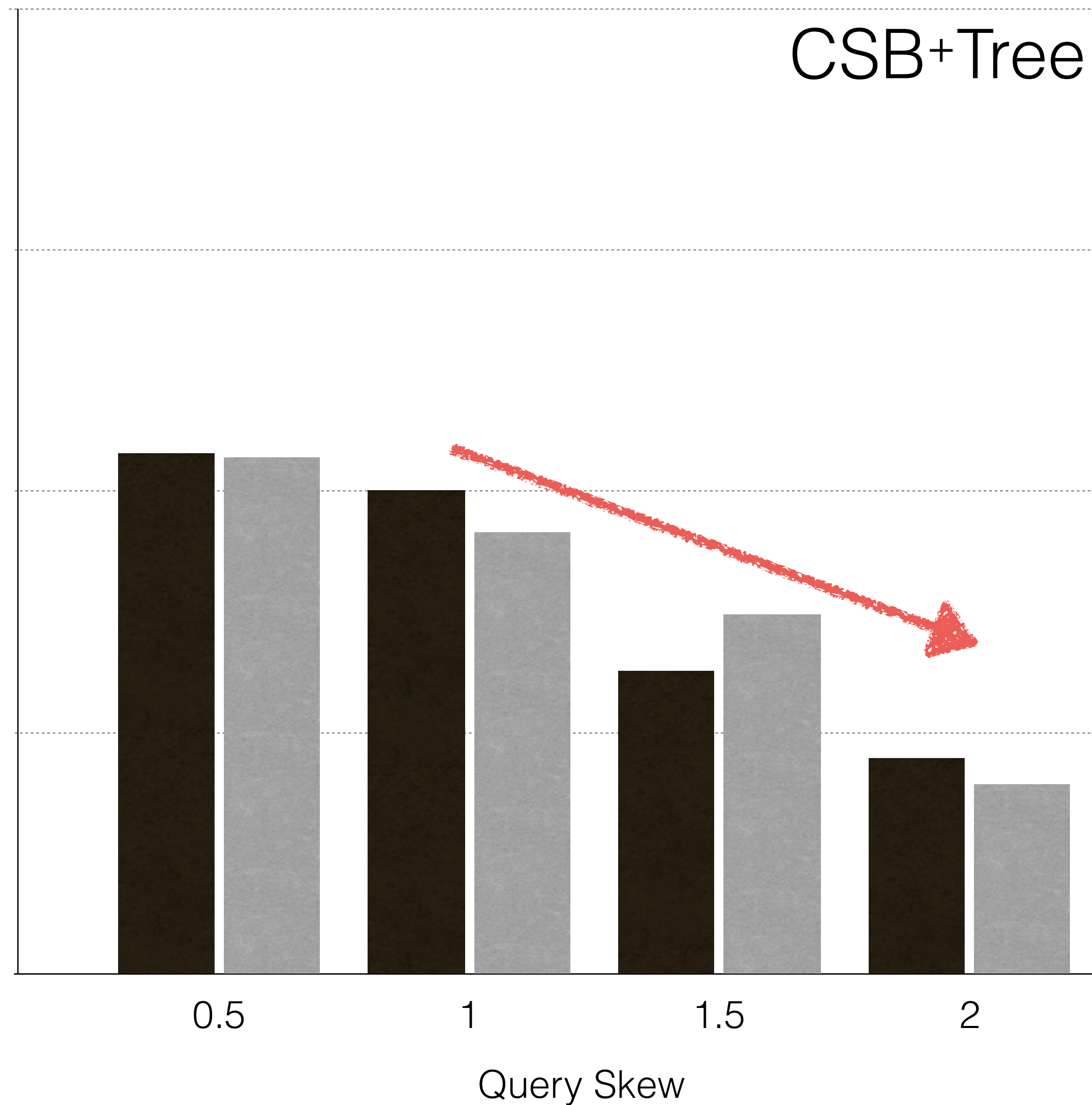
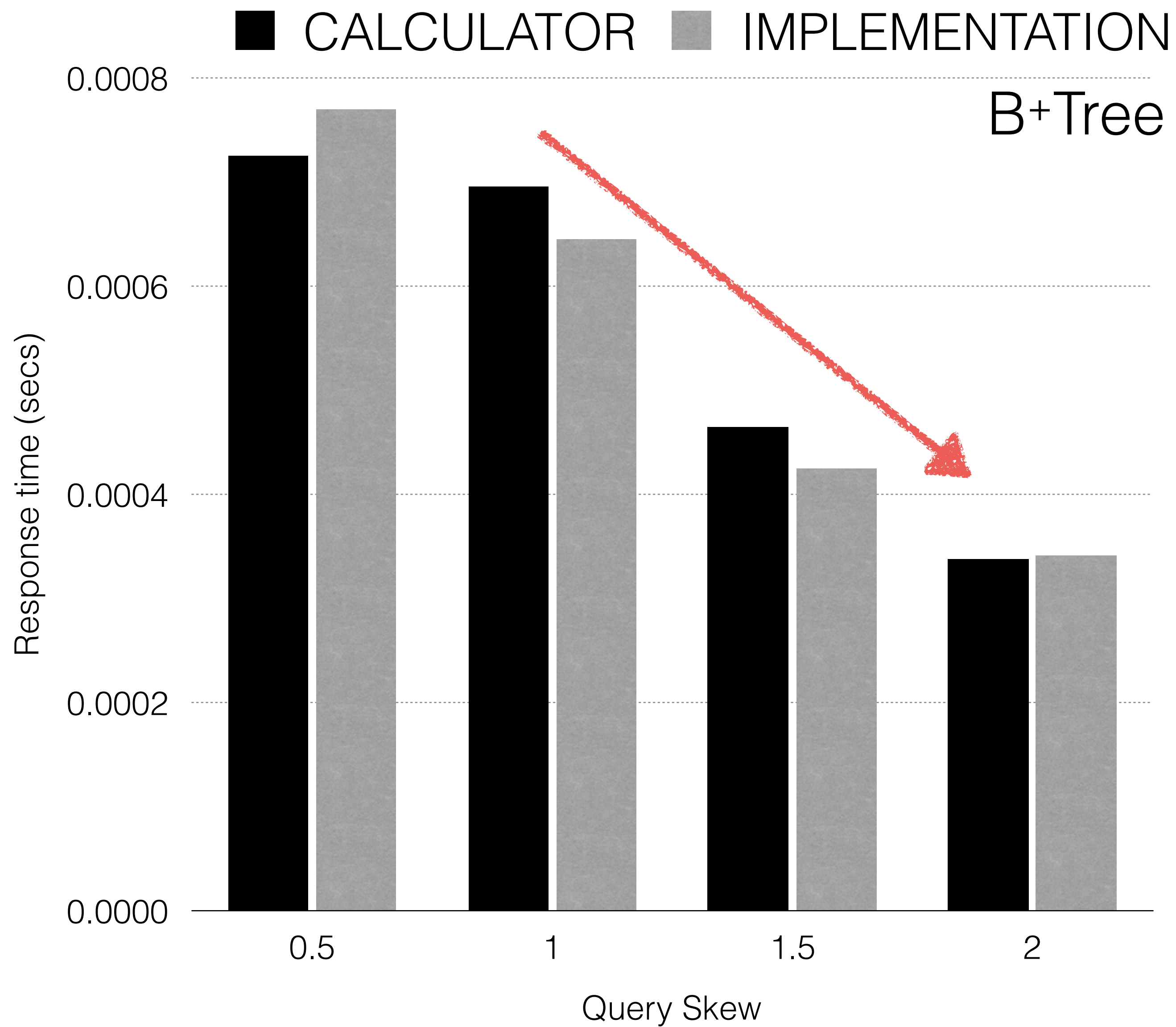


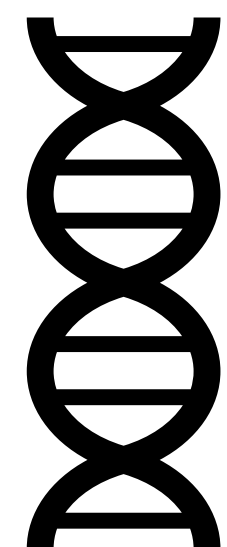




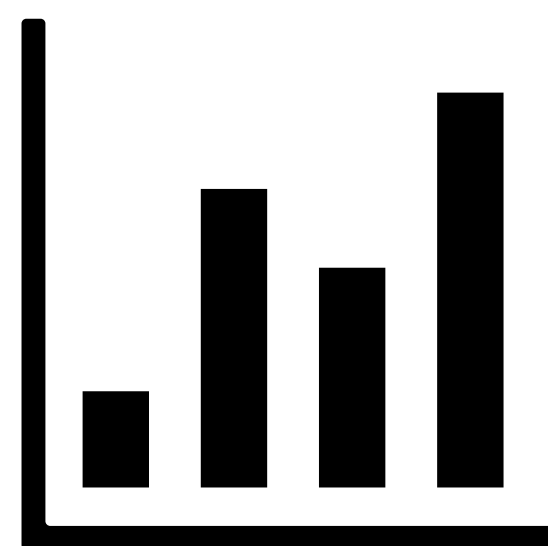
{10M (uniform) k-v pairs, 100 point queries (skewed)}



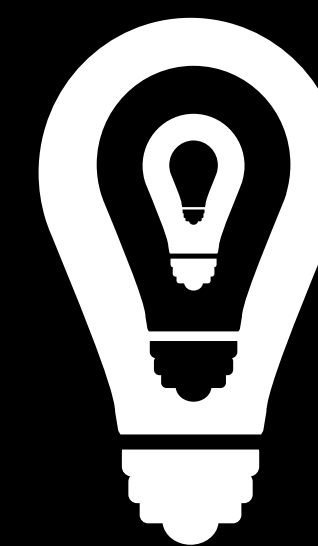




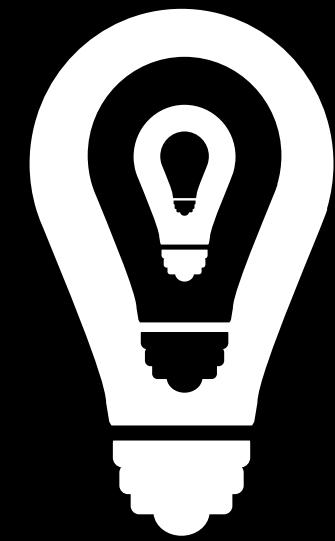
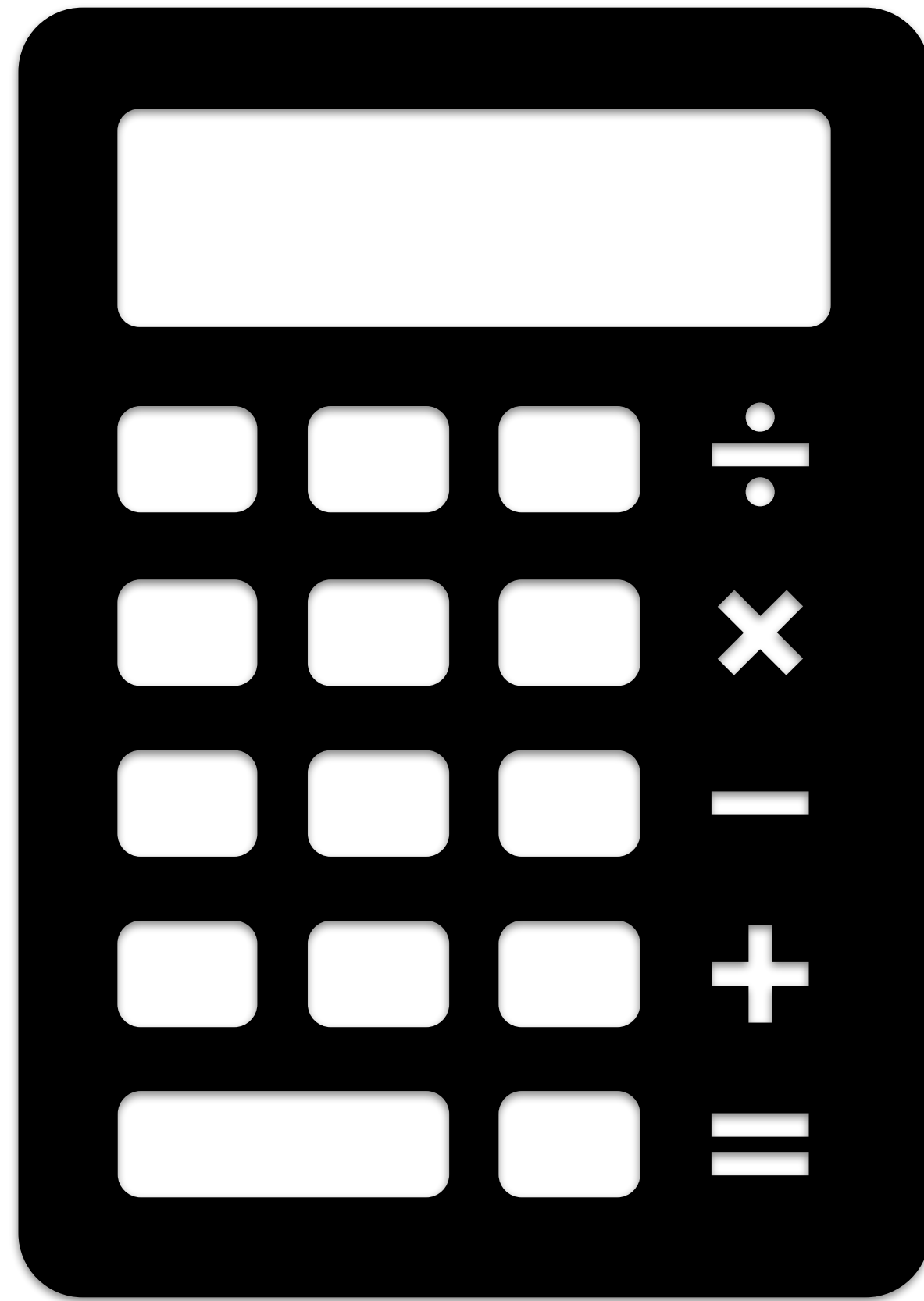
DESIGN SPACE



COST SYNTHESIS



WHO AND  
HOW TO USE



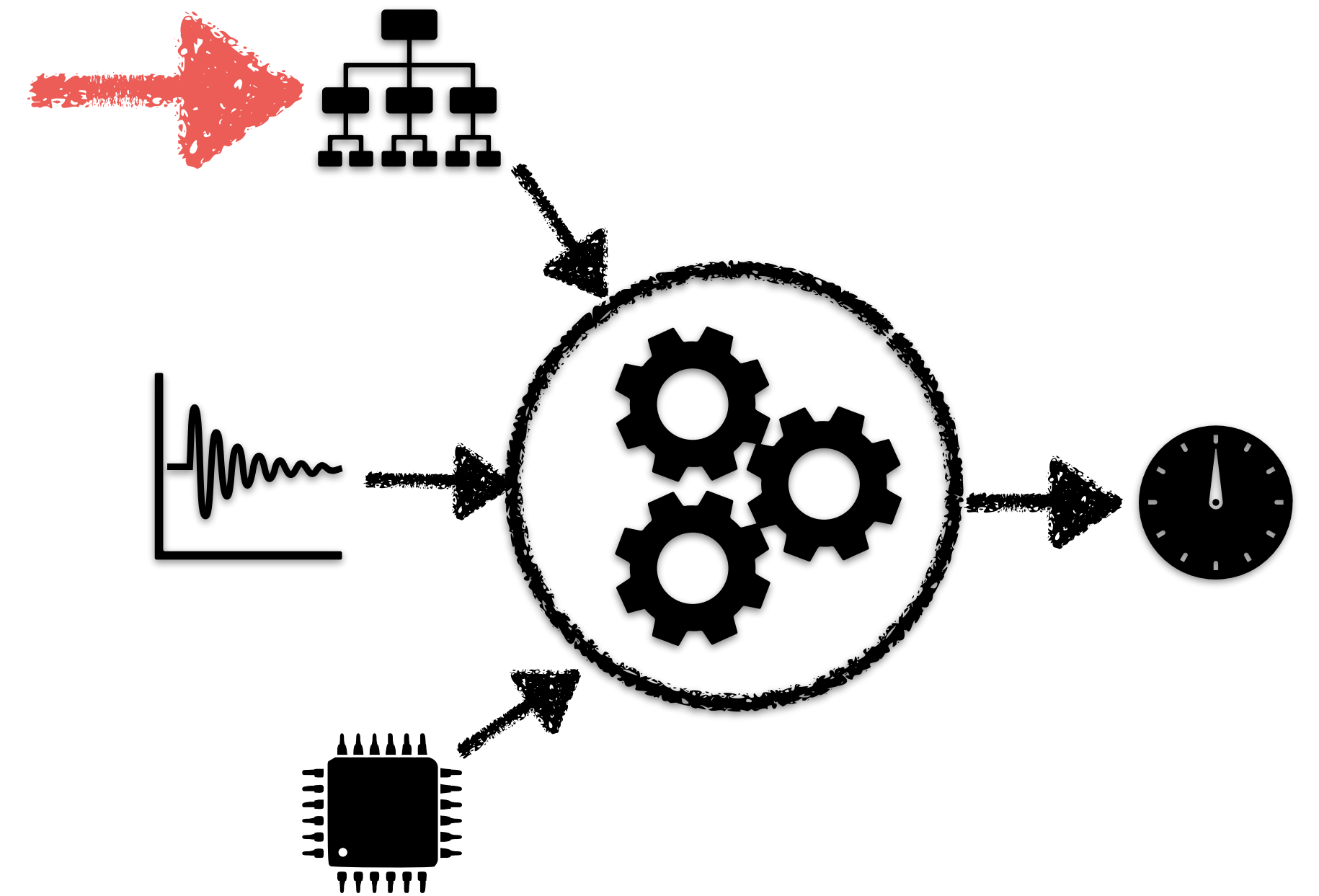
WHO AND  
HOW TO USE



# **what-if.design**

“IMAGINE” DESIGNS

*What-if we **add bloom filters**  
in the hash-table buckets?*

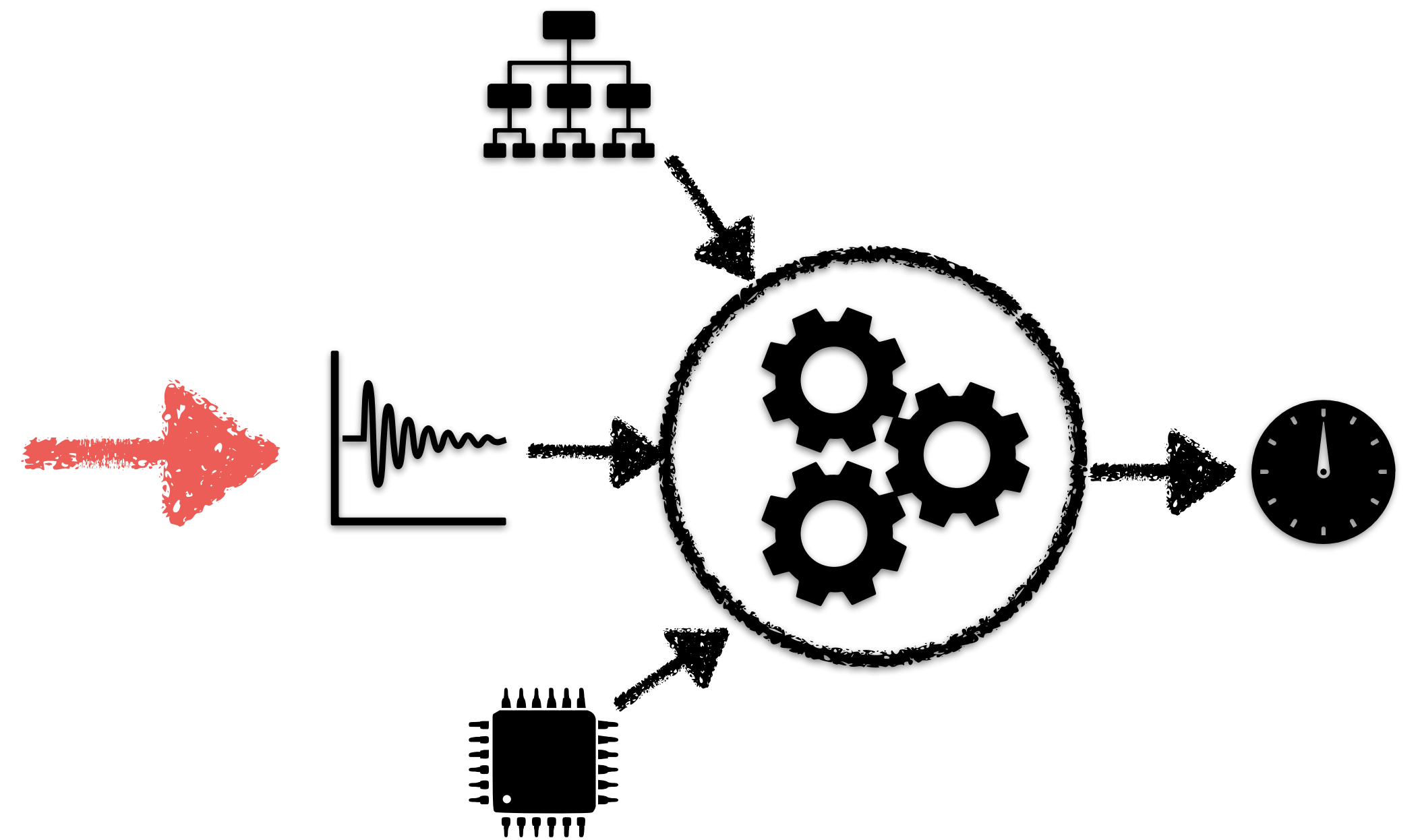


**~20 SECONDS**

(workload: 10 Million entries, 100 queries)

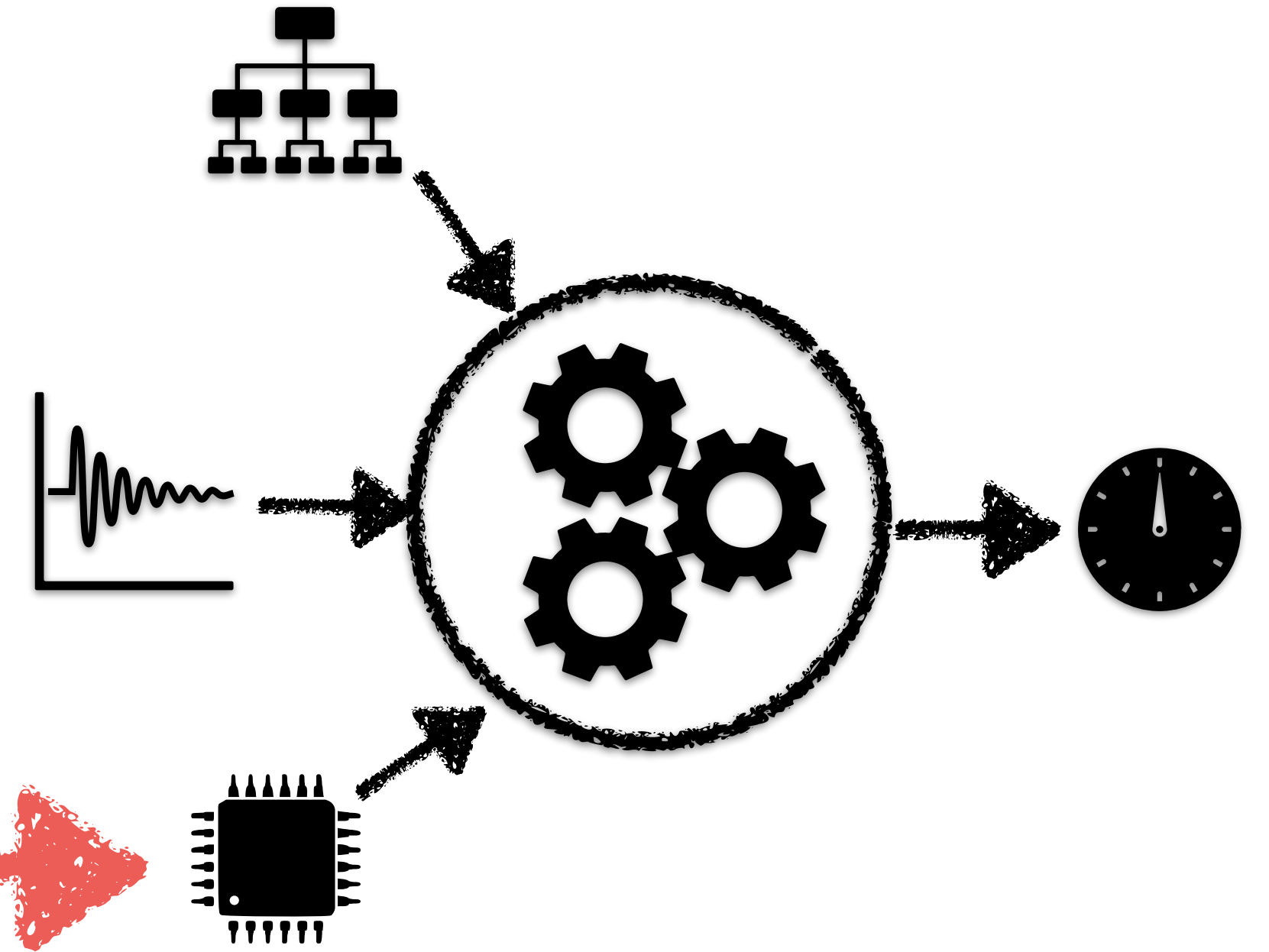


*What-if the workload  
changes to **90% writes**?*



**~20 SECONDS**

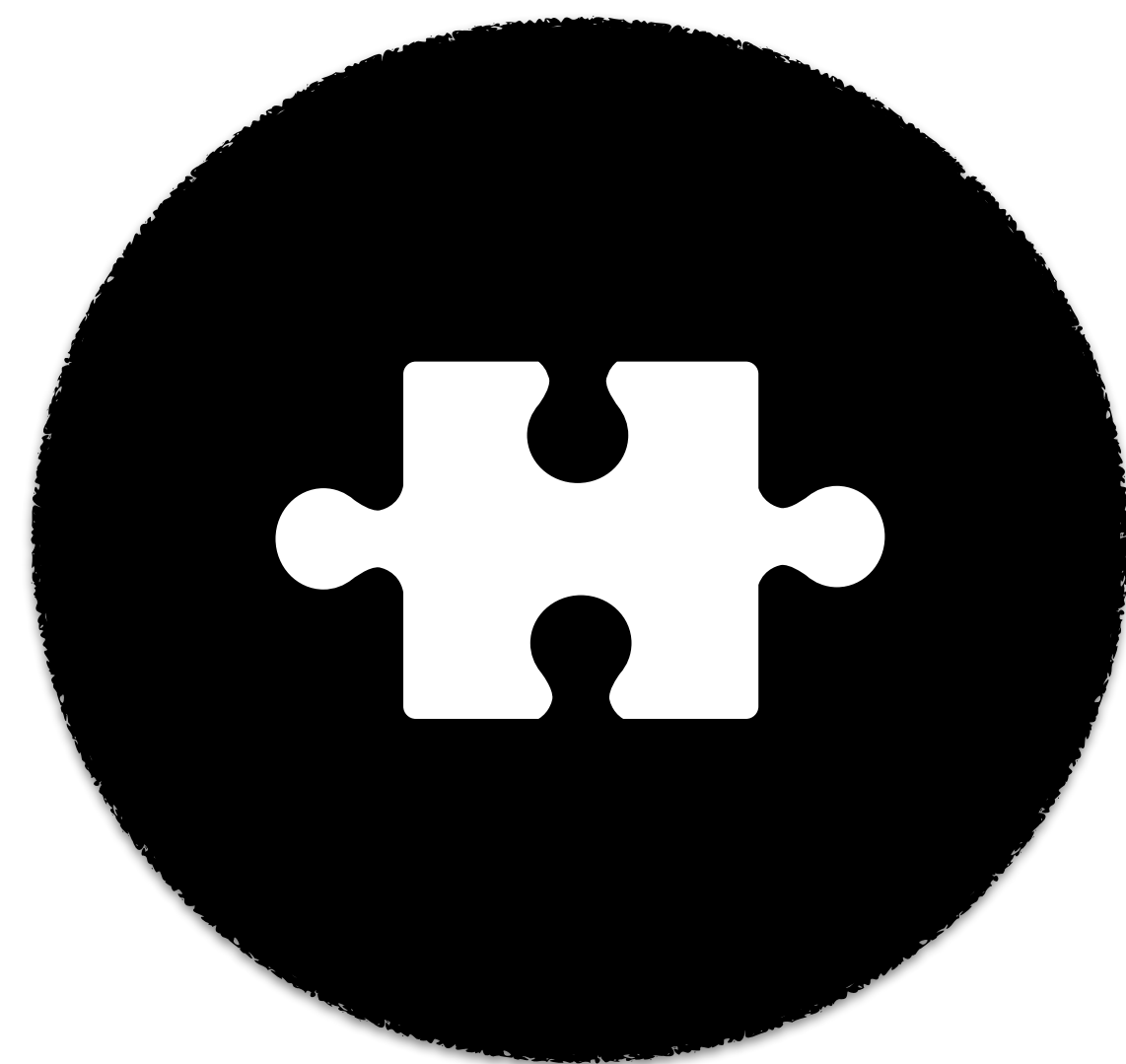
(workload: 10 Million entries, 100 queries)



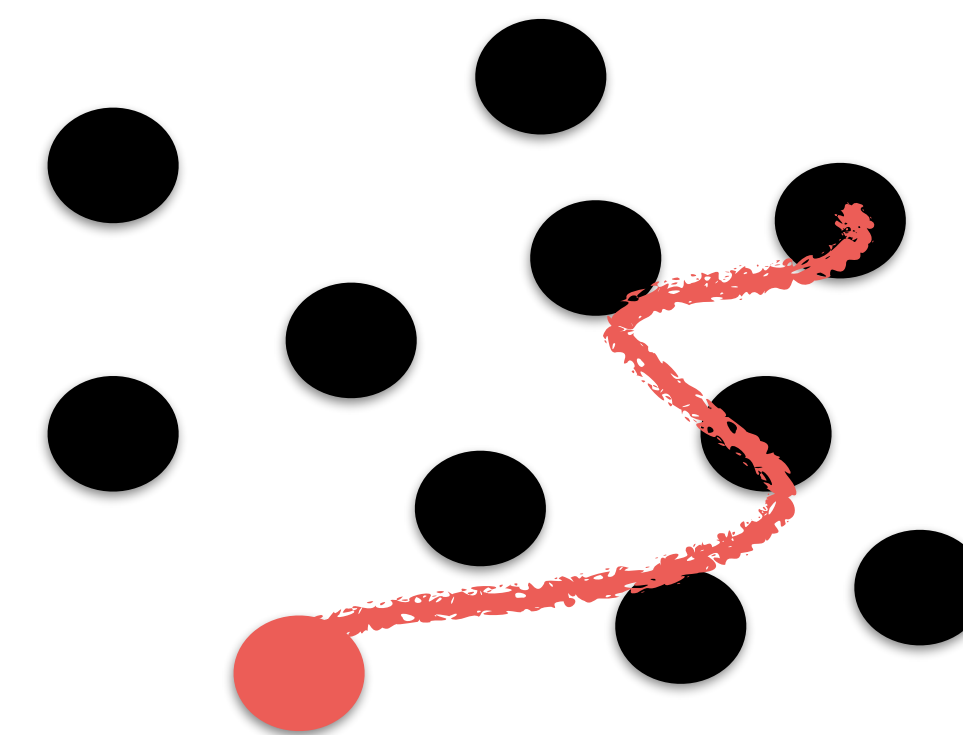
*What-if we **buy faster CPU X?*** →

**~20 SECONDS**

(workload: 10 Million entries, 100 queries)



# auto.design



dynamic programming  
genetic algorithms  
reinforcement learning

Hybrid B+Tree / Hash Table / Array

B+TREE  
ELEMENT

Point get  
intensive

Range  
intensive

Only writes

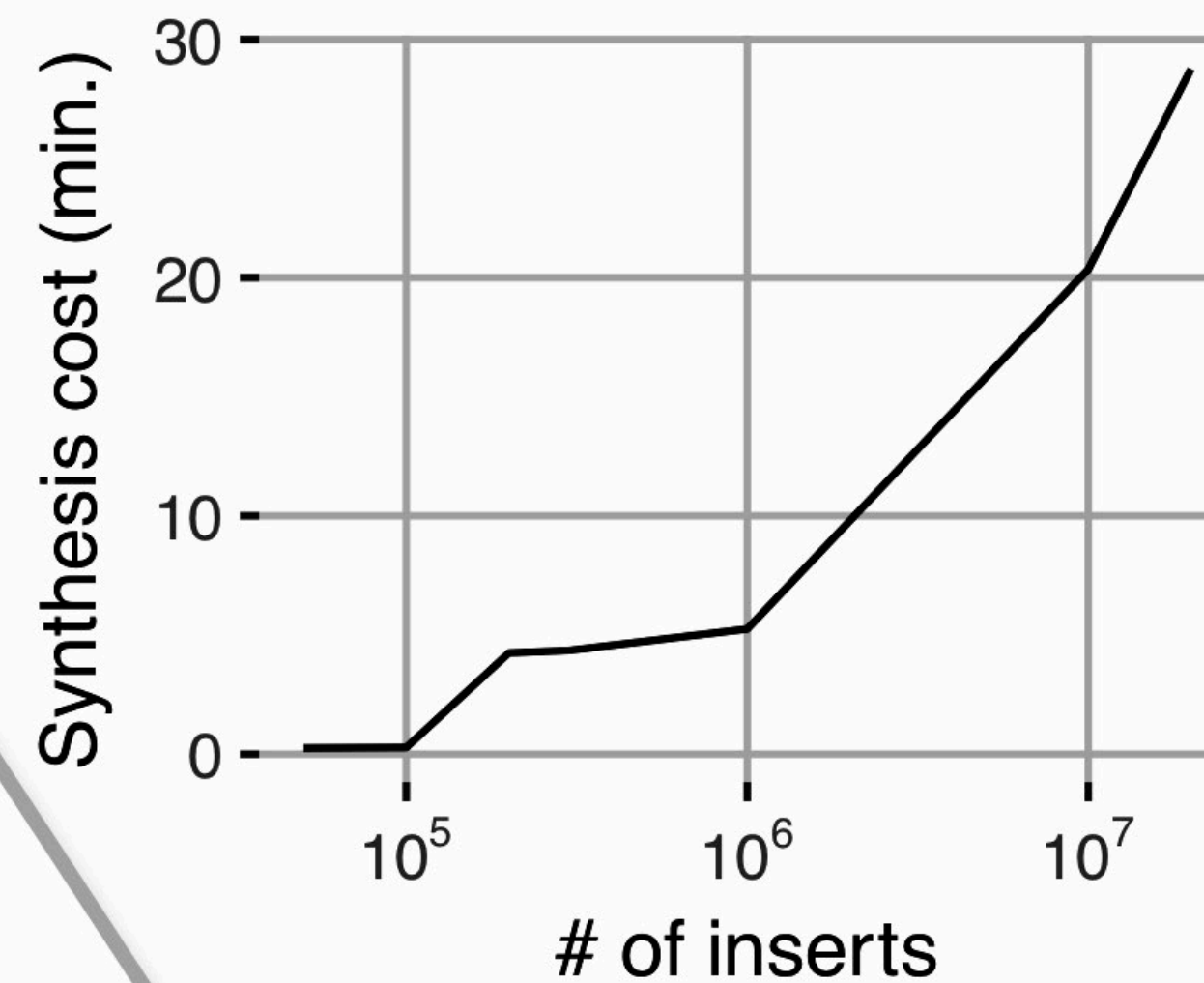
HASH  
PARTITIONING

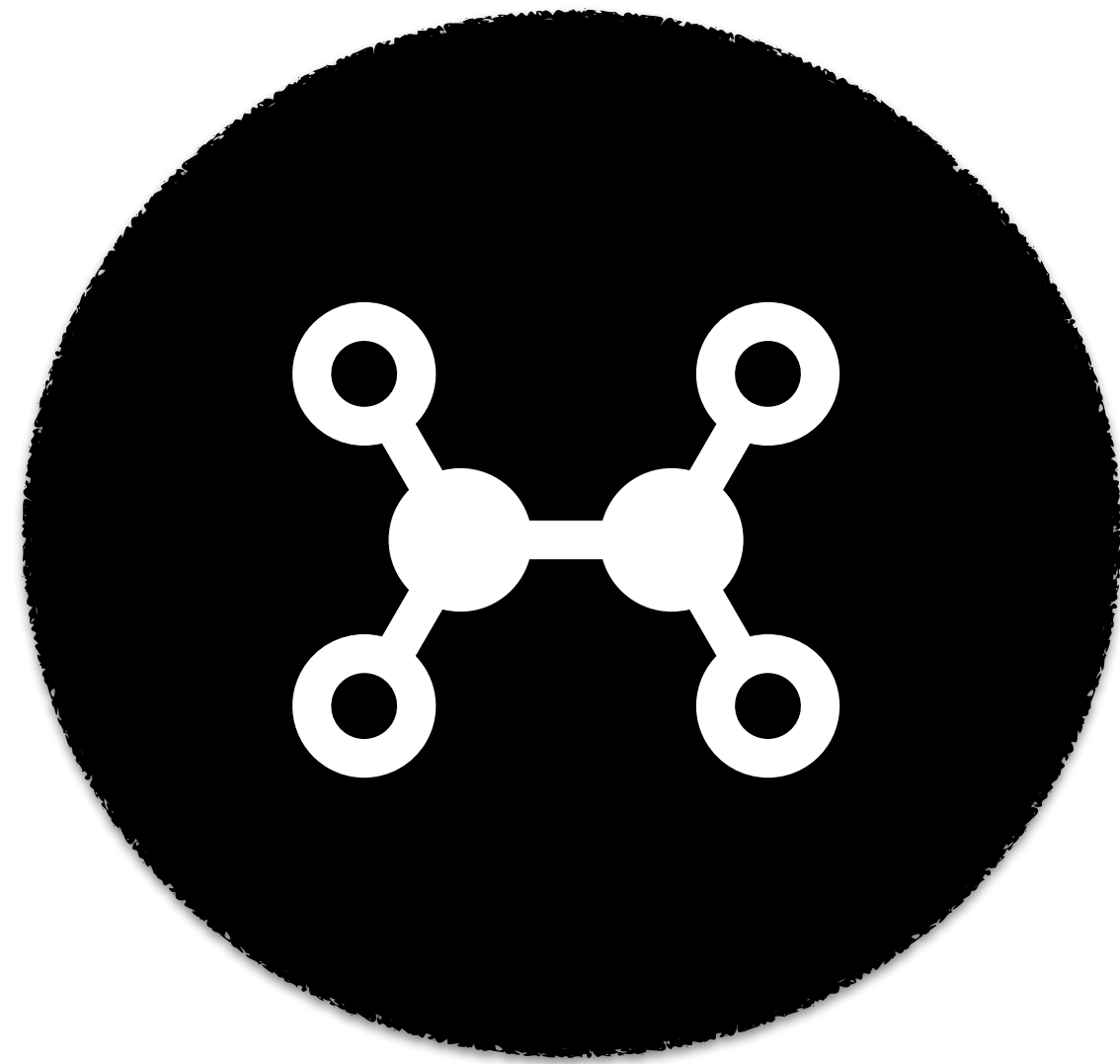
B+TREE  
ELEMENT

DATAPAGE  
(system page size)

DATAPAGE  
(large chunks)

DATAPAGE  
(system page size)



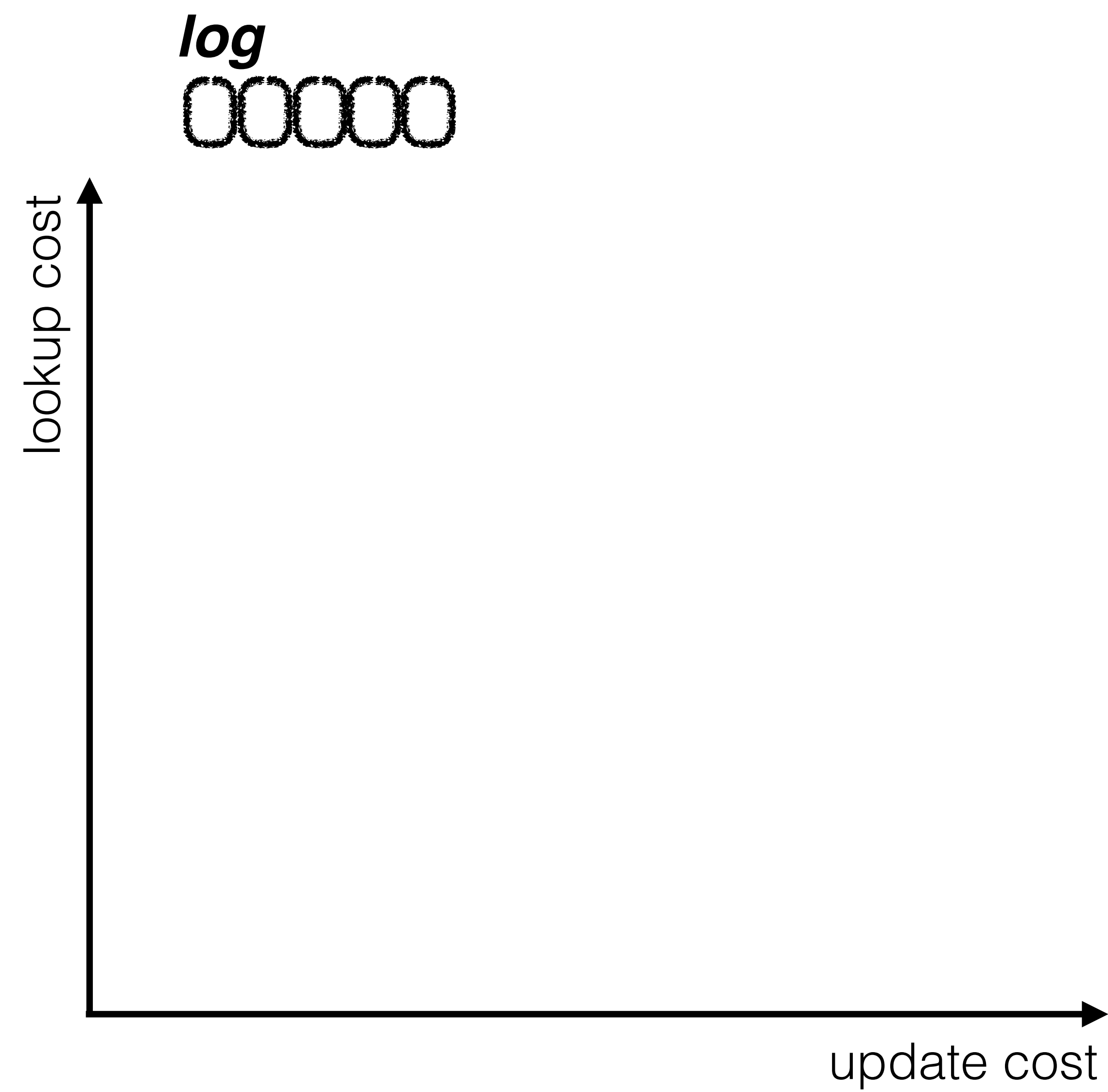


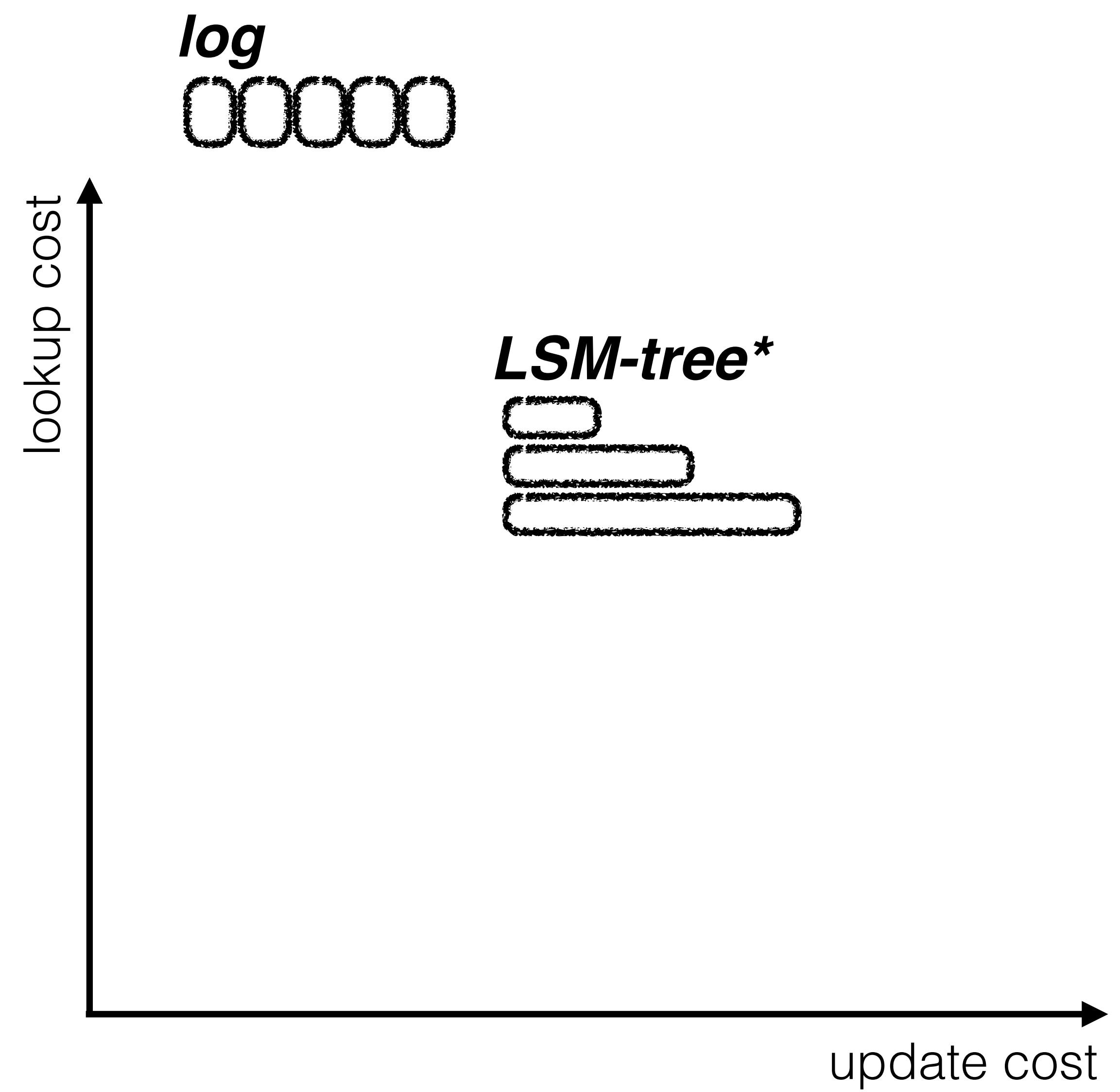
# **design.continuums**

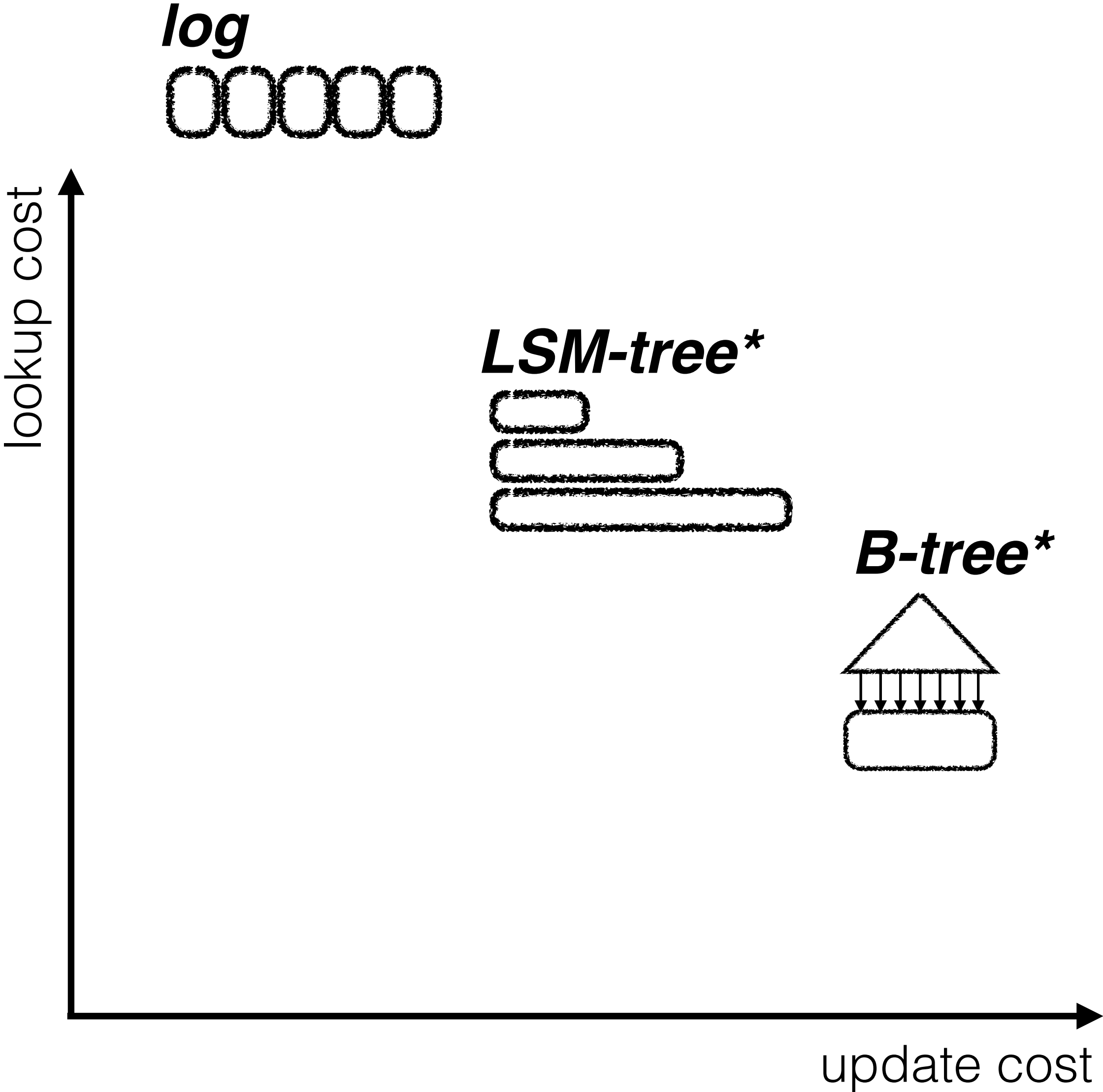
UNIFY DESIGNS FOR FAST NAVIGATION

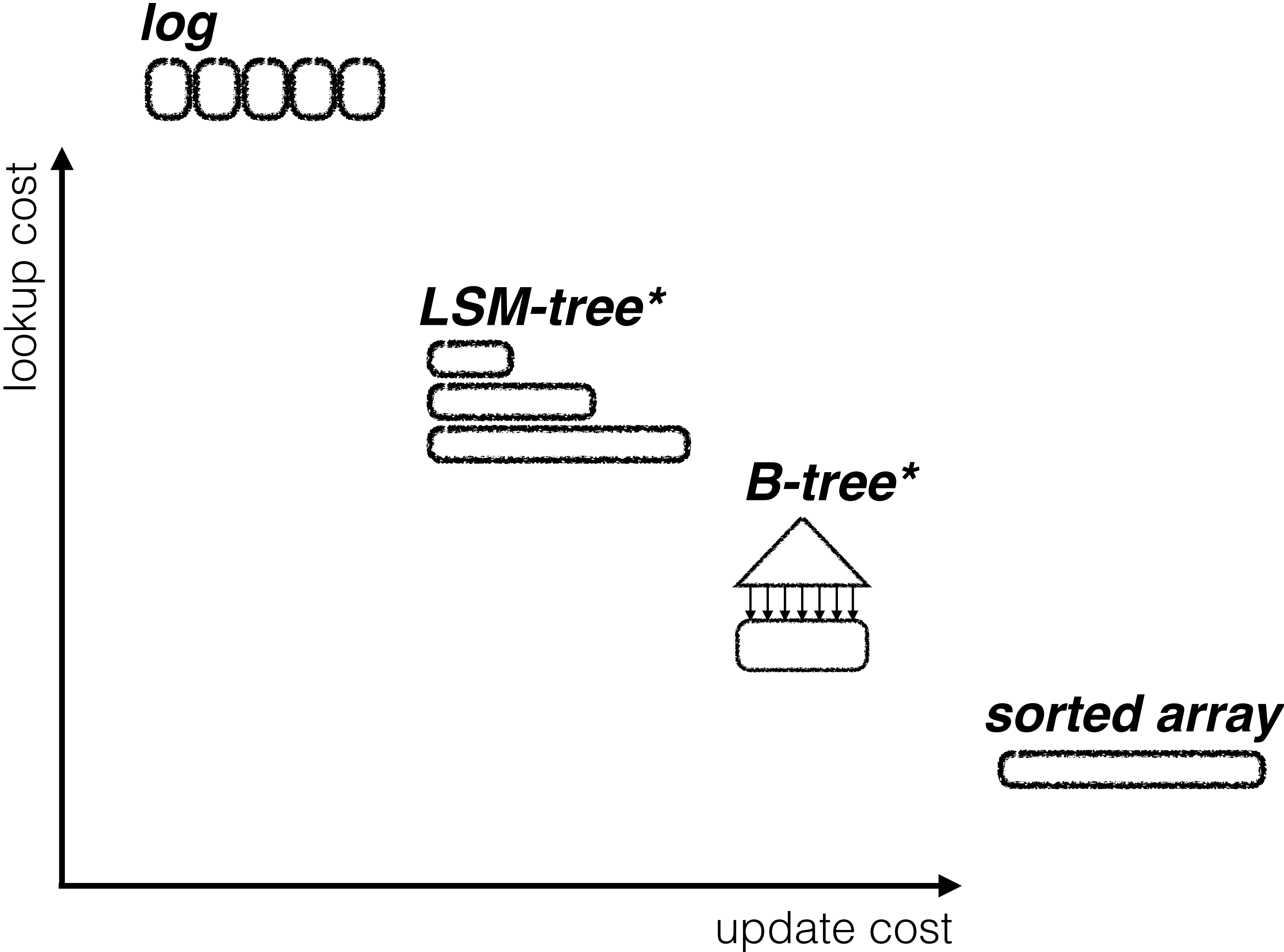




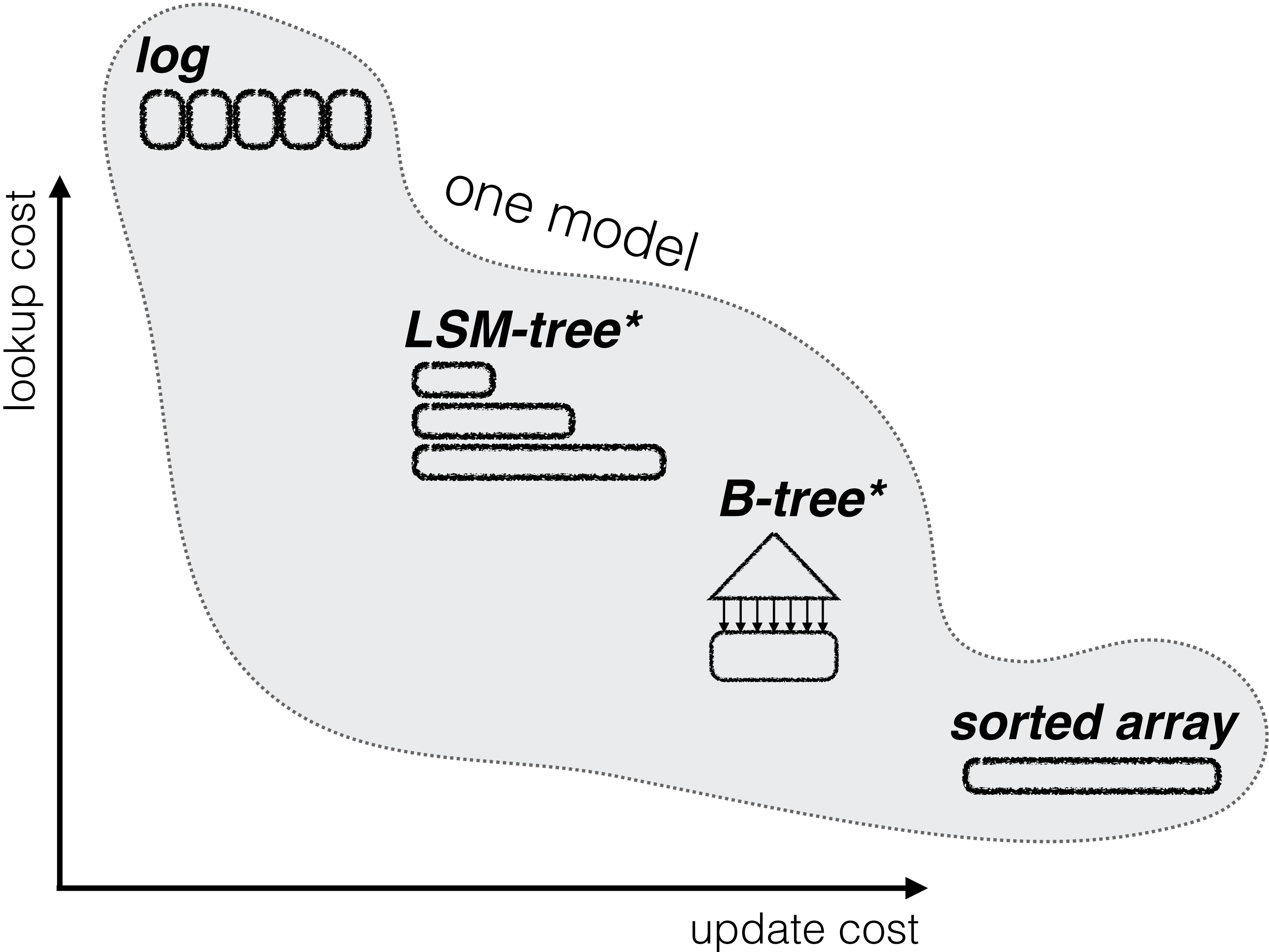


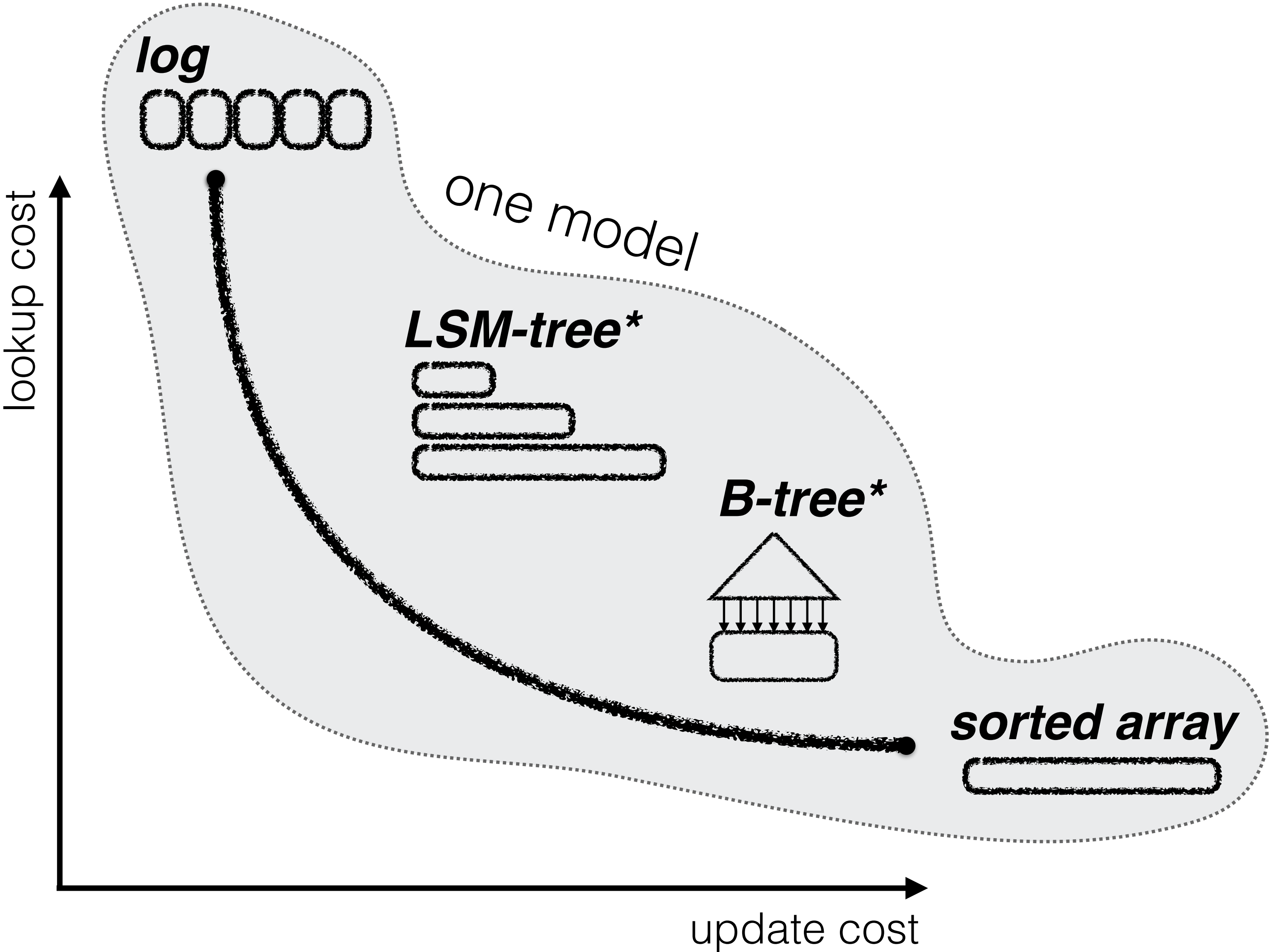


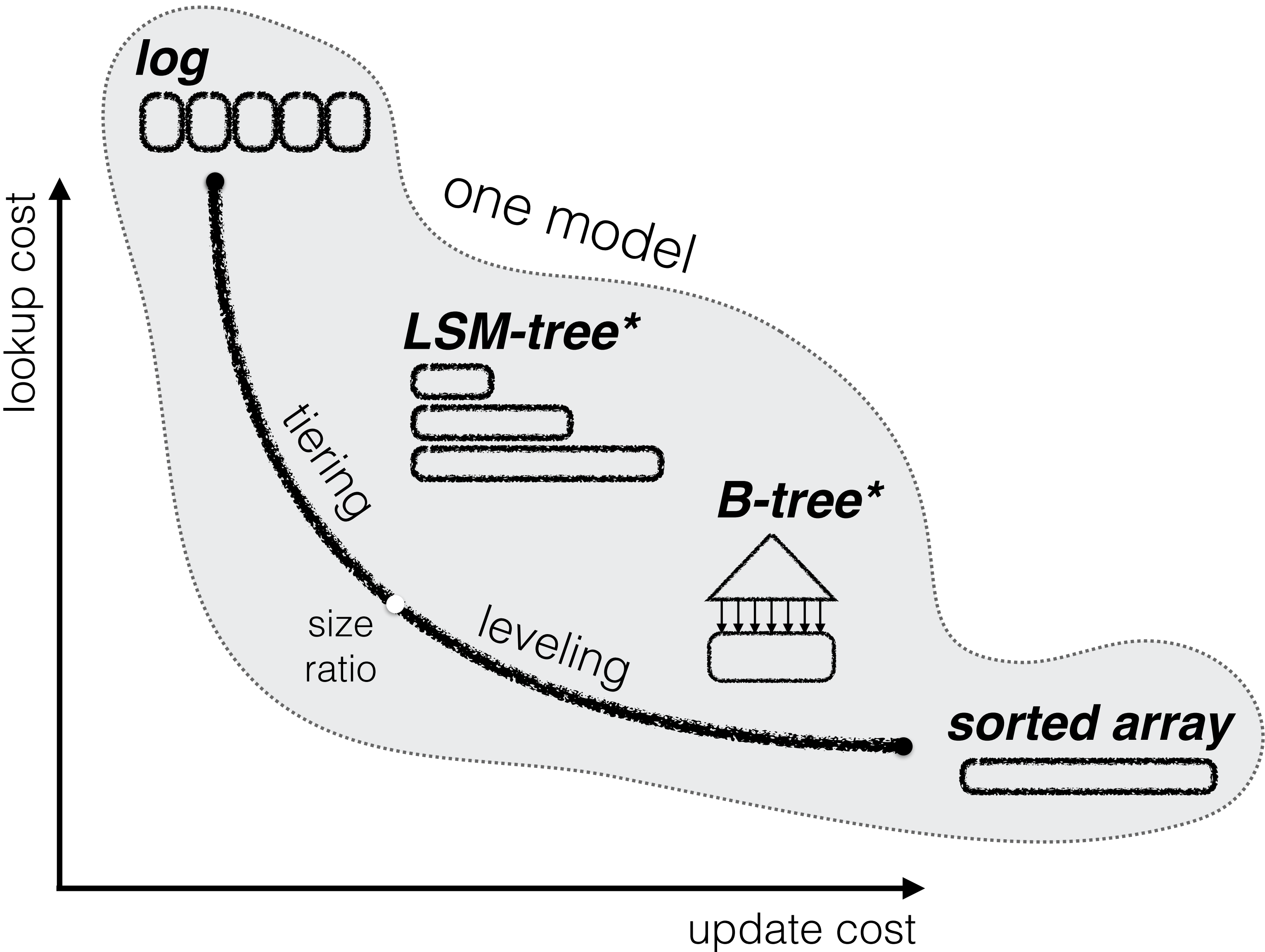




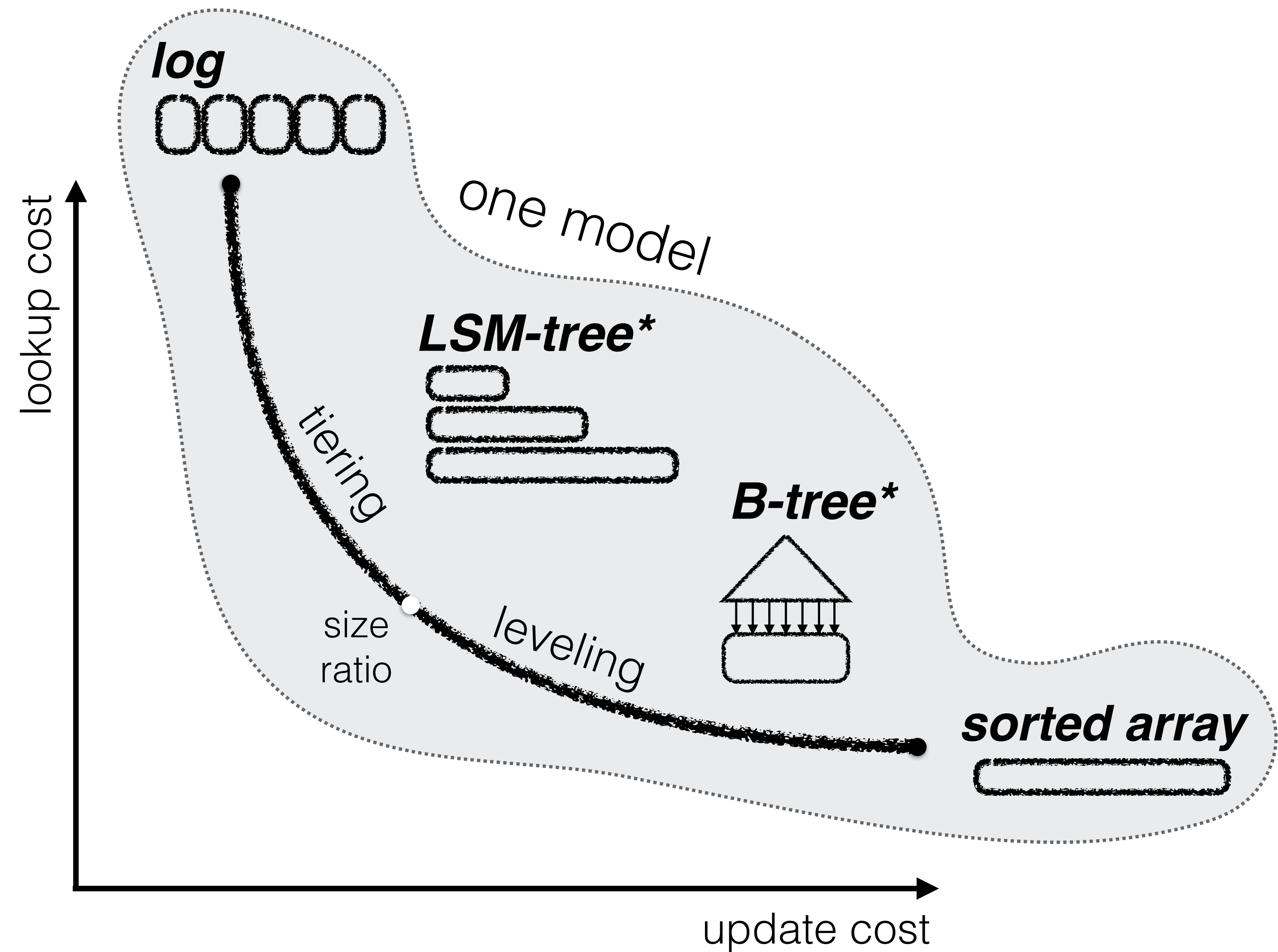


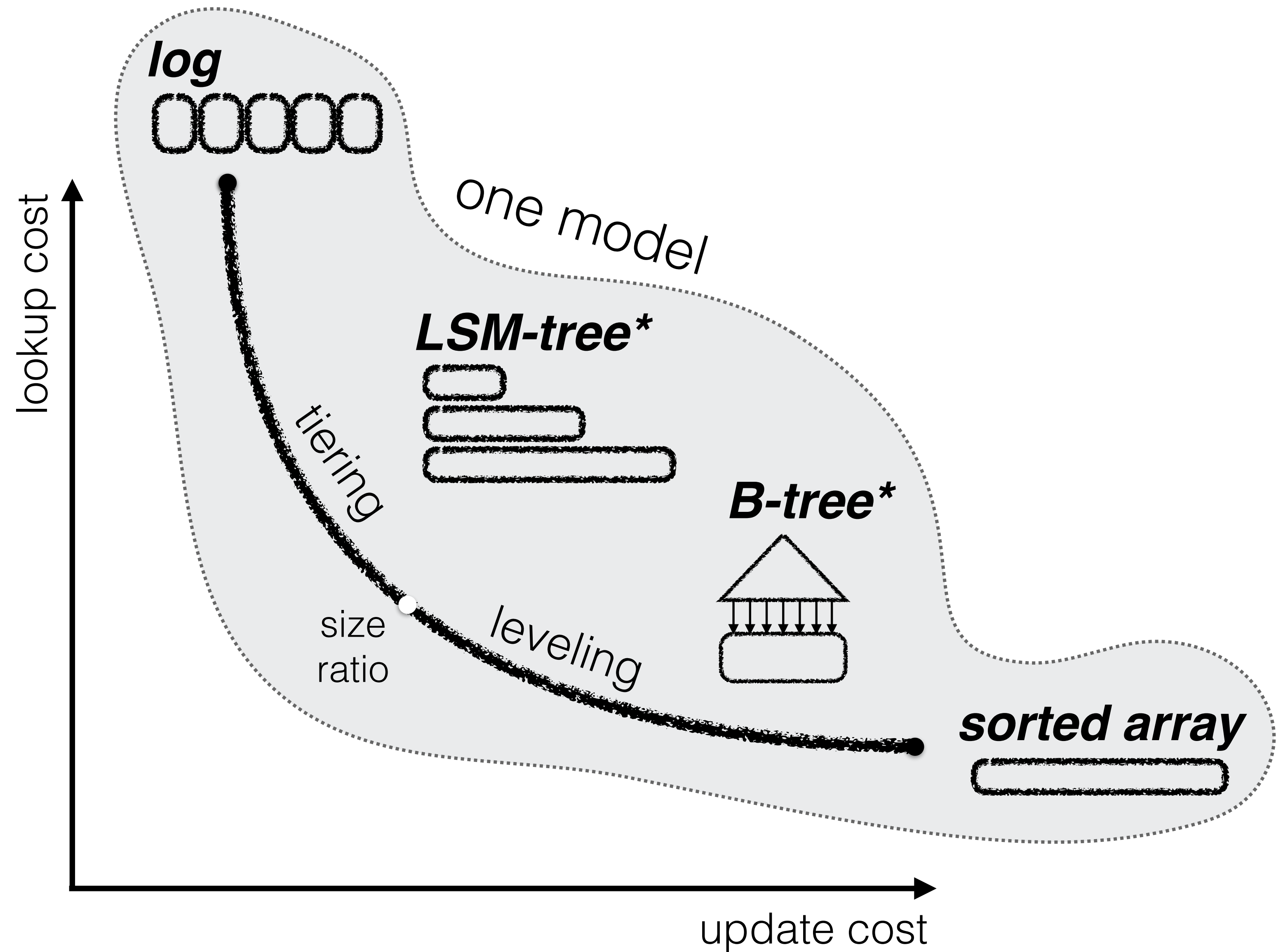






**few continuous**  
parameters  
&  
**closed form**  
formulas for metrics



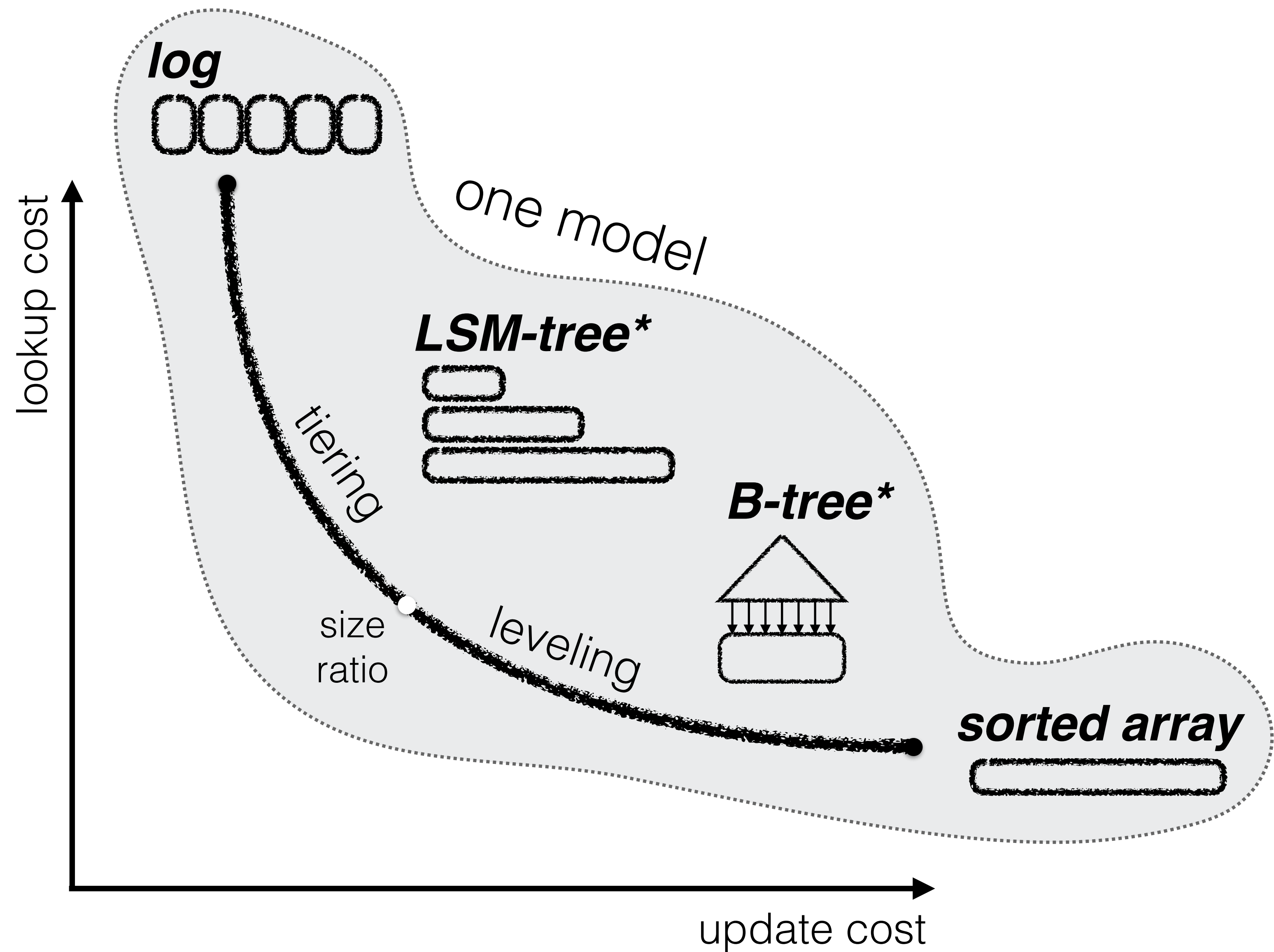


**few continuous**  
parameters  
&  
**closed form**  
formulas for metrics

↓

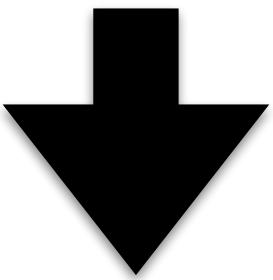
near **instant** design  
space navigation



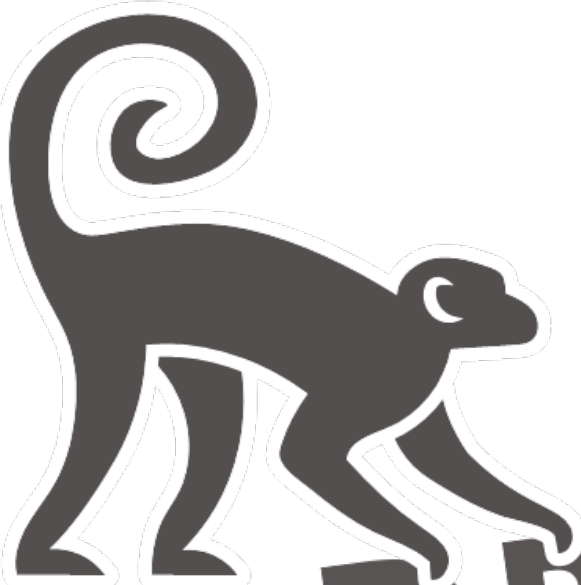
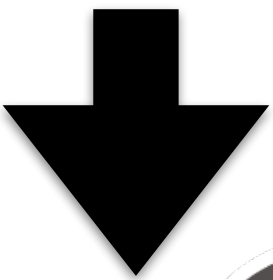


**few continuous**  
parameters  
&

**closed form**  
formulas for metrics



near **instant** design  
space navigation



**CrimsonDB**  
a self-designing key-value store



S. BING YAO  
models/advisors



DON BATORY  
modular synthesis



JOE HELLERSTEIN  
extensible indexing



STEFAN MANEGOLD  
model synthesis



## *calculator infrastructure*

DESIGN SPACE (updates, concurrency)

COST SYNTHESIS (accuracy, scalability)

EASY EXTENSIBILITY (plug & play rules)

## *study structure & gaps*

DESIGN CONTINUUMS (optimizations)

GO AFTER GAPS (new design opport.)

DESIGN GUIDE (static design rule sys)

## *building more on top of*

AUTO-SEARCH (ML/algo hybrids, hints)

SELF-DESIGNING (log to sorted arrays)

DSL & COMPILERS (productivity & perf)





**Manos  
Athanassoulis**  
*Postdoctoral  
Researcher*



**Niv Dayan**  
*Postdoctoral  
Researcher*



**Kostas  
Zoumpatianos**  
*Postdoctoral  
Researcher*



**Michael Kester**  
*Ph.D. Researcher*



**Lukas Maas**  
*Ph.D. Researcher*



**Abdul Wasay**  
*Ph.D. Researcher*



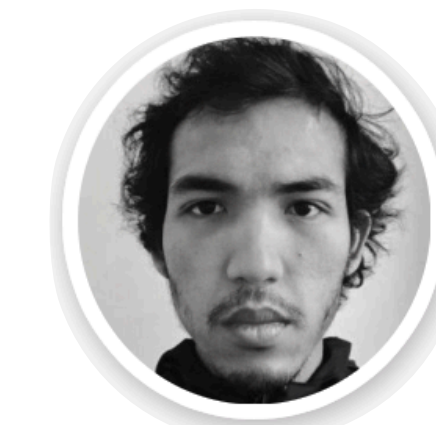
**Brian Hentschel**  
*Ph.D. Researcher*



**Wilson Qin**  
*Ph.D. Researcher*



**Angelo  
Kastroulis**  
*Graduate  
Researcher*



**Franco Solleza**  
*Graduate  
Researcher*



**Mali Akmanalp**  
*Graduate  
Researcher*



**Rachna Sha**  
*Graduate  
Researcher*



**Dhruv Gupta**  
*Undergraduate  
Researcher*



**Demi Guo**  
*Undergraduate  
Researcher*



**Yiyu Sun**  
*Undergraduate  
Research Intern*



**Mo Sun**  
*Undergraduate  
Research Intern*



**Yuze Liao**  
*Undergraduate  
Research Intern*

THANKS!

*calculator infrastructure*

DESIGN SPACE (updates, concurrency)

COST SYNTHESIS (accuracy, scalability)

EASY EXTENSIBILITY (plug & play rules)

*study structure & gaps*

DESIGN CONTINUUMS (optimizations)

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*building more on top of*

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